Broadmore 1700

USER MANUAL

Carrier Access
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PREFACE

Compliance

FCC Requirements, Part 15

This equipment has been tested and found to comply with the limits for a Class A digital device pursuant to Part 15 of the Federal Communications Rules. These limits are designed to provide reasonable protection against harmful interference when equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy, and if not installed and used in accordance with the instruction manual may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference, in which case the user will be required to correct the interference at the user's own expense.

WARNING! **TO MEET FCC PART 15 CLASS A RADIATED EMISSIONS REQUIREMENTS, YOU MUST FOLLOW SPECIFIC INSTALLATION REQUIREMENTS GIVEN FOR EACH MODULE USED IN THIS CHASSIS WHICH ARE PROVIDED IN THE MODULE INSTALLATION SECTION. FOR MORE INFORMATION ABOUT INSTALLING CABLES ACCORDING TO FCC PART 15 CLASS A, FOLLOW THE INSTRUCTIONS IN *"Cabling and Compliance Requirements" on page 6-4*.**

Common Criteria Evaluation and Validation Scheme (CCEVS) issued Validation Report number CCEVS-VR-06-0039, 26 June 2006, for EAL 3 Conformance for the Carrier Access Broadmore 500, 1700, and 1750 Release 4.1.1. This report is maintained on the NIAP Validated Products List (VPL) at http://niap.bahialab.com/cc-scheme/.

DISA Validated

Defense Information System Agency, Center for Information Assurance Engineering validated Broadmore Information Assessment, 2 August 2006, contract number SP0700-98-D-1002, TAT 5-15.

FIPS 140-2 Validated

FIPS 140-2 Inside.

National Institute of Technology (NIST) Cryptographic Module Validation Program (CMVP) validated Broadmore to FIPS 140-2, Level 1 overall, Certificate #478, November 2004. Certificate and Security Policy maintained at http://csrc.nist.gov/cryptval/.

JITC Validated

Joint Interoperability Test Certification (JITC) at Ft. Huachuca, 23 June 2006. TSSI Certificate on NIPRNET at http://jitc.fhu.disa.mil/tssi/.

IPv6 Ready

The IPv6 Forum certified the Broadmore as IPv6 ready on 26 January, 2007. The certificate is maintained at http://www.ipv6ready.org/logo_db/logo_search2.php?logoid_number=01-000379&btm=Search

National Electrical Code Requirements

Equipment intended to be electrically connected to a telecommunications network shall be listed for the purpose. The Broadmore 1700 is listed and is in compliance with UL60950 third edition, and CSA-C22.2 NO. 60950-00.

No. 950-95 Standard for Safety for Information Technology Equipment. CSA has certificated to both standards for product safety. The CSA File Number is LR 107313.

Some telecommunications equipment does not provide overvoltage or power-cross protection on DS1 lines. Equipment that does not provide overvoltage or power-cross protection is not compliant with the National Electrical Code for customer premises installation. The Broadmore 1700 provides this protection.

UL60950/CSA-C22.2 NO. 60950-00 compliance is an important requirement for carriers installing equipment within customer buildings and is designed to prevent the product and the telephone wiring from starting building fires.

Safety Information

CAUTION! **ALWAYS USE CAUTION WHEN INSTALLING TELEPHONE LINES. READ THE CAUTIONS BELOW FOR DETAILS ON SAFETY GUIDELINES TO PREVENT INJURY.**

- **Never touch uninsulated telephone wires and terminals** unless the telephone line has been disconnected at the Network Interface (NI) as voltage potentials as high as 300 VAC may be present across the transmit and receive pairs.
- **Only use No. 26 AWG or larger** telecommunication line cord, to reduce the risk of fire.
- Never install telephone wiring during a lightning storm.
- **Never install telephone jacks in wet locations** unless the jack is specifically designed for wet locations.
- Refer to the installation section of this manual for a safe and proper installation procedure. All wiring external to this equipment should follow the current provision of the National Electrical Code.

Notices

This manual contains important information and warnings that must be followed to ensure safe operation of the equipment.

DANGER! **A *DANGER* NOTICE INDICATES THE PRESENCE OF A HAZARD THAT CAN OR WILL CAUSE DEATH OR SEVERE PERSONAL INJURY IF THE HAZARD IS NOT AVOIDED.**

CAUTION! **A *CAUTION* NOTICE INDICATES THE POSSIBILITY OF INTERRUPTING NETWORK SERVICE IF THE HAZARD IS NOT AVOIDED.**

WARNING! **A *WARNING* NOTICE INDICATES THE POSSIBILITY OF EQUIPMENT DAMAGE IF THE HAZARD IS NOT AVOIDED.**

NOTE: **A *NOTE* INDICATES INFORMATION TO HELP YOU UNDERSTAND HOW TO PERFORM A PROCEDURE OR HOW THE SYSTEM WORKS. NOTES SHOULD BE READ BEFORE PERFORMING THE REQUIRED ACTION.**

Electrostatic Discharge (ESD) Precautions

WARNING! **THE BROADMORE CONTAINS CIRCUIT CARDS AND COMPONENTS THAT ARE SUBJECT TO DAMAGE BY ELECTROSTATIC DISCHARGE.**

ESD can damage processors, circuit cards, and other electronic components. Always observe the following precautions before installing a system component.

1. Do not remove a component from its protective packaging until ready to install it.
2. Wear a wrist grounding strap and attach it to an ESD connector or a metal part of the system unit before handling components. If a wrist strap is not available, maintain contact with the system unit throughout any procedure requiring ESD protection.

An ESD warning label appears on packages and storage bags that contain static-sensitive products and components.

Warranty

Carrier Access warrants to BUYER that Product Hardware will be free from substantial defect in material and workmanship under normal use in accordance with its Documentation and given proper installation and maintenance for period of one year from the date of shipment by Carrier Access.

Carrier Access warrants that the Licensed Software, when used as permitted under its License Terms and in accordance with the instructions and configurations described in the Documentation (including use on Carrier Access product or a computer hardware and operating system platform supported by Carrier Access), will operate substantially as described in the Documentation for a period of ninety (90) days after date of shipment of the Licensed Software to BUYER.

This warranty shall not apply to Products or Software that have been either resold or transferred from BUYER to any other party. Any such transfer voids the above warranty and related licenses. Carrier Access offers expanded product care beyond what is covered by the warranty through different support plans. The plans are designed to maximize network availability through advance replacement for defective equipment. Please contact your Carrier Access representative for support program details.

Warranty Procedure

BUYER must promptly notify Carrier Access of any defect in the Product or Software and comply with Carrier Access' return/repair policy and procedures. Carrier Access or its agent will have the right to inspect the Product or workmanship on BUYER's premises. With respect to a warranty defect in Product hardware reported to Carrier Access by BUYER during the warranty period, Carrier Access, as its sole obligation and BUYER's exclusive remedy for any breach of warranty, will use commercially reasonable efforts, at its option, to:

a. repair, replace, or service at its factory or on the BUYER's premises the Product, or component therein, or workmanship found to be defective so that the Product hardware operates substantially in accordance with Carrier Access Documentation; or

b. credit BUYER for the Product in accordance with Carrier Access's depreciation policycredit BUYER for the Product in accordance with Carrier Access's depreciation policy.

With respect to a warranty defect in the Licensed Software reported to Carrier Access by BUYER during the 90-day software warranty period, Carrier Access, at its own expense and as its sole obligation and BUYER's exclusive remedy for any breach of the software warranty, will use commercially reasonable efforts to, at its option,

a. correct any reproducible error in the Licensed Software, or

b. replace the defective Licensed Software, as follows: Should a Severity 1 or 2 warranty defect with the Software occur during the 90-day warranty period, Carrier Access will provide, in its sole determination, either

1. software to resolve the defect to be downloaded into the affected units by the BUYER or
2. a documented workaround to address the issue.

Severity 1 issues are failures of the Licensed Software to comply with the Carrier Access software specifications and that completely or severely affect the Carrier Access Product and its traffic or service capacity, or maintenance or monitoring capabilities.

Severity 2 issues are failures of the Licensed Software to comply with the Carrier Access software specifications and that result in a major degradation of the Carrier Access Product so as to impact its system or service performance, or significant impairments to network operator control or effectiveness. Should a Severity 3 warranty defect with the Licensed Software occur during the 90-day warranty period, Carrier Access will provide assistance to Buyer to determine if a solution or workaround will be provided in a subsequent software release following the reported issue.

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Determination of Severity 1, 2 or 3 shall be made solely by Carrier Access following receipt of the reported problem. Refurbished material may be used to repair or replace the Product. BUYER shall bear the risk of loss for Products or Software returned to Carrier Access for repair, replacement, or service, and the same must be shipped pre-paid by BUYER.

Requests for warranty services and troubleshooting must be made to, and will be provided by, the Carrier Access Customer Support Center via telephone during the warranty period and during normal business hours. Normal business hours for Carrier Access Customer Support Center are 7:00 a.m. to 6:00 p.m. Mountain Standard Time, Monday through Friday, excluding weekends and standard Carrier Access recognized holidays.

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b. the negligent, unlawful or other improper use or storage of the Product or Software, including its use with incompatible equipment or software; or

c. fire, explosion, power failures, acts of God, or any other cause beyond Carrier Access' reasonable control; or

d. handling or transportation after title of the Product passes to BUYER.

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The foregoing summarizes Carrier Access' entire product and software warranties, which are subject to change without notice.

Warranty Product Returns

Before returning any equipment to Carrier Access Corporation, first contact the distributor or dealer from which you purchased the product.

A Return Material Authorization (RMA) number is required for all equipment returned to Carrier Access Corporation. Call Carrier Access Corporation Customer Support at (800) 786-9929 or (303) 442-5455 for RMA number, repair/warranty information and shipping instructions. Be prepared to provide the following information:

- Carrier Access Corporation serial number(s) from the system chassis or circuit card(s)
- Name of distributor or dealer from which you purchased the product
- Description of defect

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CHAPTER 1

Product Description

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Purpose

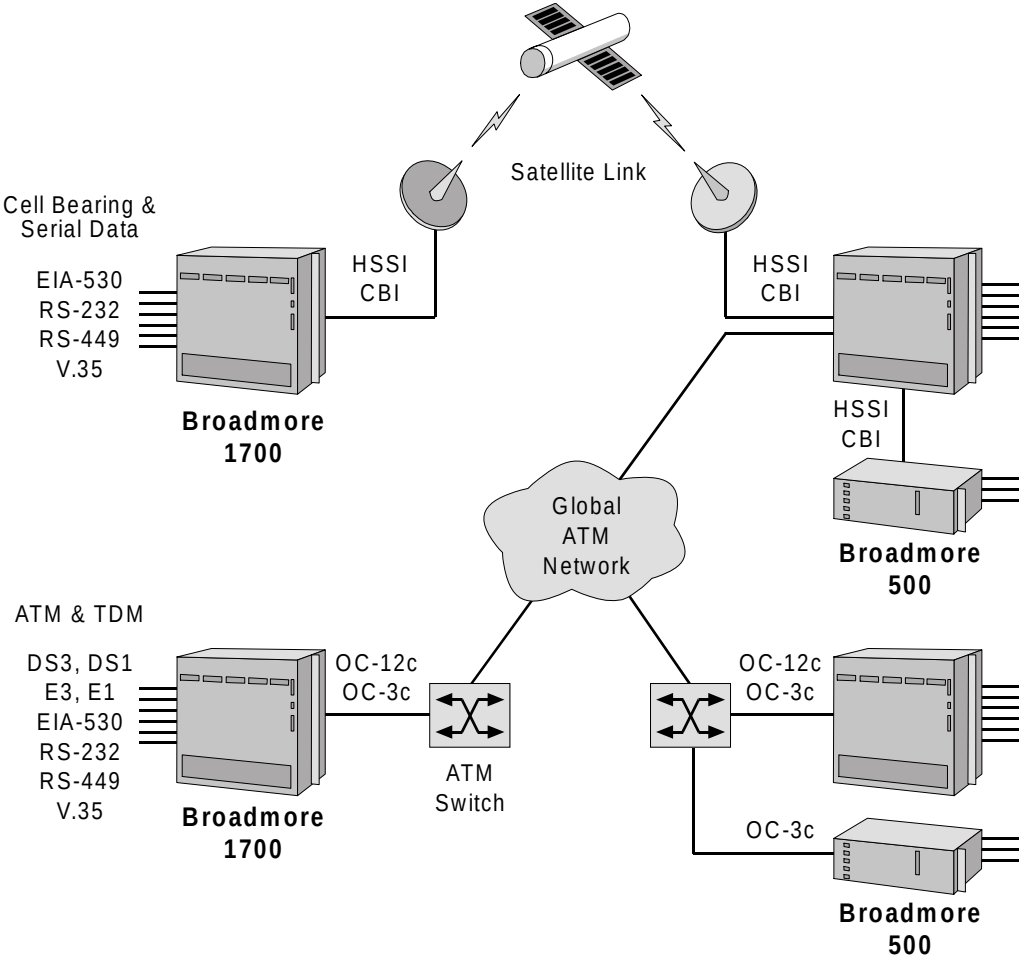
The Broadmore 1700 is an Asynchronous Transfer Mode (ATM) service multiplexer that enables connection of existing and future services through an ATM network. The Broadmore allows users to implement tailored ATM strategies. This modular system has flexible configurations for service access, network interface, and redundancy.

The Broadmore functions as an ATM network service access node that supports the transport of existing broadband services (voice, video, and data) over ATM networks. Typically, it is deployed at the edge of an ATM network as the ATM node element closest to the customer. The Broadmore accepts signals from non-ATM-ready equipment, converts the signals to standard ATM cells, and multiplexes the cells onto a single ATM User Network Interface (UNI) port. The Broadmore accepts constant-bit-rate (CBR) services. Permanent Virtual Circuit (PVC) and Switched Virtual Circuit (SVC) services are available based upon the user-defined module configuration.

The Broadmore now includes FIPS 140-2 validated system management software that meets the security requirements of Federal Information Processing Standard PUB 140-2.

Applications

The Broadmore 1700 is currently deployed in Government and commercial ATM and satellite networks. Cell bearing (HSSI and CBI) interfaces allow efficient transfer of serial data over satellite links while OC-12c and OC-3c network interfaces transport a variety of multiplexed ATM and TDM services including structured and unstructured DS3, DS1, E3, and E1, as well as cell bearing and serial data.



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ATM Interfaces

The Broadmore 1700 supports the following ATM Network Interface Modules (NIMs):

- OC-12c/STM-4c (622 Mbps), singlemode, intermediate reach (IR), SC connectors
- OC-12c/STM-4c (622 Mbps), multimode, premise reach (PR), SC connectors
- OC-3c/STM-1c (155 Mbps) singlemode, ST/SC connectors
- OC-3c/STM-1c multimode, SC connectors
- Cell Bearing Interface (CBI)
- High Speed Cell Bearing Interface (HSSI-CBI)
- ATM DS3

User Equipment Interfaces

The Broadmore 1700 supports the following network and Service Access Modules (SAMs) to support user data services:

- Unstructured DS3
- Structured DS3
- DS1 N×64
- E1 N×64
- Unstructured E3-3
- Multi-Bit Rate (MBR)
- High Bit Rate (HBR)
- High Speed (HSAM)
- Synchronous Cell Bearing Interface (CBI)
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- Serial Multiplexer (Mux)
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Security

The Broadmore is controlled via system management software embedded in the CPU's flash memory. This software defines the system command structure and provides a user interface for operation and administration. The Broadmore supports multiple user accounts and access levels. There are four levels of user access. Only a network administrator or crypto officer with "SuperUser" access can assign user names, passwords, and access levels.

FIPS Interface

The Broadmore now includes the Broadmore/SSHield Management Module, which is a FIPS 140-2 validated software-only module that meets the security requirements of Federal Information Processing Standard PUB 140-2. The Broadmore can operate in either FIPS mode or non-FIPS mode, depending on the desired level of security. (For a description of the FIPS mode features, see *"Security Management (FIPS Mode)" on page 11-1*.)

NOTE: Enabling FIPS mode security will disable FTP and Telnet access. Users must then log in using secure client replacements such as SecureCRT® and SecureFX®. A secure terminal emulator is required to enter a secure Broadmore system. Although many secure terminal emulators are available, SecureCRT is recommended.

Physical and Logical Interfaces

The CPU is the entry point for both local and remote network management of the Broadmore. The management interface can be reached either in-band or out-of-band via Telnet or SecureCRT. The CPU module provides a serial port (DB9) for local console access and the CPU IOM provides a 10Base-TX Ethernet port for LAN access. Both interfaces provide full support for out-of-band access to all of the Broadmore management interfaces, depending on the user's assigned security level.

The embedded software operating system can be accessed via:

- Craft terminal or PC using the RS-232 serial or modem interface
- IP over ATM protocols: RFC-1577 Classical IP (CLIP) and LAN Emulation Client (LANE) with the Broadmore acting as the LEC
- Telnet or SecureCRT via Ethernet or IP over ATM

User Interfaces

There are two principal user interfaces to the Broadmore system management software:

- Communication Access Multiplexer Management Interface (CAMMI), a quasi-graphical user interface accessible from a serial terminal and by Telnet or SecureCRT over IP.
- Command line interface (CLI) accessible from a serial terminal and by Telnet or SecureCRT over IP.

File Access and Software Upgrades

Data stored on the CPU's flash memory is protected. In FIPS mode, only a SuperUser can access data files or upgrade the system software. In non-FIPS mode, a SuperUser or SysAdmin can transfer files.

System Description

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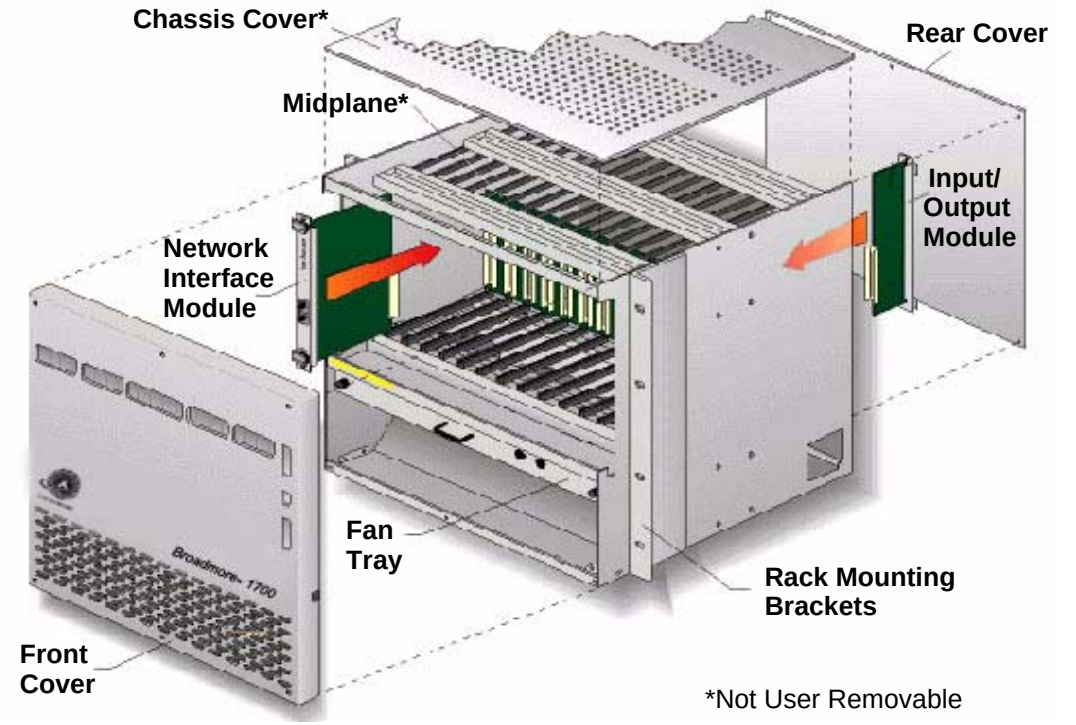
Features

The Broadmore 1700 consists of a chassis and various plug-in modules. This fully integrated system provides the following features:

- Redundant power sources, CPUs, and NIMs.
- Front-loadable, hot-swappable modules that allow for single part replacement without affecting other portions of the system.
- Cable separation with user equipment copper/coax interfaces on the chassis rear, and fiber optic ATM interface cables on the chassis front.

Chassis

The Broadmore 1700 chassis can be configured to perform various functions with plug-in modules. The chassis includes a passive midplane, 17 slots for modules, and a fan assembly. Attached mounting brackets are configured for standard EIA 19-inch rack installation. These brackets may be rotated 90 degrees for 23-inch relay rack installation. The chassis has removable front and rear covers, which should remain in place during normal operations. These covers should only be removed to provide internal access for installation, maintenance, or system upgrade.



Fan Tray

A removable fan tray, with two fans, is located below the card slots. These fans provide vertical airflow to aid in heat dissipation. The fan tray can be removed for maintenance or replacement without powering down the Broadmore. Empty module slots must be covered by blank panels to maintain proper air flow.

Power and Redundancy

There are two power input connectors, labeled A and B, for receiving –48 VDC at the rear of the chassis. Normally, power is provided directly from the installation facility's –48 VDC source to the connectors at the rear of the chassis. The Broadmore will operate with one power input but connecting an independent power source to the second input will provide redundancy.

A redundant AC power supply tray (P/N 7660-17PS) is available as a separate unit designed to be rack-mounted directly below the Broadmore 1700 chassis. The AC Power Supply supports two non-load-sharing, hot-swappable power supply modules. Each module is capable of supporting a Broadmore 1700 with a fully loaded complement of interface cards. It is recommended that each power supply have its own separate fused outlet for true power redundancy. There is no impact to the Broadmore 1700 as long as one (or both) of the modules is operating normally.

Grounding

Ground lugs at the rear of the chassis allow connection to the building ground system.

A separate ESD ground connection is provided for use with a ground strap when performing maintenance.

Alarms

A four-wire terminal block at the rear of the chassis provides "form C" relay contact closure connections for major and minor alarm signals.

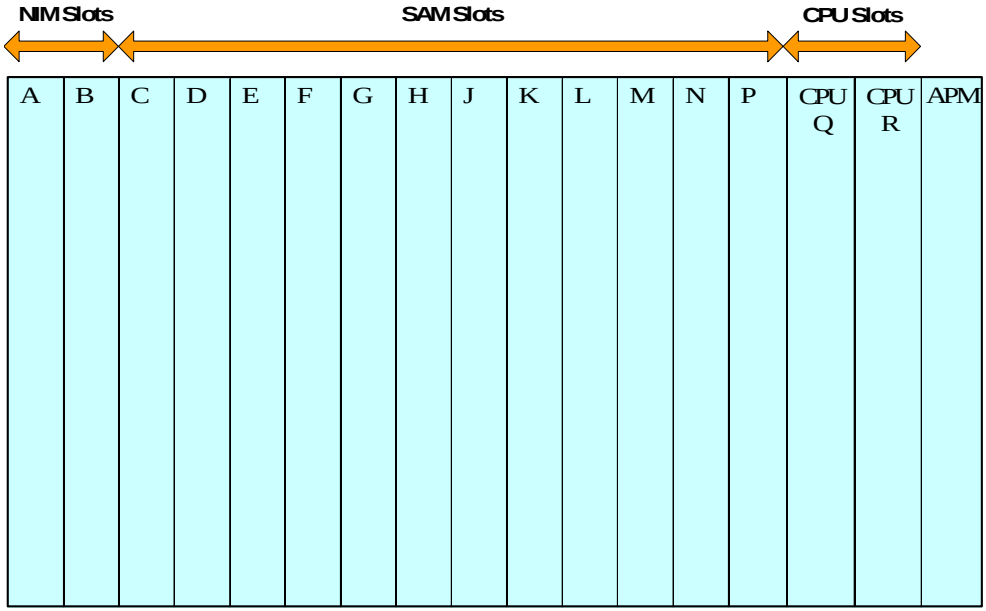
Modules

The Broadmore 1700 system architecture is based upon a midplane design allowing modules to be installed from the front and rear of the chassis. There are 17 vertical slots as viewed from the front with the cover removed (see figure below).

The Broadmore 1700 is shipped in a minimum usable configuration with a factory installed Alarm/Power Module in the right-most slot. Unused slots are covered with blank panels except the slots for a single NIM, SAM, and CPU module.

The Broadmore 1700 chassis can be configured with the following modules.

- Network Interface Module (NIM) – 1 or 2 (must be identical)
- Service Access Module (SAM) – 1 or more
- Central Processing Unit (CPU) – 1 or 2 (must be identical)
- Alarm/Power Module (APM) – 1 (factory installed)
- Input/Output Module (IOM) – 1 for each NIM, SAM, and CPU (installed in rear panel immediately behind corresponding module in front panel)



Module Descriptions

Network Interface Modules

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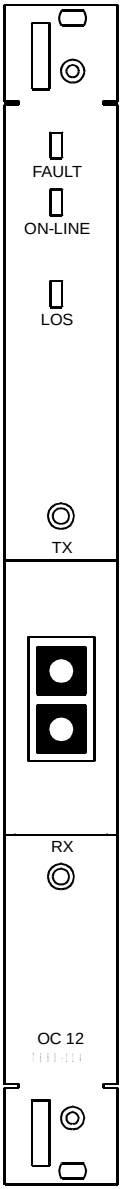
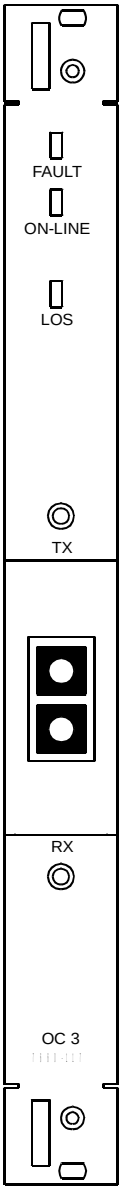
OC-12c/STM-4c NIM

Module Type	NIM
Part Number	7660-113 (Premise Reach, Multi Mode fiber) 7660-114 (Intermediate Reach, Single Mode fiber)
Slot Number	A (Protection), B (Working)
Features	OC-12 (622.080 Mbps) SC fiber optic connectors
Description	The OC-12c/STM-4c NIM is available with either Intermediate Reach or Premise Reach fiber optic terminations. It is compatible with any combination of SAMs installed in the Broadmore 1700 chassis, up to the available bandwidth. Interface from the SAMs to the NIM is accomplished via the chassis midplane. An OC-12c/STM-4c, single mode, Intermediate Reach (IR) module supports ATM physical (PHY) data rates up to OC-12 (622.080 Mbps).
Indicators	FAULT – normal (no fault), major alarm, or minor alarm ON-LINE – normal, standby, or not ready LOS – good RX power, or loss of signal
Connectors	TX and RX fiber interfaces to the ATM network are made via the SC connectors on the NIM, accessed from the front of the chassis.



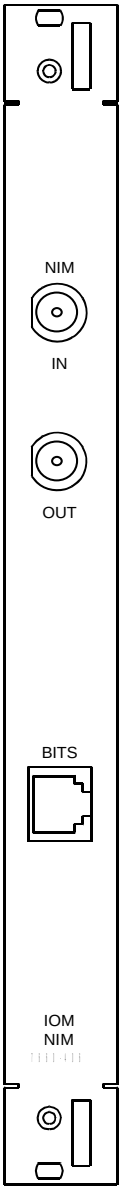
OC-3c/STM-1c NIM

Module Type	NIM
Part Number	7660-017 (Premise Reach, Multi Mode fiber) 7660-016 (Intermediate Reach, Single Mode fiber)
Slot Number	A (Protection), B (Working)
Features	OC-3 (155.520 Mbps) SC fiber optic connectors
Description	The OC-3c/STM-1c NIM is available with either Intermediate Reach or Premise Reach fiber optic terminations. It is compatible with any combination of SAMs installed in the Broadmore 1700 chassis, up to the available bandwidth. Interface from the SAMs to the NIM is accomplished via the chassis midplane. An OC-3c/STM-1c, single mode, Intermediate Reach (IR) module supports ATM physical (PHY) data rates up to OC-3 (155.520 Mbps).
Indicators	FAULT – normal (no fault), major alarm, or minor alarm ON-LINE – normal, standby, or not ready LOS – good RX power, or loss of signal
Connectors	TX and RX fiber interfaces to the ATM network are made via the SC connectors on the NIM, accessed from the front of the chassis.

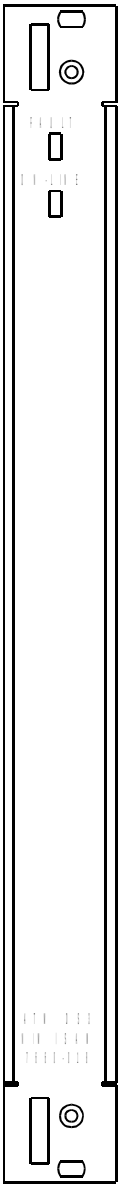


NIM IOM

Module Type	NIM IOM
Part Number	7660-406
Slot Number	Mounts in rear behind corresponding OC-3c/STM-1c NIM or OC-12c/STM-4c NIM
Features	BITS clock input Jumper selectable BITS clock termination impedance
Description	The NIM IOM provides a physical interface to an external clock, giving the option of synchronizing the Broadmore 1700 master clock to an external source. BITS timing is provided to the Broadmore 1700 via the NIM IOM. In a redundant system, BITS clock may be provided to each NIM IOM.
Connectors	NIM IN – BNC connector reserved for future use. NIM OUT – BNC connector reserved for future use. BITS – RJ48C connector for BITS input clock.
Jumpers	BITS impedance matching jumpers located on the component side of the circuit board provide selection of 100 ohms, 75 ohms, or no termination.

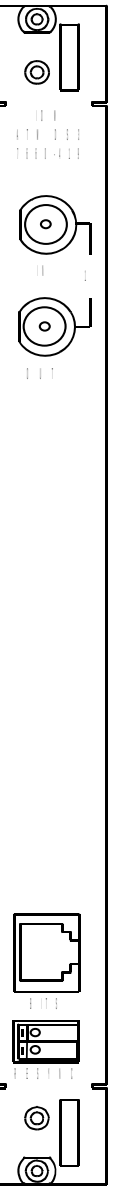


ATM DS3 NIM/SAM



Module Type	NIM or SAM depending on slot
Part Number	7660-018
Slot Number	NIM: A (Protection), B (Working) SAM: C thru P
Features	One DS3 port (44.736 Mbps)
Description	The ATM DS3 NIM provides one bi-directional ATM port at 44.736 Mbps with B3ZS code. Input and output coax connectors are provided for the port on the ATM DS3 IOM. Both PVC and SVC services are provided. Either may be chosen through appropriate module configuration. The Broadmore 1700 supports eleven ATM DS3 ports within the available OC-12 bandwidth of 622.080 Mbps.
Indicators	FAULT – normal (no fault), major alarm, or minor alarm ON-LINE – normal, standby, or not ready

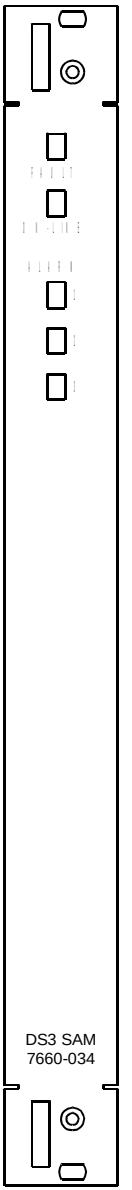
ATM DS3 IOM



Module Type	IOM
Part Number	7660-419
Slot Number	Mounts in chassis rear behind corresponding NIM or SAM
Features	BITS clock input
Description	When used with an ATM DS3 NIM, the ATM DS3 IOM provides a physical ATM cell based interface to an external clock, giving the option of synchronizing the Broadmore 1700 master clock to an external source. In a redundant system, BITS clock may be provided to each ATM DS3 IOM. When used with an ATM DS3 SAM, the ATM DS3 IOM provides a physical ATM cell based interface to user equipment.
Connectors	IN – BNC connector for ATM DS3 input. OUT – BNC connector for ATM DS3 output. BITS – RJ48C connector for BITS input clock.

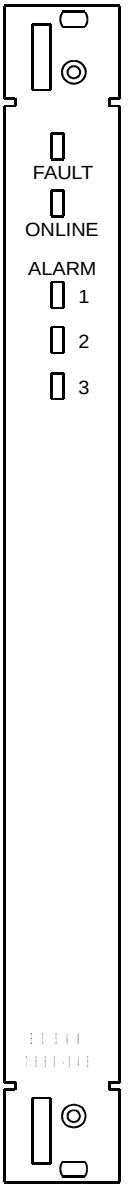
Unstructured DS3-3 SAM

Module Type	SAM
Part Number	7660-034
Slot Number	C thru P
Features	Three DS3 port (44.736 Mbps) 622.080 Mbps total ATM bandwidth
Description	The Unstructured DS3 SAM provides three bi-directional ports, each at 44.736 Mbps. Input and output coax connectors are provided for each port on the DS3 IOM. Both PVC and SVC services are provided. Either may be chosen through appropriate module configuration. The Broadmore 1700 supports 11 DS3 ports within the available OC-12 ATM bandwidth of 622.080 Mbps.
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal, standby, or not ready ALARM (one LED per port) – normal (enabled), major alarm, minor alarm, or no connection



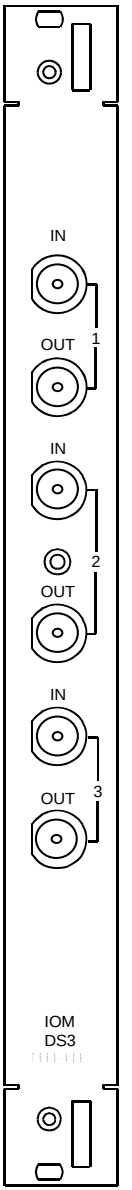
Unstructured E3-3 SAM

Module Type	SAM
Part Number	7660-045
Slot Number	C thru P
Features	Three E3 ports (34.368 Mbps)
Description	The Unstructured E3-3 SAM provides three bi-directional ports, each at 34.368 Mbps. Input and output coax connectors are provided for each port on the IOM. Both PVC and SVC services are provided. Either may be chosen through appropriate module configuration. The Broadmore supports 12 Unstructured E3 ports within the available OC-12 ATM bandwidth of 622.080 Mbps.
Controls	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal, standby, or not ready ALARM (one LED per port) – normal (enabled), major alarm, minor alarm, or no connection



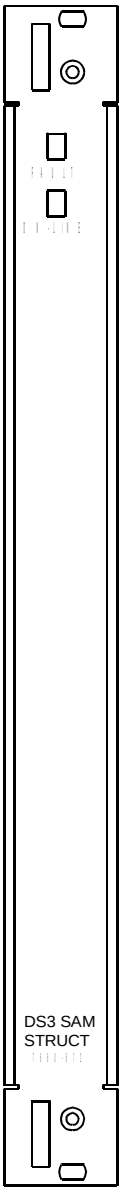
Unstructured DS3-3/E3-3 IOM

Module Type	IOM
Part Number	7660-409
Slot Number	Mounts in chassis rear behind corresponding SAM
Description	Each Unstructured DS3/E3 IOM provides three ports. When used with a DS3 SAM, each port operates at 44.736 Mbps. When used with an E3 SAM, each port operates at 34.368 Mbps.
Connectors	Three pairs of BNC coaxial connectors for RG-59, 75 ohm cable. IN – port receiver RX input OUT – port transmitter TX output



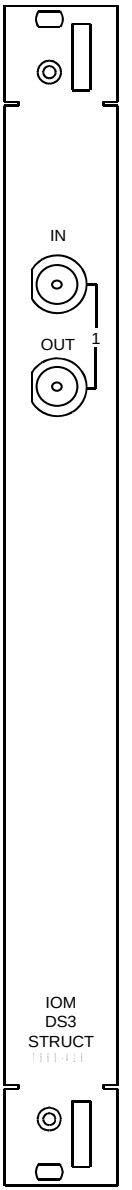
Structured DS3 SAM

Module Type	SAM
Part Number	7660-672
Slot Number	C thru P
Features	One DS3 port (44.736 Mbps) Logical connections directly to DS1 or DS0
Description	The Structured DS3 SAM provides one bi-directional port at 44.736 Mbps. Input and output coax connectors are provided for the port on the IOM. Both PVC and SVC services are provided. Either may be chosen through appropriate module configuration. The Broadmore 1700 supports a maximum of four Structured DS3 modules.
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal, standby, or not ready ALARM (one LED per port) – normal (enabled), major alarm, minor alarm, or no connection

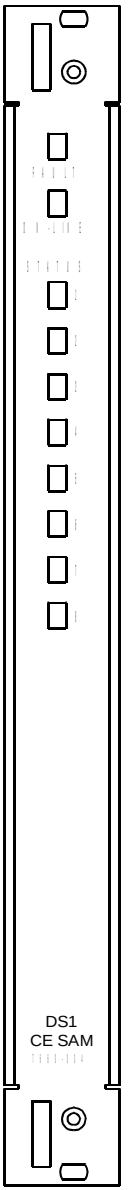


Structured DS3 IOM

Module Type	IOM
Part Number	7660-416
Slot Number	Mounts in chassis rear behind corresponding SAM
Description	One structured DS3 port at 44.736 Mbps.
Connectors	One pair of BNC coaxial connectors. IN – receiver input OUT – transmitter output



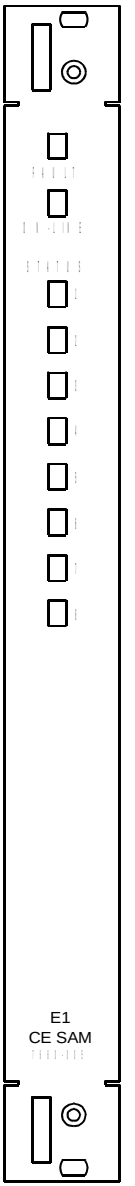
DS1 CE SAM



Module Type	SAM
Part Number	7660-004
Slot Number	C thru P
Features	Eight DS1 circuits per card 1.544 Mbps per port Structured or Unstructured services Logical connections directly to DS0s
Description	The DS1 N×64 CE SAM provides users with DS1 circuit emulation (CE) and the ability to minimize ATM bandwidth by transporting only the timeslots required for the service. There are eight DS1 ports, each at 1.544 Mbps. Each port interfaces to user equipment via an RJ48 connector on the IOM. In the structured mode, each port supports an DS1 signal made up of twenty-four 64 kbps channels (N×64). Each 64 kbps channel or group of channels is groomed for transport through the ATM network. In the unstructured mode, each port carries a 1.544 Mbps data stream.
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal (no fault), standby, or not ready STATUS (one LED per port) – normal (enabled), major alarm, minor alarm, or no connection

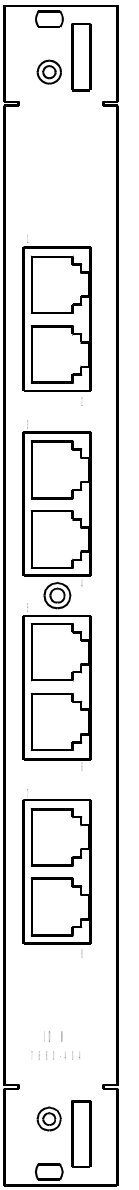
E1 CE SAM

Module Type	SAM
Part Number	7660-005
Slot Number	C thru P
Features	Eight E1 circuits per card 2.048 Mbps per port Structured or Unstructured services Logical connections directly to DS0s
Description	The E1 N×64 CE SAM provides users with E1 circuit emulation (CE) and the ability to minimize ATM bandwidth by transporting only the timeslots required for the service. The E1 SAM conforms to the ATM Forum specification CES-IS (AFVTOA-0078.0000). There are eight E1 ports, each at 2.048 Mbps. In the structured mode, each port supports an E1 signal made up of thirty-two 64 kbps channels (N×64). Each 64 kbps channel or group of channels is groomed for transport through the ATM network. In the unstructured mode, each port carries a 2.048 Mbps data stream.
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal (no fault), standby, or not ready STATUS (one LED per port) – normal (enabled), major alarm, minor alarm, or no connection

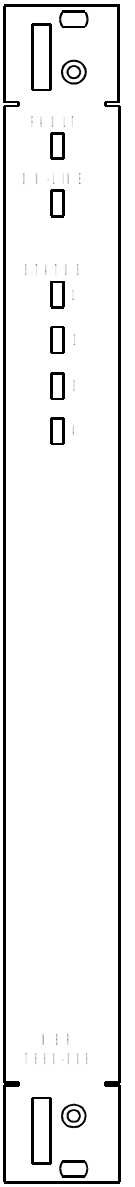


DS1/E1 IOM

Module Type	IOM
Part Number	7660-404
Slot Number	Mounts in chassis rear behind corresponding SAM
Description	This IOM is used with the DS1 and E1 SAMs and provides eight ports. When used with a DS1 SAM, each IOM port is at 1.544 Mbps, with twenty-four 64 kbps channels per port. When used with an E1 SAM, each IOM port is at 2.048 Mbps, with thirty-two 64 kbps channels per port.
Connectors	Eight RJ48 connectors, one per port.



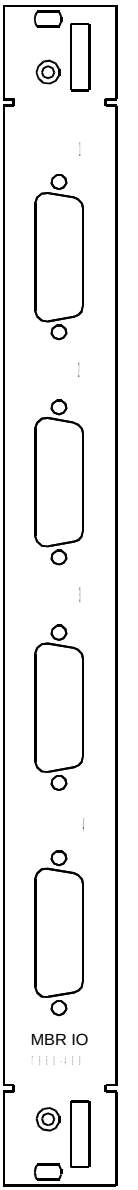
Multi Bit Rate SAM



Module Type	SAM
Part Number	7660-008
Slot Number	C thru P
Features	Four Serial Interfaces per card 75 bps to 10 Mbps in 1 bps increments per port Synchronous or Asynchronous per port
Description	The Multi Bit Rate (MBR) SAM supports up to four separate serial data streams with data rates from 75 bps to 10 Mbps in 1 bps increments. Four connectors are on the IOM. Cable adapters are available for RS-232, RS-449, EIA-530, and V.35 (see *"Cable Specifications" on page E-1*).
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal, standby, or not ready STATUS (one LED per port) – normal (enabled), major alarm, minor alarm, or no connection

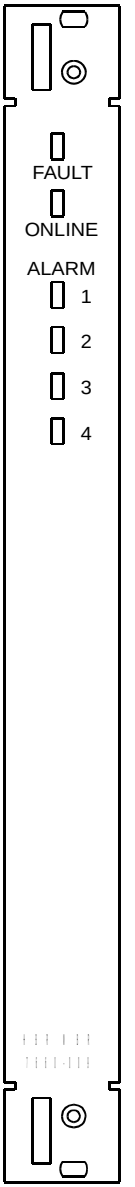
Multi Bit Rate IOM

Module Type	IOM
Part Number	7660-408
Slot Number	Mounts in chassis rear behind MBR SAM as well as Serial Mux and Serial Demux SAMs.
Description	The IOM provides four MBR serial interfaces that can be configured as either DCE or DTE, synchronous or asynchronous. When used with the Serial Multiplexer and Serial Demultiplexer SAMs, the IOM provides eight synchronous serial interfaces (two per connector). The Mux interface is a DTE and the Demux interface is a DCE.
Connectors	For MBR interfaces, the IOM provides four HDB26 (high density) connectors, one per port. Cable adapters are available for RS-232, RS-449, EIA-530, and V.35. For Mux and Demux interfaces, the IOM provides four HDB26 (high density) connectors, two per port. Cable adapters are available for merging the four HDB26 connections into one DTE or DCE DB37 connector (see *"Cable Specifications" on page E-1*).



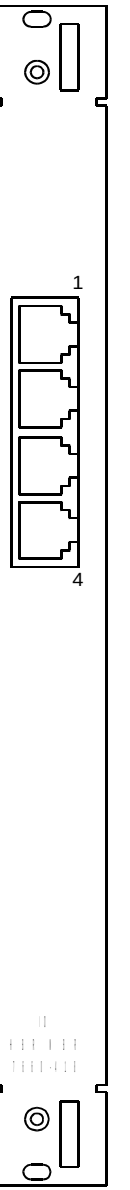
High Bit Rate SAM

Module Type	SAM
Part Number	7660-009
Slot Number	C thru P
Features	Four Serial Interfaces per card 75 bps to 24 Mbps in 1 bps increments per port Synchronous only
Description	The High Bit Rate (HBR) SAM is a high-speed MBR that supports up to four separate data streams. The interfaces can be configured as either DCE or DTE, synchronous only, clock and data only.
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal, standby, or not ready STATUS (one LED per port) – normal (enabled), major alarm, minor alarm, or no connection



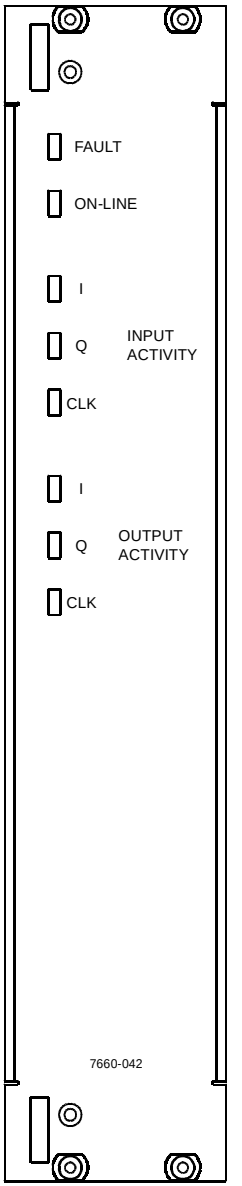
High Bit Rate IOM

Module Type	IOM
Part Number	7660-418
Slot Number	Mounts in chassis rear behind corresponding SAM
Description	The IOM provides four serial interfaces that can be configured as either DCE or DTE, synchronous only.
Connectors	Four RJ45 connectors. An RJ45 to DB25 cable adapter is available (see *"Cable Adapter, HBR (P/N 7660-524)" on page E-21*).



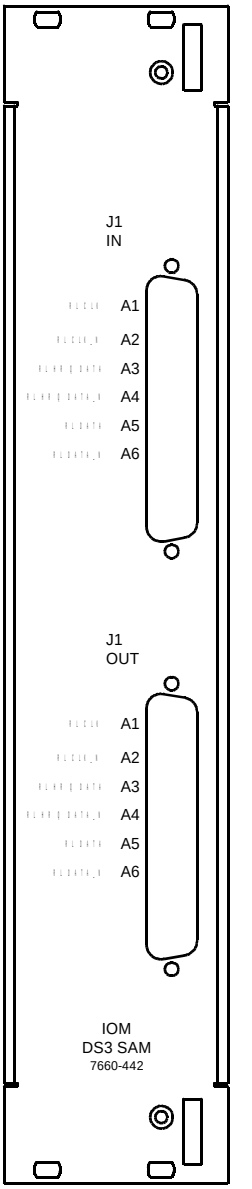
High Speed SAM

Module Type	SAM, double width
Part Number	7660-042
Slot Number	D and E
Features	The High Speed SAM (HSAM) provides multi-rate input/output interfaces for synchronous I, Q, and Clock signals at data rates up to 137.088 Mbps.
Description	Sustained synchronous data rates are 137.088 Mbps (on both I & Q), 47.85 Mbps, 10.00 Mbps, or 200 Kbps. At the 137 Mbps rate, data is aligned and presented on both I and Q at a composite rate of 274.176 Mbps by bridging cellbus C and D through a Bridge Card installed on the rear midplane connectors. At the 47.85 Mbps, 10.00 Mbps, and 200 Kbps rates, data is presented on the I channel only, and is a composite rate.
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal, standby, or not ready INPUT ACTIVITY (one LED per I, Q, and Clock port) OUTPUT ACTIVITY (one LED per I, Q, and Clock port)

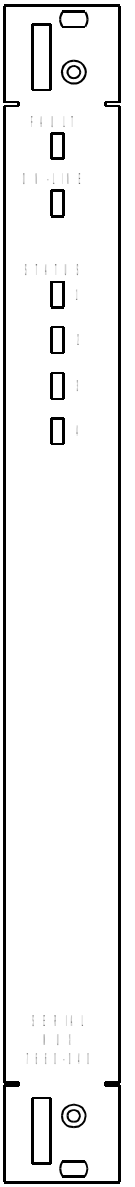


High Speed SAM IOM

Module Type	IOM, double width
Part Number	7660-442
Slot Number	Mounts in chassis rear behind corresponding HSAM in slots D and E.
Description	The high speed interface (input and output) is Low Voltage Differential Signal (LVDS) for sending and receiving I, Q, and Clock data at a composite rate of 274.176 Mbps by bridging cellbus C and D. **Note**: An HSAM Bridge card must be installed on the rear midplane connectors before installing the HSAM IOM.
Connectors	Two Combo-D female connectors, one for Inputs and one for Outputs. For connector pinouts, see *". Wire List for High Speed SAM (HSAM) Connectors (Combo-D female)" on page E-27*.

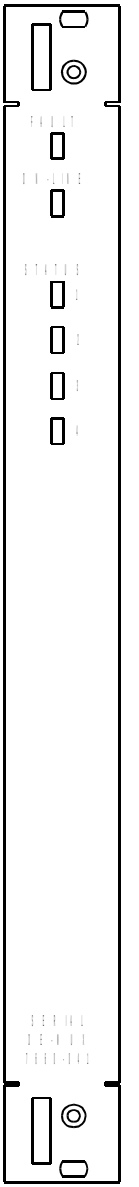


Serial Multiplexer SAM



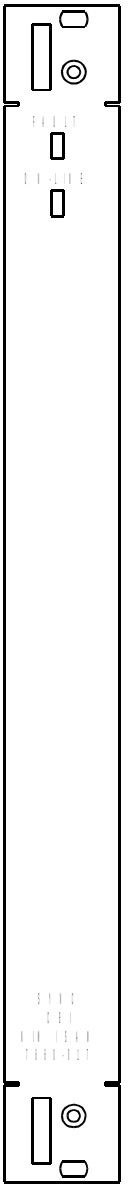
Module Type	SAM
Part Number	7660-040
Slot Number	C thru P
Description	The Serial Multiplexer (Mux) SAM provides synchronous data multiplexing eight RS-422A interfaces over one ATM AAL1 VCC. Temporal alignment is provided by using the clock input on port 1 to synchronize all eight interfaces. A typical application is to use a Serial Multiplexer to combine several serial data streams into one VCC for efficient transmission over an ATM network or satellite link. A Serial Demultiplexer at the receiving site then splits the VCC into separate, time aligned data streams. The Serial Multiplexer supports data rates of 500 kbps to 3 Mbps per port, with a combined rate of 24 Mbps. This SAM uses the *"Multi Bit Rate IOM" on page 1-26*, with two ports on each of the four connectors. Cable adapters are available for merging the four HDB26 connections into one DB37 DTE connector (see *"Cable Specifications" on page E-1*).
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal, standby, or not ready STATUS – not used

Serial Demultiplexer SAM



Module Type	SAM
Part Number	7660-041
Slot Number	C thru P
Description	The Serial Demultiplexer (Demux) SAM provides synchronous data demultiplexing of a single ATM AAL1 VCC into eight RS-422A interfaces. All eight output interfaces are clocked together to ensure temporal alignment. A typical application is to use a Serial Multiplexer to combine several serial data streams into one VCC for efficient transmission over an ATM network or satellite link. A Serial Demultiplexer at the receiving site then splits the VCC into separate, time aligned data streams. The Serial Demultiplexer supports data rates of 500 kbps to 3 Mbps per port, with a combined rate of 24 Mbps. This SAM uses the *"Multi Bit Rate IOM" on page 1-26*, with two ports on each of the four connectors. Cable adapters are available for merging the four HDB26 connections into one DB37 DCE connector (see *"Cable Specifications" on page E-1*).
Indicators	FAULT – normal (no fault), major alarm, minor alarm, or no connection ONLINE – normal, standby, or not ready STATUS – not used

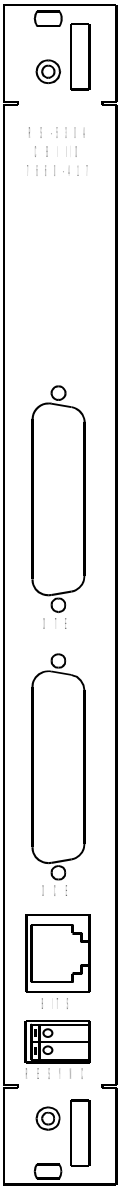
Cell Bearing Interface (CBI) NIM/SAM



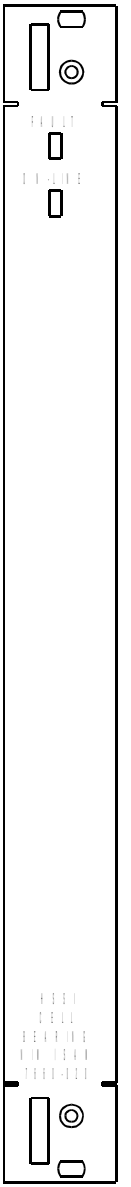
Module Type	NIM or SAM depending on slot
Part Number	7660-017
Slot Number	NIM: B (Working), A (empty) SAM: C thru P
Features	Can be used as a NIM or SAM 56 kbps to 16.384 Mbps in 1 kbps increments
Description	The Cell Bearing Interface (CBI) is a synchronous serial interface that can be configured as either a DTE or DCE. The CBI provides the ability to multiplex multiple cell streams (PVCs) over a single interface. Essentially the CBI performs a Cell Relay function while maintaining the existing Class of Service for each connection. Each CBI is also capable of supporting multiple VPIs on a per PVC basis. A typical application for the CBI would be to multiplex multiple PVCs over a SATCOM uplink connecting remote ATM networks. The CBI can be used as a Network Interface Module (NIM) to provide network backbone transport or as a Service Access Module (SAM) to aggregate multiple synchronous serial links over an ATM network. The CBI NIM supports a BITS interface to ensure proper network timing. The CBI also supports Crypto Resync for applications that require encryption. The CBI has been successfully tested with the following encryption devices: KG-94, KIV-19, KG-194, KIV-7, KG-175, and KG75.
Indicators	FAULT – indicates normal (no fault), major alarm, minor alarm, or no PVC assigned ON-LINE – indicates normal, standby, or not ready

Cell Bearing Interface (CBI) IOM

Module Type	IOM
Part Number	7660-417
Slot Number	Mounts in chassis rear behind corresponding NIM or SAM
Features	DTE or DCE interface
Description	The CBI IOM provides the physical interface connection to the user equipment. There is a single DB25 DCE interface and a single DB25 DTE interface but only one interface can be used at a time. The interface is software selectable as part of the provisioning process.
Connectors	DTE – DB25 RS-530 serial data port for DTE device DCE – DB25 RS-530 serial data port for DCE device BITS – RJ48 for Building Integrated Timing System input. (Used only when used as NIM IOM.) RESYNC – Spring terminal block for Crypto Resync output to cryptographic equipment.
Jumpers	RESYNC – Jumpers on the component side of the card can be set to configure the output signal for +5V (default), –5V, or contact closure.

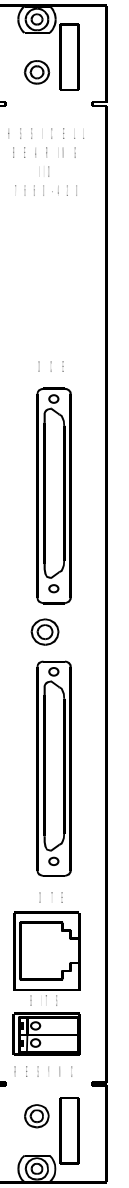


High Speed Cell Bearing Interface NIM/SAM



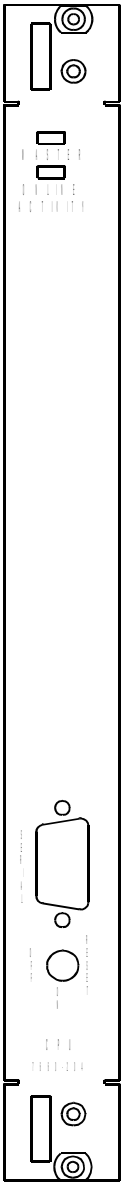
Module Type	NIM or SAM depending on slot
Part Number	7660-020
Slot Number	NIM: B (Working), A (empty) SAM: C thru P
Features	Can be used as a NIM or SAM 56 kbps to 52 Mbps in 1 kbps increments
Description	The High Speed Serial Interface (HSSI) Cell Bearing Interface is a synchronous serial interface that can be configured as either a DTE or DCE. The HSSI-CBI provides the ability to multiplex multiple cell streams (PVCs) over a single interface. Essentially the HSSI-CBI is performing a Cell Relay function while maintaining the existing Class of Service for each connection. Each HSSI-CBI is also capable of supporting multiple VPIs on a per PVC basis. A typical application for the HSSI-CBI would be to multiplex multiple PVCs over a SATCOM uplink connecting remote ATM networks. The HSSI-CBI can be used as a Network Interface Module (NIM) to provide network backbone transport or as a Service Access Module (SAM) to aggregate multiple synchronous serial links over an ATM network. The HSSI-CBI supports a BITS interface to ensure that proper network timing is being utilized. In addition, the HSSI-CBI supports Crypto Resync for applications that require encryption.
Indicators	FAULT – indicates normal (no fault), major alarm, minor alarm, or no PVC assigned ON-LINE – indicates normal, standby, or not ready

High Speed Cell Bearing Interface IOM



Module Type	IOM
Part Number	7660-420
Slot Number	Mounts in chassis rear behind corresponding NIM or SAM
Features	DTE or DCE interface
Description	The HSSI-CBI IOM provides SCSI-2 (HDB50) type DTE and DCE interface connectors but only one may be used at a time. The interface is software selectable as part of the provisioning process.
Connectors	DTE – HDB50 serial data port for DTE device DCE – HDB50 serial data port for DCE device (Cables are available for above; see *"Cable Specifications" on page E-1*). BITS – RJ48 for Building Integrated Timing System input. (Used only when used as NIM IOM.) RESYNC – Spring terminal block for Crypto Resync output to cryptographic equipment. 26 to 18 AWG
Jumpers	RESYNC – Jumpers on the component side of the card can be set to configure the output signal for +5V (default), –5V, or contact closure.

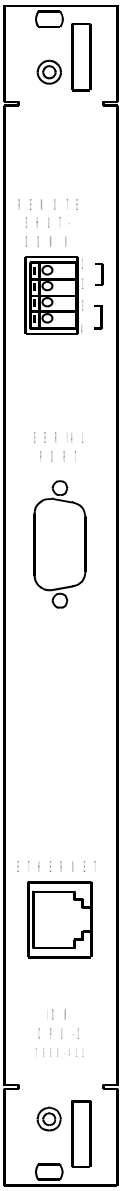
CPU



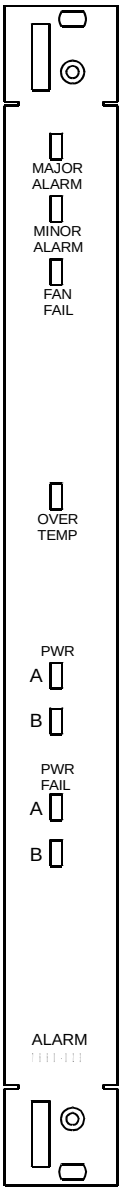
Module Type	CPU
Part Number	7660-204
Slot Number	CPU Q (primary), CPU R (redundant)
Description	The CPU module provides the facilities for all system monitoring, reporting, logging, and management. The CPU also performs Segmentation and Reassembly (SAR) for all in-band network management over the ATM network. The CPU stores the FIPS-2 validated Broadmore/SSHield management software on a 40MB flash memory system. The operating system is pSOS version 2.2.7.
Controls	Toggle switch ON – normal operation OFF – turns CPU off RESET – resets the CPU
Indicators	MASTER – lights green when operating as master; lights amber when operating as standby. (Normally, the CPU that comes online first will be the master.) ON-LINE ACTIVITY – blinks amber to indicate CPU activity including master/standby mirroring.
Connectors	SERIAL – DB9 RS-232 DTE serial management port (Ethernet management port is on CPU IOM)

CPU IOM

Module Type	IOM
Part Number	7660-411
Slot Number	Mounts in chassis rear behind corresponding CPU
Description	The CPU IOM provides physical access to the system for Ethernet and also provides the remote shutdown interface as explained below.
Controls	REMOTE SHUT-DOWN – Spring terminal block for installing cables for remote CPU shut-down. A remote contact closure is used to short pins 1-2 or pins 3-4. Jumpers control how these contact closures work.
Jumpers	REMOTE SHUT-DOWN – Two user-installed jumpers on the component side of the module control how the remote contact closures work. As shipped, the jumpers connect pins 1-2 to pins 3-4 so that shorting either pair will reboot both CPUs. With the jumpers removed, shorting pins 1-2 will reboot the other-slot CPU; shorting pins 3-4 will reboot the same-slot CPU. It is recommended that the jumpers be removed.
Connectors	SERIAL – reserved for future use. ETHERNET – RJ48 modular connector.

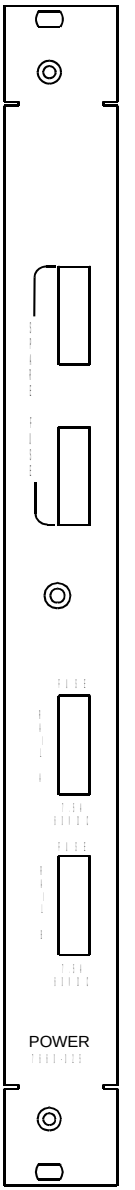


Alarm Power Module (APM)



Module Type	APM
Part Number	7660-023
Slot Number	APM
Description	An APM is factory-installed in the right-most card slot as viewed from the front. The APM provides EMI power conditioning and over-current protection for each of the two –48 VDC power sources. Two 7.5 amp, 60 VDC fuses are located on the bottom of the module. A green LED is provided for each power source to indicate power is present. Major and minor alarms are displayed via LEDs, which are visible with the front panel installed. An over-temperature indicator on the front of this module is reserved for future use. The APM is user-replaceable.
Indicators	MAJOR ALARM – lights red for major alarm MINOR ALARM – lights amber for minor alarm FAN FAIL – lights red when fan tray fails OVER TEMP – (reserved for future use) PWR A/B – lights green when power is applied to the A or B inputs PWR FAIL A/B – lights red when power supply A or B fails

Alarm Power Module IOM



Module Type	IOM
Part Number	7660-025
Slot Number	Mounts in chassis rear behind APM
Description	The Alarm Power IOM comes with four Bussman GMT 7.5A, 60V fuses. Two of the fuses are used to protect the redundant DC power inputs (A and B) and the other two fuses are provided as spares. The chassis will operate with only one power source but two independent sources are recommended to provide power supply redundancy. Power, alarm, and grounding connections are made to the connector panel at the bottom rear of the chassis.
Fuses	RAIL A – protection fuse for –48VDC power input A RAIL B – protection fuse for –48VDC power input B SPARE FUSES – two spare fuses in holders

CHAPTER 2

Planning and Ordering Guide

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Application Planning Guide

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- Deployment Options ... *2-4*
- ATM Gateway ... *2-5*
- Mission-Critical Voice Service ... *2-6*
- TDM Circuit Aggregation and Backhaul ... *2-7*
- Tactical Communications Extension ... *2-8*
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- Custom 300 Mbps Serial Data System ... *2-11*
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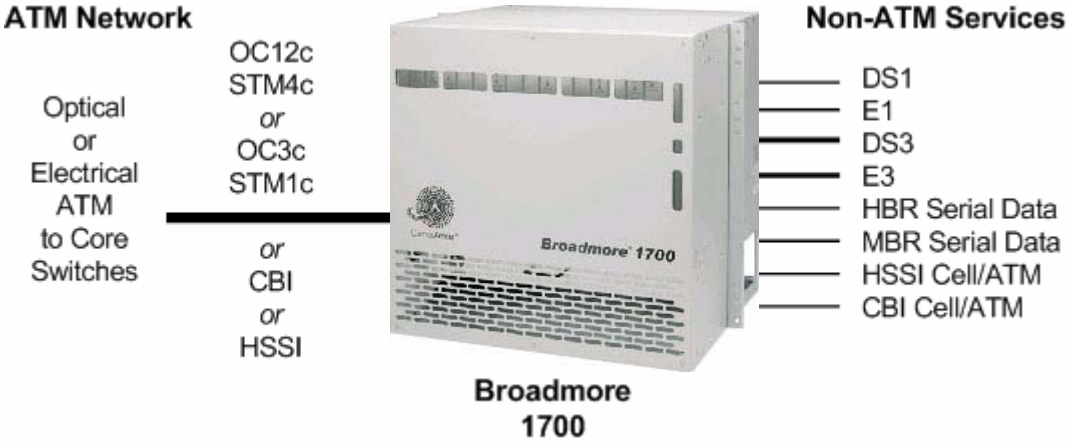
Basic Features

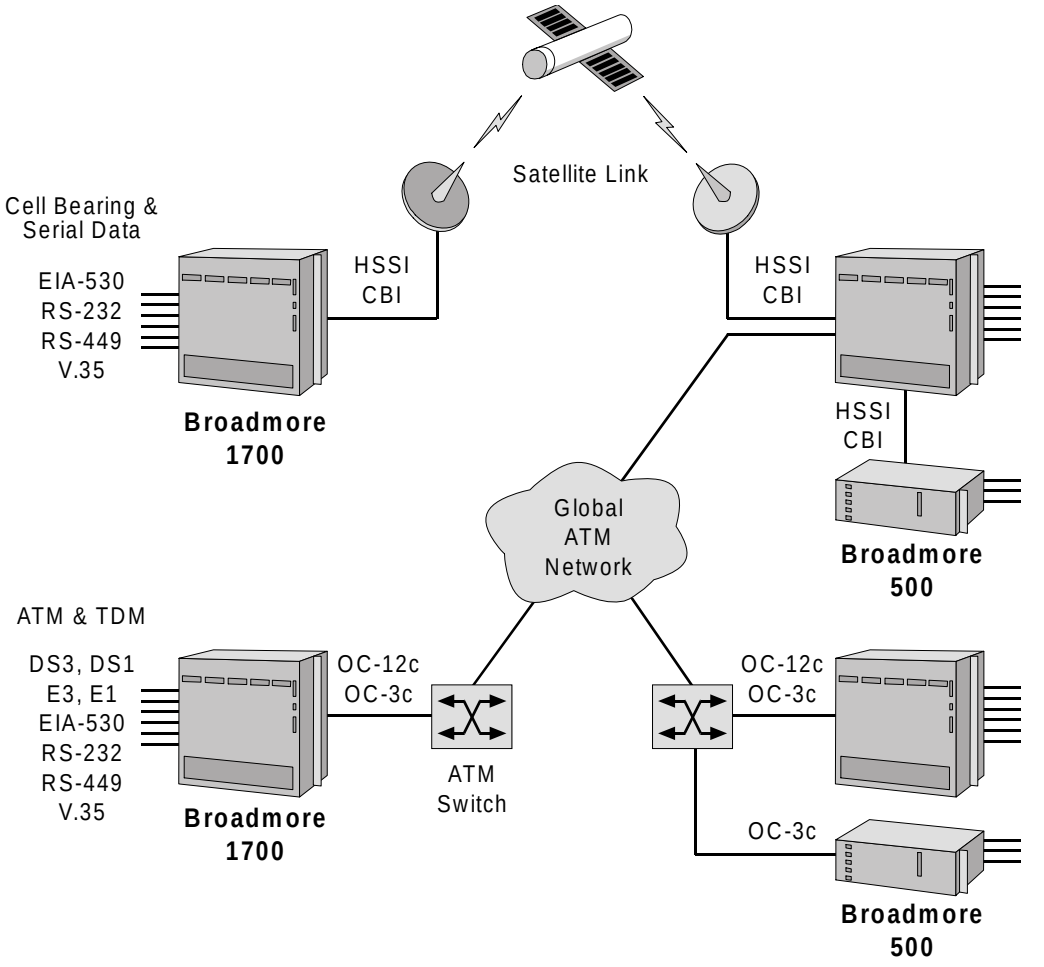
The Broadmore is designed as an ATM network service access node that supports the transport of existing broadband services (voice, video, and data) over ATM networks. The Broadmore accepts signals from non-ATM-ready equipment, converts the signals to standard ATM cells, and multiplexes the cells onto a single ATM User Network Interface (UNI) port. Permanent Virtual Circuit (PVC) and Switched Virtual Circuit (SVC) services are available based upon the user-defined module configuration.

The Broadmore accepts a variety of constant bit rate (CBR) services including structured and unstructured DS3, DS1, E3, E1, cell bearing (HSSI and CBI), and high speed and multi bit rate (HBR and MBR) serial data.



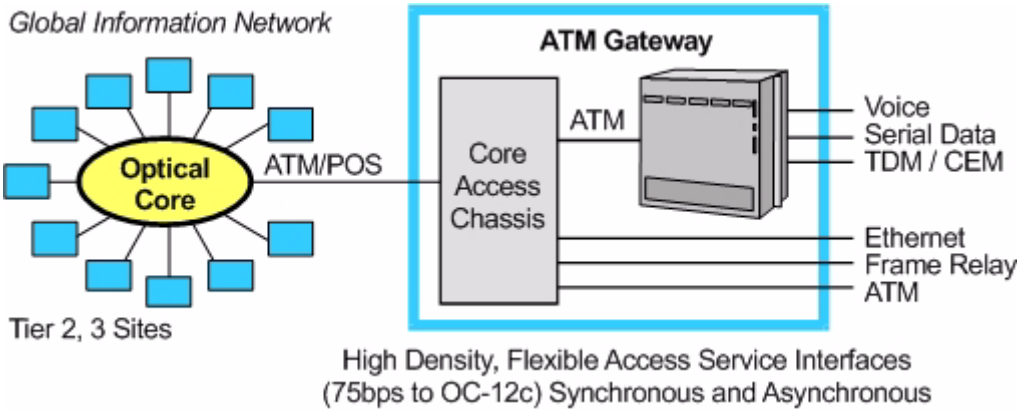
Deployment Options

Typically, the Broadmore 1700 is deployed at the edge of an ATM network as the ATM node element closest to the customer. Cell bearing interfaces (HSSI and CBI) allow efficient transfer of serial data over satellite links while OC-12c and OC-3c network interfaces transport a variety of multiplexed ATM and TDM services.



ATM Gateway

The Broadmore 1700 provides secure, reliable, TDM circuit aggregation and backhaul, transport for mission-critical applications.

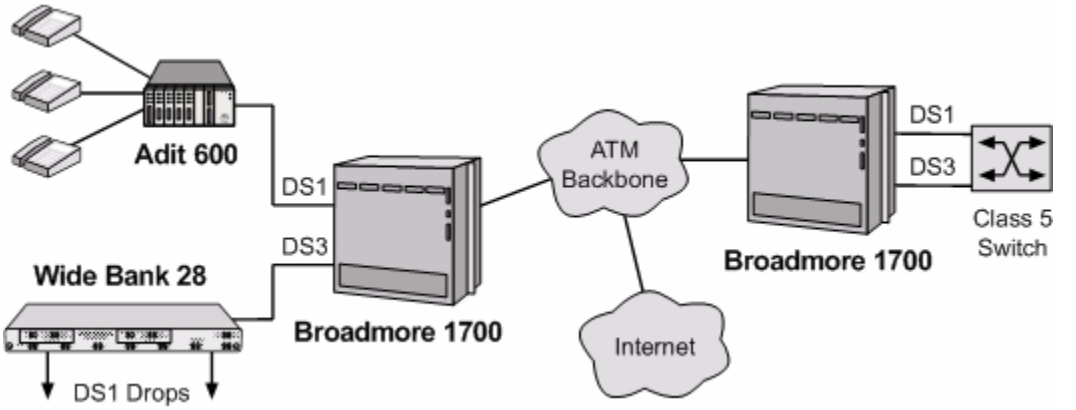


Attributes	Functions	Interfaces
• **Scalable**: Expand the network, node capacity, and users with minimal devices • **Secure**: FIPS 140-2 validated to protect the traffic and nodes from unauthorized access • **Predictable**: Provide appropriate QoS guarantees under all traffic conditions • **Reliable**: Provide high service availability with system and component redundancy • **Flexible**: Provide a wide range of services and interfaces with mix of service cards	• Supports mission-critical, real-time applications • Reduces capital equipment expense • Improves network reliability • Improves service availability • Improves network security • Simplifies operations and management	• ATM (DS3 to OC-12c) • TDM/CEM • Synchronous Serial • Asynchronous Serial • V.35 • RS-232 • RS-449 • EIA-530

Mission-Critical Voice Service

The Broadmore 1700 provides:

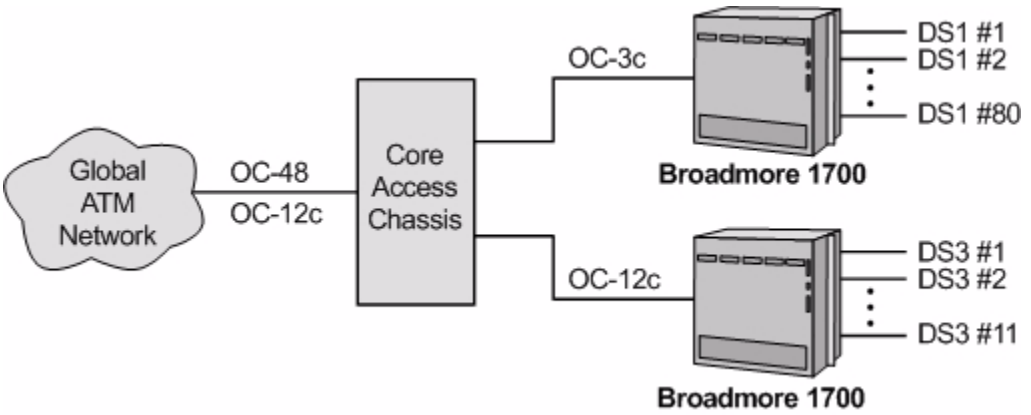
- Cost effective deployment of voice services over fiber
- Provisioning of traditional TDM services while benefiting from ATM QoS
- Bulk termination of DS1s, DS3s, or structured services
- Multiple voice and data services from a single shelf



TDM Circuit Aggregation and Backhaul

The Broadmore 1700 provides:

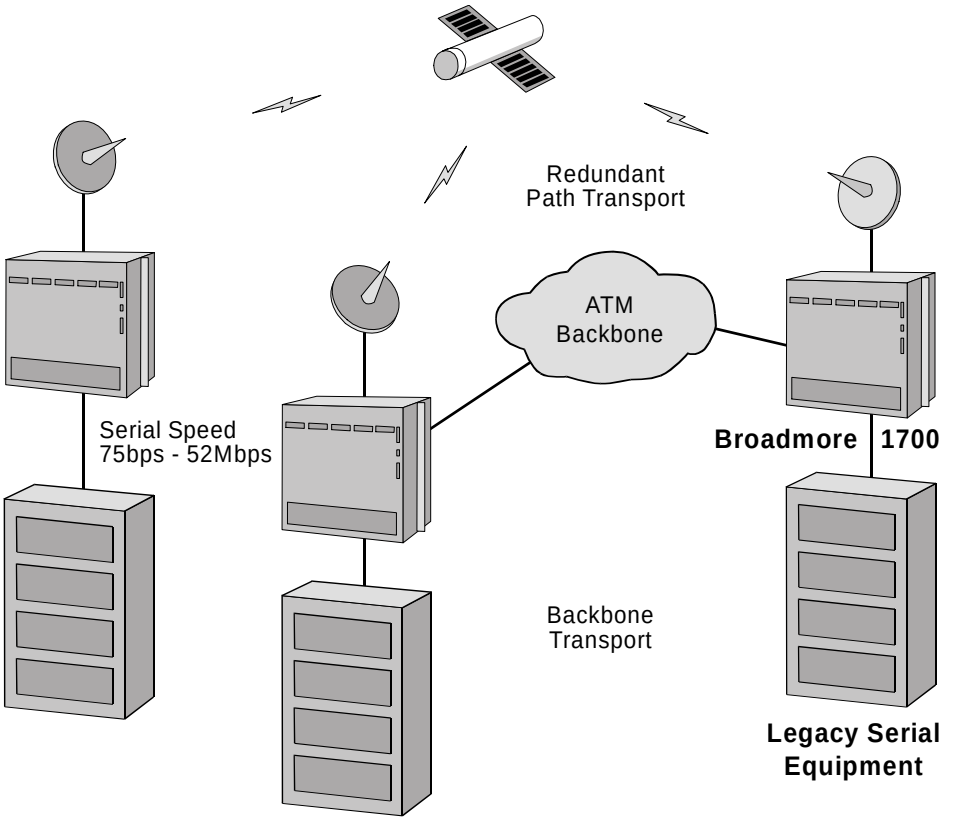
- Massive DS3/DS1 trunking
  - – Remote shelf extension to the ATM network
- Reduced costs, increased access
  - – Up to 11 DS3s per ATM Switch interface (versus the usual 4)
  - – Up to 80 DS1s per ATM Switch interface
  - – Additional circuits with channelized ports
- Edge cross-connect capability
  - – User connections switched directly
  - – Central terminations are not required (as with DACS)



Tactical Communications Extension

The Broadmore 1700 provides:

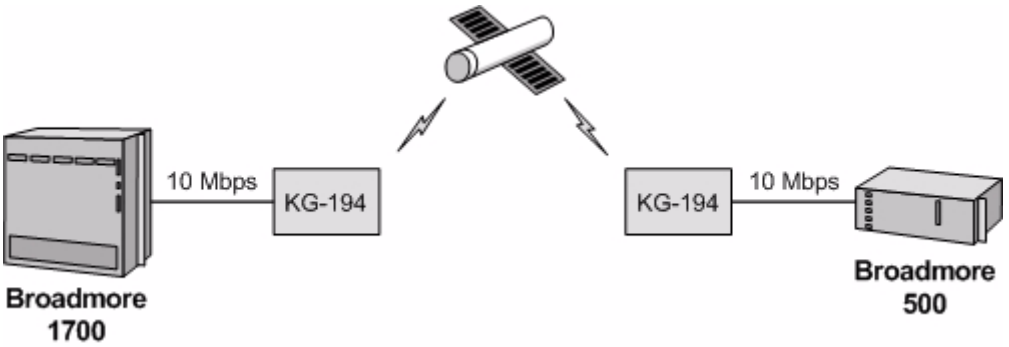
- Network Services extension over SATCOM
- Bulk serial transport over SATCOM to support legacy applications
- Single Transport System (STS) to support future end-to-end cell networks
- QoS extended over the Satellite for voice, video, and data
- Crypto resync ensures that the secure network keeps running



Encrypted Circuits

The Broadmore's cell bearing interfaces support KG resync:

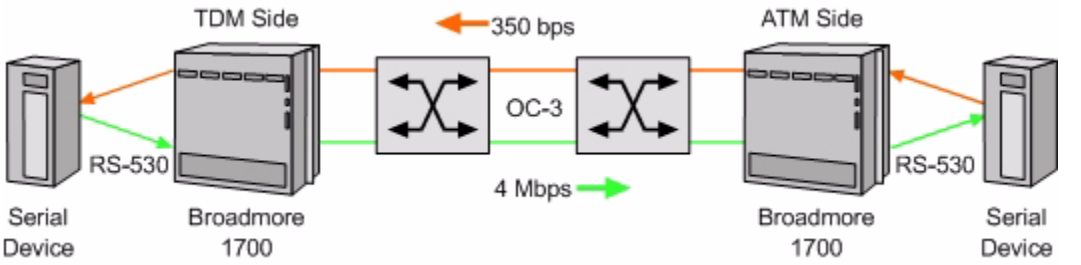
- CBI and HSSI-CBI modules
- Re-sync "On" time: 0 to 6 seconds duration in 0.1 second increments
- Re-sync "Off" time: 0 to 60 seconds in 0.1 second increments
- JITC Certified and tested with KG-75, KG-94, KG-175, KG-194, KIV-7, and KIV-19
- Can use smaller Broadmore 500 chassis at remote sites



Custom Communications

- Adaptive timing – Transmit at one rate, receive at another rate
- QoS preserved – Regardless of bit rate
- Oddball bit rates – Supports any serial bit rate in one-bit increments
- Single clock source across all 8 interfaces – Guaranteed synchronization
- Single muxed cell stream consisting of up to 8 channels – Single PVC interlaced with user defined number of channels
- Auto provisioning of channels at the de-mux side – No user guessing as to what channels the data is flowing on
- Up to 52 Mbps per aggregate stream

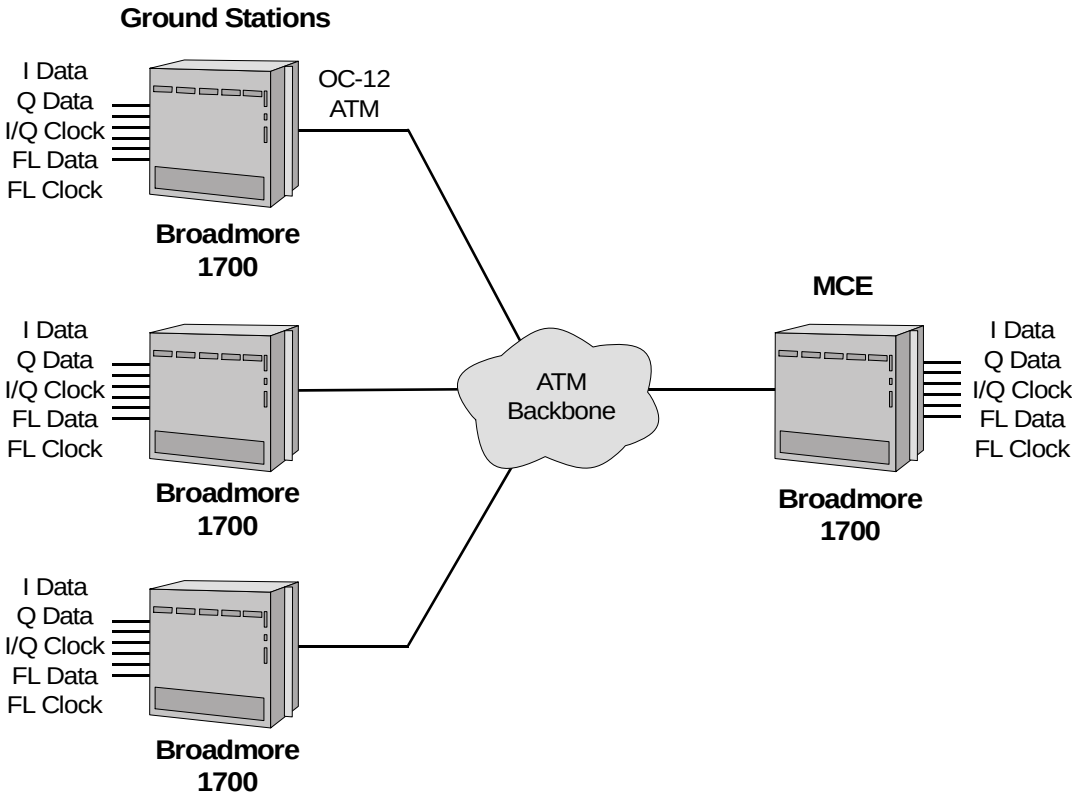
Example with Asymmetrical Data Rates



Custom 300 Mbps Serial Data System

This example shows how easy it is to configure the Broadmore 1700 to meet specific customer requirements for a 300 Mbps serial data to ATM converter system providing both high rate and low rate data transmission between user equipment interfaces. It comprises four identical Broadmore 1700 units located at three Ground Terminals (GTs) and one end point called the MCE.

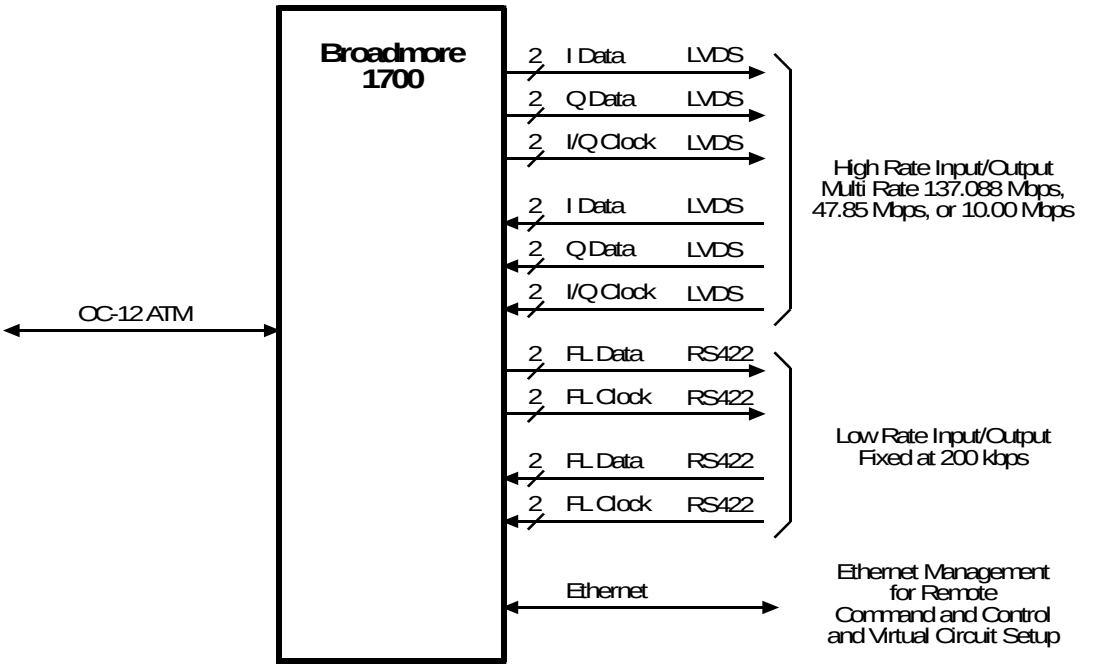
GTs convert high rate I/Q inputs and low rate FL inputs into ATM for transmission across the ATM backbone or satellite network to the MCE, which converts the ATM back to I/Q and FL outputs.



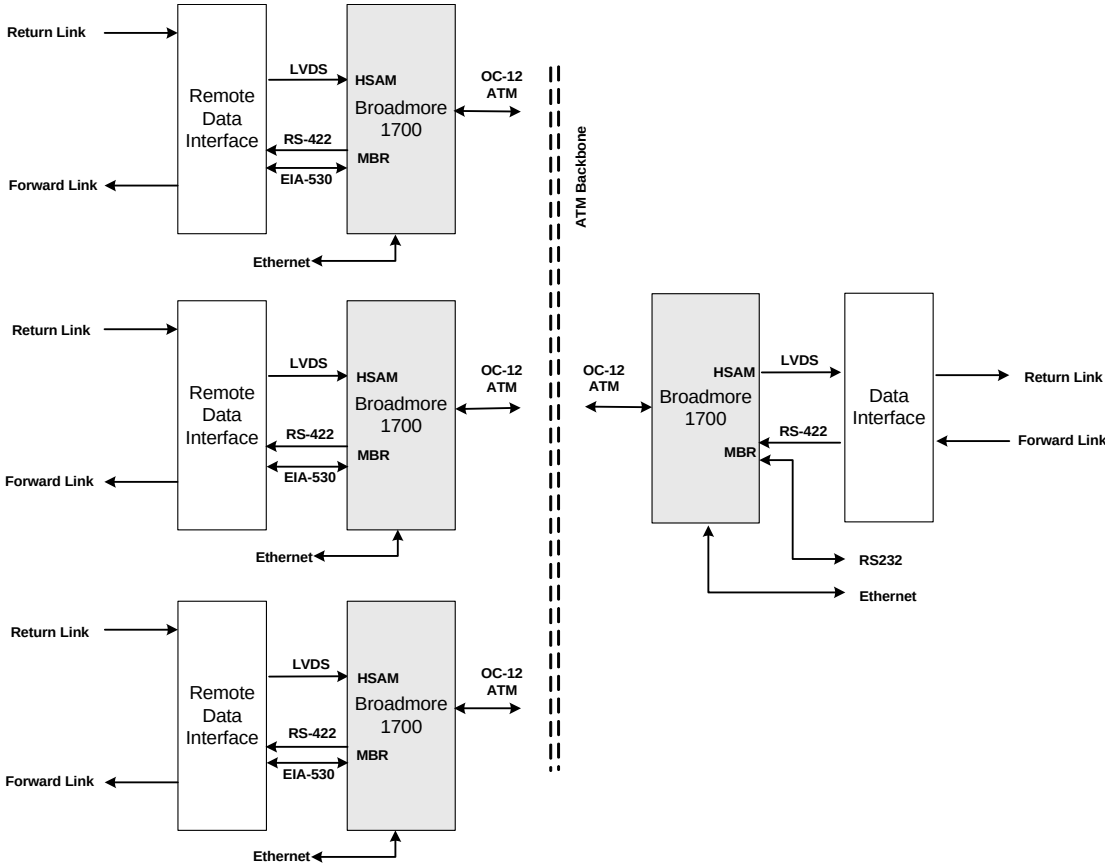
Unit Configuration. Each Broadmore 1700 unit provides an ATM SONET OC-12c / SDH STM-4 fiber optic interface to the ATM backbone or satellite network. Each Broadmore 1700 unit also has low rate (MBR) and high rate (HSAM) data interfaces that may be used as either inputs or outputs, depending on the network location. High rate interfaces are differential Low Voltage Data Signals (LVDS). Low rate interfaces are RS422.

The high rate interface aligns and presents 137.088 Mbps I and Q data as ATM at a composite rate of 274.176 Mbps. The receiving end reconstructs the data and clock signals. At rates of 200 kbps, 47.85 kbps, and 10.00 bps, the data is presented on the I channel only at a composite rate identical to the input rate.

The 200 kbps low rate interface converts RS422 data and clock to OC-12 for transmission to the receiving end, where it is converted back to RS422 data.



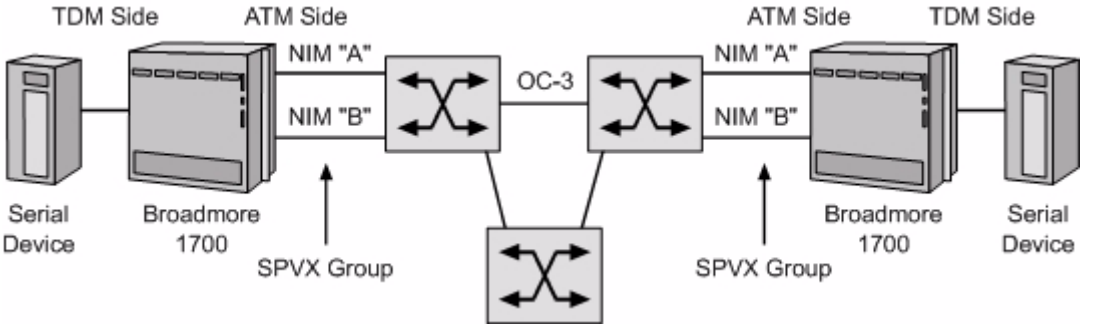
Equipment Interface. The overall system interface is shown below. Contact Carrier Access for interface specifications.



Mission-Critical Circuit Resiliency

The Broadmore 1700 provides:

- Carrier-class equipment features including fully redundant, hot-swappable components
- Automatic Protection Switching (APS)
  – Circuit, source, and destination device protection
- Distributed Protection Switching (DPS)
  – Circuit, source, destination device, edge node, and CPE protection
  – Logical and physical redundancy between the switch and access shelf
  – Unprecedented SLA delivery for TDM and Serial link
  – Maximum network availability
  – APS functionality through ATM technology
  – Guaranteed interoperability
  – Simple configuration



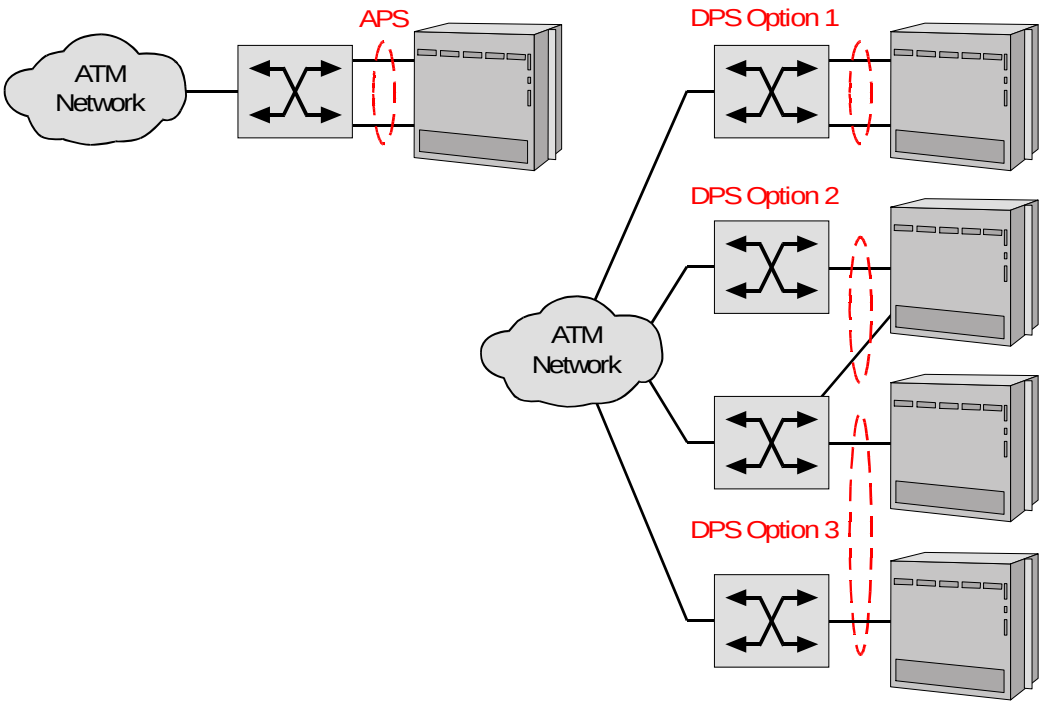
APS Features and Benefits

- Two ports are grouped as primary and secondary SPVC ports (see figure below). Provides access link, core, and port redundancy for source and/or destination devices.

DPS Features and Benefits

- SPVx Redundancy Group (Option 1) – Two ports are grouped as primary and secondary SPVC ports. Provides access link, core, and port redundancy for source and/or destination devices.
- SPVx Source Resiliency (Option 2) – SPVC connection between source switches monitors status of active switch. Once failure is identified, initiates fail-over to backup.
- SPVx Destination Resiliency (Option 3) – Full redundancy for the destination device, port, destination switch, access link, and core. Primary and backup destinations can be geographically separate.

APS and DPS Features



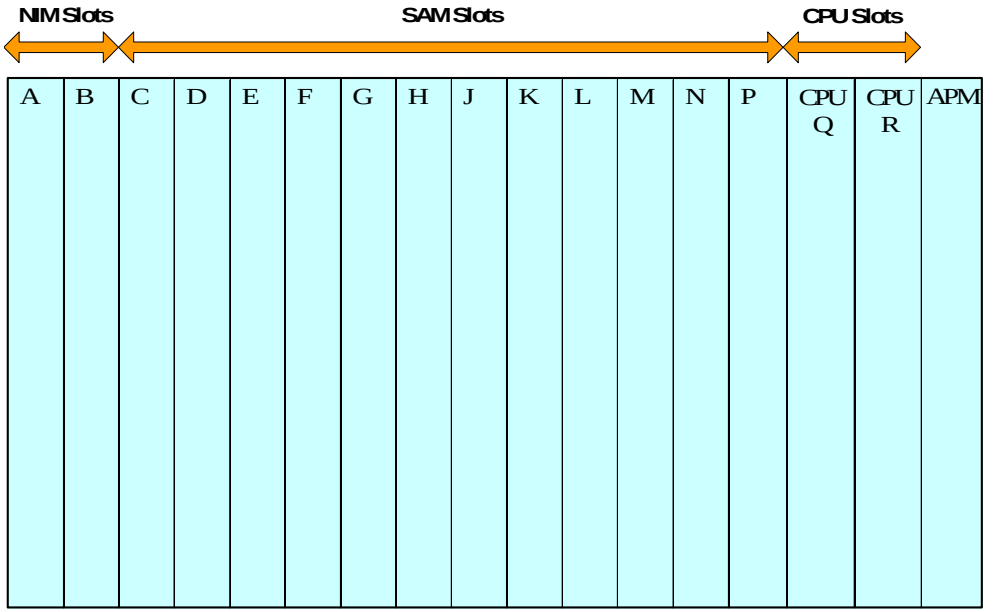
System Planning Factors

- System Architecture ... *2-16*
- Cell Bus Configuration ... *2-18*
- OC-12c NIM Configuration Guidelines ... *2-19*
- OC-3c NIM Configuration Guidelines ... *2-20*
- 300 Mbps Serial Data System Configuration Guidelines ... *2-21*
- CBI and HSSI NIM Configuration Guidelines ... *2-22*
- ATM Bandwidth per Cell Bus ... *2-22*
- ATM Bandwidth per Module ... *2-23*
- ATM Network Loading ... *2-25*

The Broadmore 1700 is a customizable system. The following factors should be considered when planning and configuring a system to meet customer requirements.

System Architecture

The Broadmore 1700 system architecture is based upon a midplane design allowing modules to be installed from the front and rear of the chassis. There are 17 vertical slots as viewed from the front with the cover removed, as shown below.



The Broadmore 1700 is shipped in a minimum usable configuration with a factory installed Alarm/Power Module in the right-most slot. Unused slots are covered with blank panels except the slots for installing a single NIM, SAM, and CPU module.

The Broadmore 1700 chassis can be configured with the following modules:

- Network Interface Module (NIM) – 1 or 2 (must be identical)
- Service Access Module (SAM) – 1 or more
- Central Processing Unit (CPU) – 1 or 2 (must be identical)
- Alarm/Power Module (APM) – 1 (factory installed)
- Input/Output Module (IOM) – 1 for each NIM, SAM, and CPU (installed in rear panel immediately behind corresponding module in front panel)

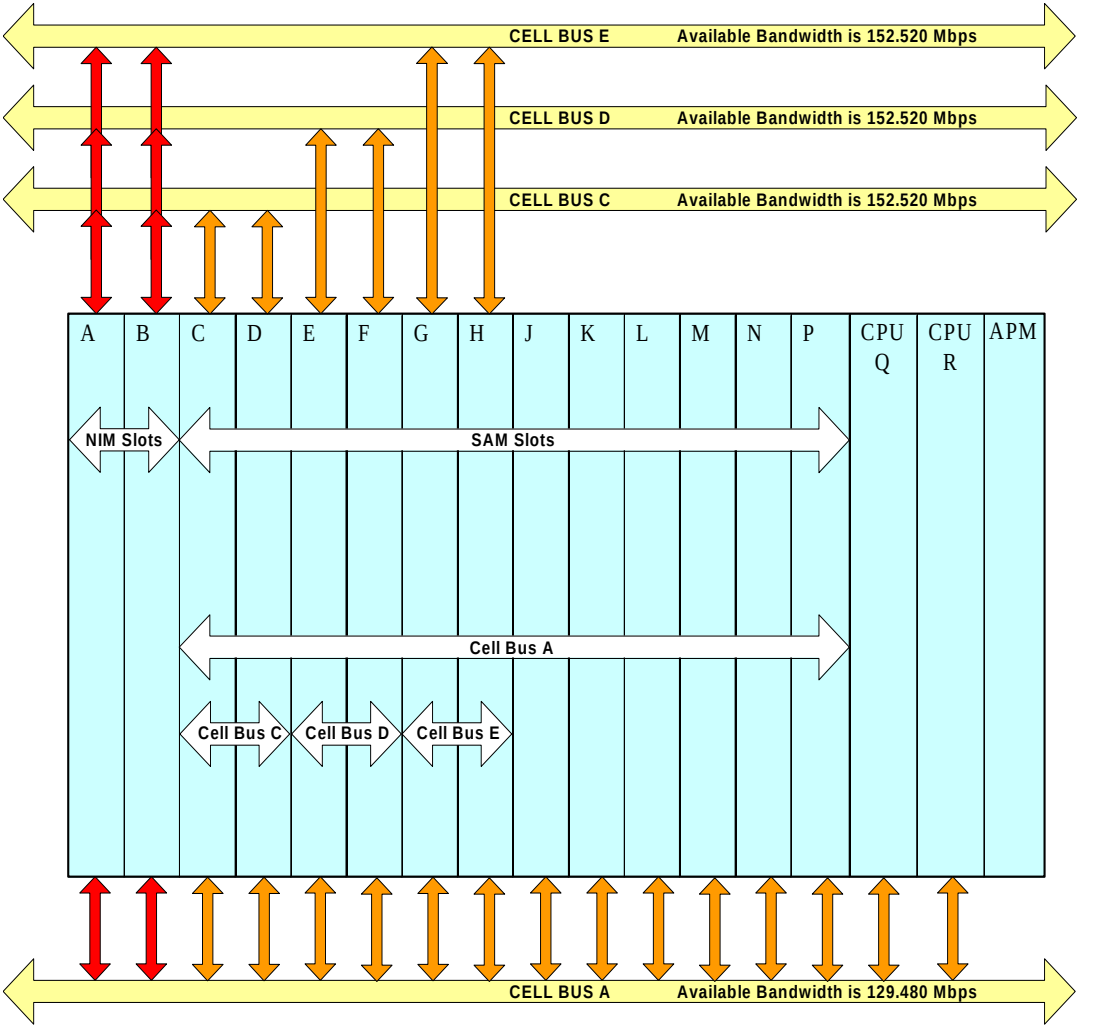
Module slots are populated as follows:

- The right-most slot, labeled “APM”, is for the Alarm/Power Module.
- The two left-most slots, labeled “A” and “B”, support Network Interface Modules (NIMs). The protection NIM goes in slot “A” and the working protection NIM in slot “B”. If the system is not configured for NIM redundancy, the single NIM should be inserted in slot “B”.
- The two slots closest to the APM slot, labeled “CPU Q” and “CPU R”, support the CPU modules. Either CPU can operate as the master or redundant protection unit. If the system is not configured for CPU redundancy, the single CPU can be inserted in either slot.
- Slots “C” through “P” are for Service Access Modules (SAMs). SAMs should be installed from left to right, starting with slot C, so that they will be close to the NIMs.
- Input/Output Modules (IOMs) install from the rear of the chassis, directly behind the corresponding front panel modules.

NOTE: For recommended SAM locations, see the following configuration guidelines.

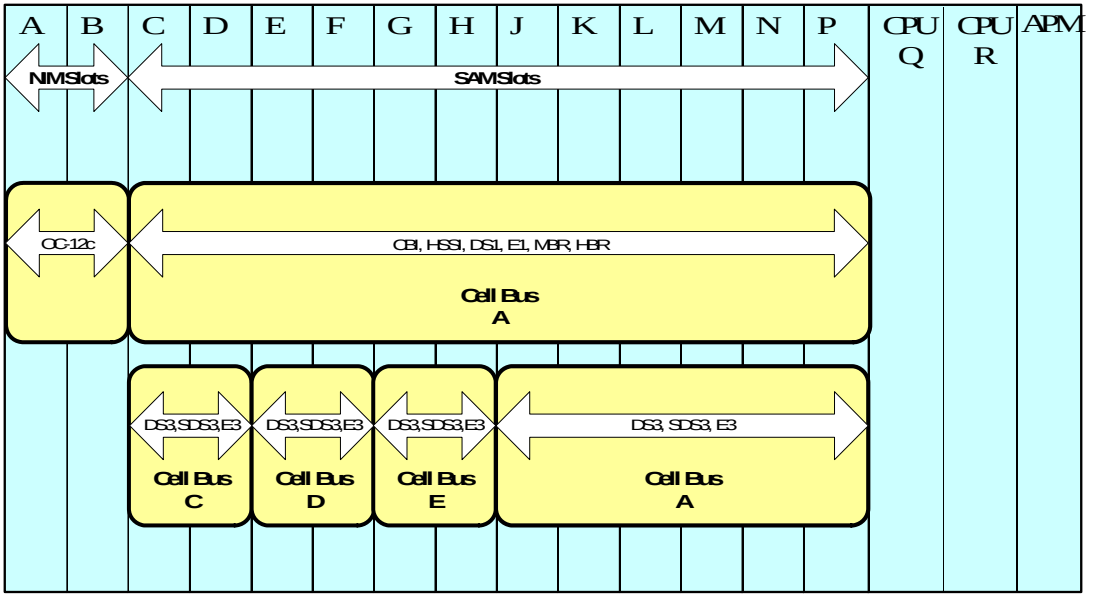
Cell Bus Configuration

The Broadmore multiplexes user data onto ATM cell buses. Depending on the installed NIM and SAMs, there can be up to four cell buses, designated A, C, D, and E. Each cell bus is associated with specific module slots, as shown in the following figure.



OC-12c NIM Configuration Guidelines

An OC-12c NIM can utilize Cell Buses A, C, D, and E. Use the following guidelines and recommendations when using OC-12c NIMs.

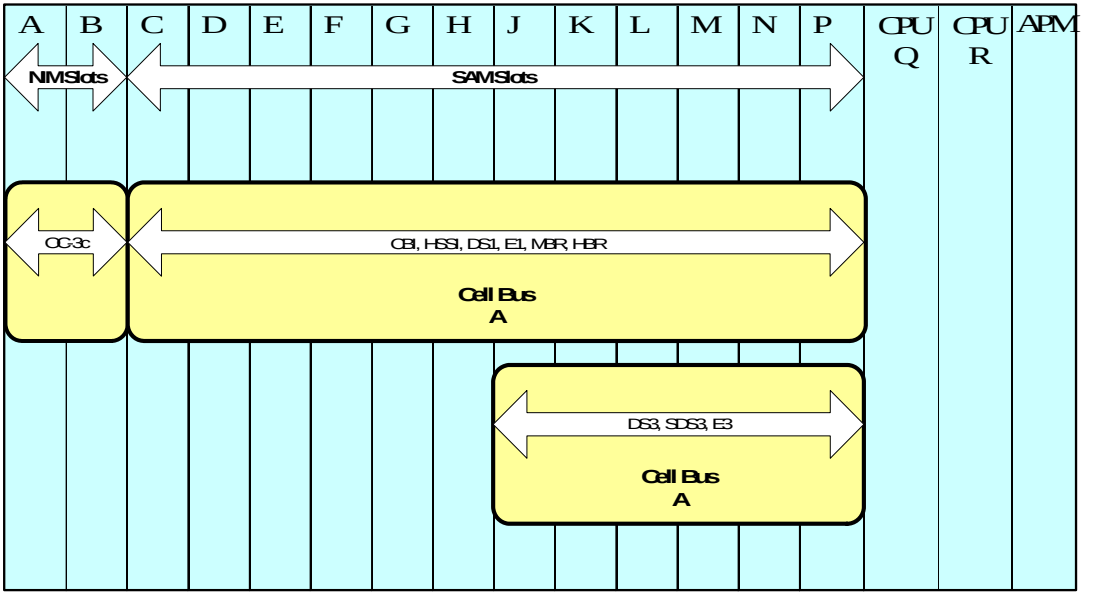


- For redundancy, install OC-12c NIMs in slots A and B.
- If a single OC-12c NIM is used, it should be installed in slot B.
- SAMs should be installed in the chassis from left to right (as viewed from the front) in slots C through P.
- All DS1, E1, CBI, MBR, and HBR SAM cards use only cell bus A.
- DS1, E1, CBI, HSSI, MBR, and HBR SAM cards can be installed in any of slots C through P.
- CBI and HSSI SAM cards should be installed as close as possible to the NIM.
- The double-width HSAM card is installed in slots D and E, enabling it to use cell buses C and D.

- Unstructured DS3, unstructured E3, and structured DS3 SAM cards should be located on different cell buses to maximize available bandwidth utilization. Only one DS3 or E3 SAM should be installed on each cell bus.
- The Broadmore 1700 supports a maximum of four Structured DS3 modules.

OC-3c NIM Configuration Guidelines

An OC-3c NIM uses only Cell Bus A. Use the following guidelines and recommendations when using OC-3c NIMs.

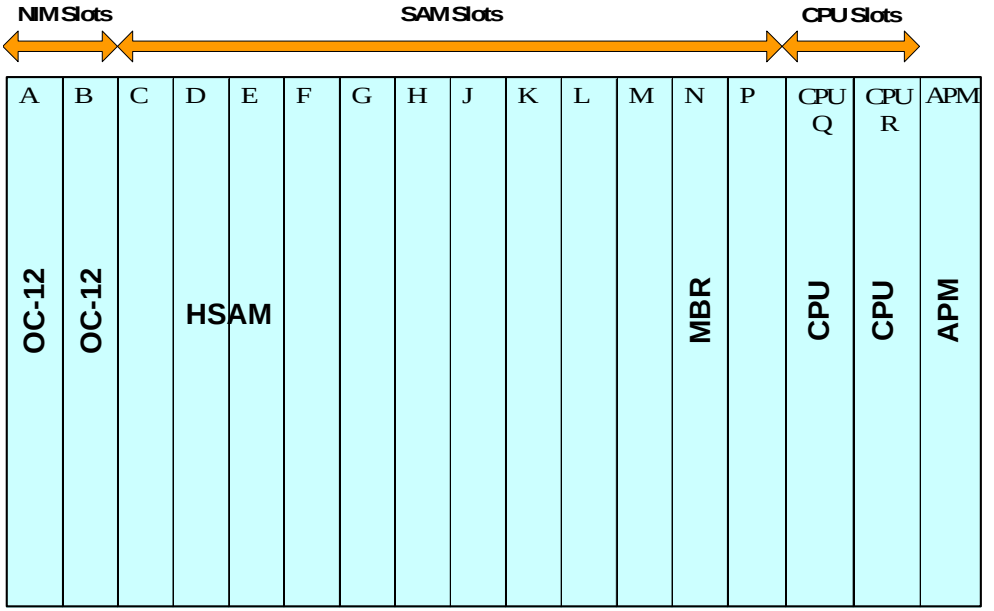


- For redundancy, install OC-3c NIMs in slots A and B.
- If a single OC-3c NIM is used, it should be installed in slot B.
- SAMs should be installed in the chassis from left to right (as viewed from the front) in slots C through P.
- DS1, E1, CBI, HSSI, MBR, and HBR SAM cards can be installed in any of slots C through P.
- CBI and HSSI SAM cards should be installed as close as possible to the NIM.
- Only one DS3 or E3 SAM can be installed.

300 Mbps Serial Data System Configuration Guidelines

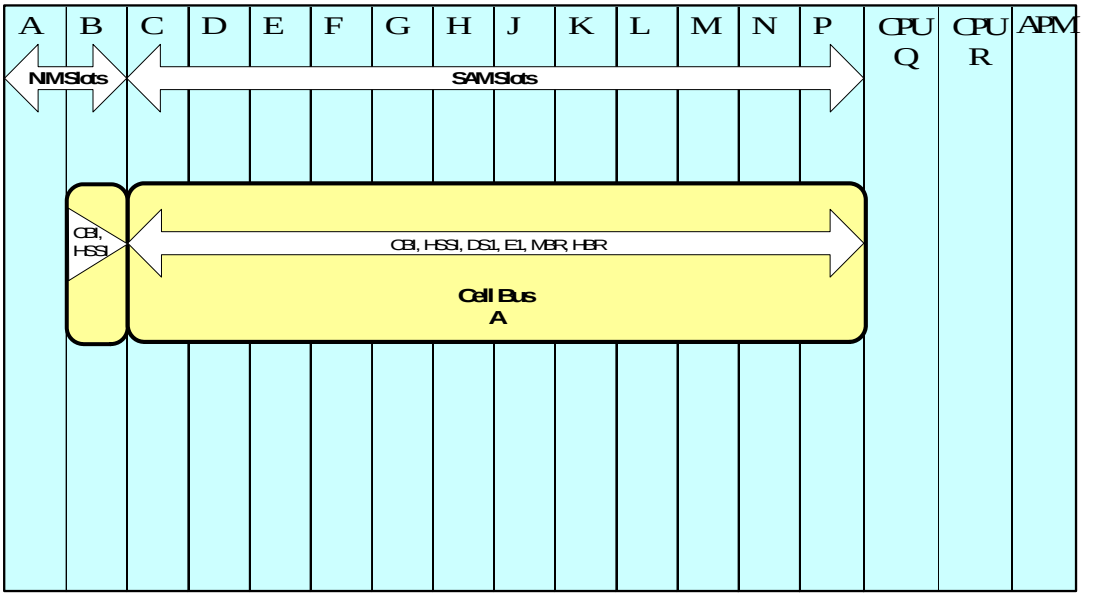
The *"Custom 300 Mbps Serial Data System" on page 2-11* is configured with the following modules installed.

- Network Interface Module (NIM) – 2 OC-12 NIMs (slots A and B)
- Service Access Module (SAM) – 1 HSAM (slots D and E) and 1 MBR (slot N)
- Central Processing Unit (CPU) – 2 (slots Q and R)
- Alarm/Power Module (APM) – 1 (factory installed)
- Input/Output Module (IOM) – 1 for each NIM, SAM, and CPU (installed in rear panel immediately behind corresponding module in front panel)
- HSAM Bridge – 1 (installed in rear panel immediately behind HSAM)



CBI and HSSI NIM Configuration Guidelines

When a CBI or HSSI-CBI card is used as a NIM, it uses only Cell Bus A. Redundant NIM operation is not available. Use the following guidelines and recommendations when using the CBI or HSSI as a NIM.



- The single CBI or HSSI NIM should be installed in slot B.
- SAMs should be installed in the chassis from left to right (as viewed from the front) in slots C through P.
- DS1, E1, CBI, HSSI, MBR, and HBR SAM cards can be installed in any of slots C through P.
- CBI and HSSI SAM cards should be installed as close as possible to the NIM.

ATM Bandwidth per Cell Bus

Depending on the SAMs being used, a Broadmore 1700 chassis with an OC-12c NIM can multiplex data onto all four ATM cell buses, providing a maximum ATM bandwidth of up to 599.04 Mbps. However, the maximum ATM bandwidth with an OC-12c NIM and using only DS1, E1, CBI, HSSI, MBR, and HBR SAMs is 129.48

Mbps because those SAMs all use cell bus A. This is the same maximum ATM bandwidth (129.48 Mbps) as when using an OC-3c NIM, which uses only cell bus A.

The maximum ATM bandwidth for each cell bus is shown below.

Cell Bus	Service Slots	ATM Bandwidth Available to SAMs
A	C thru P (used by SAMs) Q, R (used by CPUs for AAL5 management traffic)	< 129.48 Mbps
C	C, D	< 155.52 Mbps
D	E, F	< 155.52 Mbps
E	G, H	< 155.52 Mbps
Total	All service slots	< 599.04 Mbps

ATM Bandwidth per Module

The CPU and each SAM uses a specific amount of ATM bandwidth, as shown below.

Module P/N	Module Description	ATM Bandwidth Required (Mbps)
7660-206	CPU, FIPS, 10Base-T, AAL5	Average: < 2.5 Mbps Maximum: 4.0 Mbps
7660-004	DS1, CE, N×64, 8 Port	Whole DS1: 1.74 Mbps (each port) Each DS0: 0.075 Mbps Maximum: 13.94 Mbps (all ports)
7660-005	E1, CE, N×64, 8 Port	Whole E1: 2.31 Mbps (each port) Each DS0: 0.075 Mbps Maximum: 18.48 Mbps (all ports)
7660-008	Multi Bit Rate (MBR), 4 port	(53/P*) × (user clock rate) per port
7660-009	High Bit Rate (HBR), 4 port	(53/P*) × (user clock rate) per port

Module P/N	Module Description	ATM Bandwidth Required (Mbps)
7660-017	Cell Bearing Interface (CBI), 1 Port	Bandwidth equals cell rate, up to the maximum selected baud rate. Maximum: 16.384 Mbps
7660-018	ATM DS3, 1 port	Whole DS3: 44.736 Mps
7660-020	High Speed CBI (HSSI-CBI), 1 Port	Bandwidth equals cell rate, up to the maximum selected baud rate. Maximum: 52 Mbps
7660-034	DS3, Unstructured, 3 port	Whole DS3: 50.45 Mbps Maximum: 151.35 Mbps (all ports in service)
7660-042	High Speed SAM (HSAM), 1 port	Up to 137.088 Mbps per port, allowing a composite of 274.176 Mbps when using two cell buses.
7660-045	E3, Unstructured, 3 port	Each port: 39.21 Mbps Maximum: 117.62 Mbps (all ports in service)
7660-040	Serial Multiplexer, 8 port	(53/P*) × (user clock rate) per port
7660-041	Serial Demultiplexer, 8 port	(53/P*) × (user clock rate) per port
7660-672	DS3, Structured, 1 port	Whole DS3: 50.45 Mbps

*P = payload fill (or cell fill) in bytes, where P = 1 to 47 inclusive. The number of data bytes present in each ATM cell generated by the module.

NOTE: The software will automatically adjust the payload fill. For example, assume the clock rate is 100 kHz and the data rate is 100 kbaud. If the payload fill is set to 47 (maximum data fill), the ATM bandwidth used by this port would be (53/47) × (100K) = 112800 bps. If the payload fill is set to 1 (minimum data fill), the ATM bandwidth is now (53/1) × (100K) = 5300000 bps, or about 47 times greater.

ATM Network Loading

The Broadmore 1700 multiplexes cells from various input SAMs into a single ATM UNI signal that is delivered to the ATM network. There is overhead associated with this process that reduces the total bandwidth available to the connected service equipment. The Broadmore 1700 can load the network UNI to 95% capacity without dropping cells. Service capacities are shown in the following table.

ATM Service Capacity

Module	Cell Bus	ATM Network Rate (Mbps)	ATM Service Capacity (Mbps)
OC-3c/STM-1c	A	155.520	149.760
OC-12c/STM-1c	A B C D Total	155.520 155.520 155.520 155.520 622.080	149.760 149.760 149.760 149.760 599.040

Applying this overhead to individual SAMs is one way to calculate the service capacity for the Broadmore 1700. Add the individual module bandwidths to get the Broadmore 1700 total bandwidth requirement. Compare the network bandwidth requirement to the service capacity to ensure network bandwidth is not exceeded. Some typical network loading factors are shown below for SAMs in a Broadmore 1700 chassis with OC-3c and OC-12c NIMs.

For example, with an OC-3c NIM each DS1 SAM port has a line-loading factor of 1.17% per port. If all 8 DS1 SAM ports are used, the network loading would be 1.17% per port times 8 ports = 9.36%. The Broadmore 1700 with OC-3c NIM supports a maximum ATM network loading of 81 DS1 connections (1.17% × 81 = 94.77%) or 61 E1 ports (1.54% × 61 = 93.94%).

Similar calculations can be made of an OC-12c NIM but the user must also ensure that the line loading does not exceed the service capacity for each cell bus.

OC-3c Network Loading Factors

SAM	ATM Network Rate (Mbps)	ATM Service Capacity (Mbps)	OC-3c Network Incremental Loading per SAM Port
DS3	50.45	44.736	33.69%
DS1	1.74	1.544	1.17%
DS1	1.74	1.544	25.88%
E3	38.76	34.368	25.88%
E1	2.31	2.048	1.54%

OC-12c Network Loading Factors

SAM	ATM Network Rate (Mbps)	ATM Service Capacity (Mbps)	OC-12c Network Incremental Loading per SAM Port
DS3	50.45	44.736	8.4%
DS1	1.74	1.544	0.29%
DS1	1.74	1.544	0.58%
E3	38.76	34.368	6.45%
E1	2.31	2.048	0.38%

Installation Planning Factors

The Broadmore 1700 is designed and delivered ready for installation in a standard 19" or 23" relay or EIA equipment rack. Several factors should be considered and some decisions made to ensure a smooth installation that meets all requirements. Installation factors and system specifications are shown in the following tables.

Factor	Consideration
Access Clearance	Broadmore 1700 requires the following access clearances for service and maintenance: • Front: 24″ • Rear: 4″ • Top: 1.75″ (Broadmore 1700 units may be stacked without this clearance.)
Power Source	DC Power: –48 VDC at 5 amperes is connected to the rear of the Broadmore chassis. A second –48 VDC source may be connected for redundancy. Optional AC Power Supply Tray: Converts 110/240 VAC (50-60 Hz) to –48 VDC. Rack mounted tray (3.5″ high) installs immediately below Broadmore chassis and can hold two power supplies. Unit comes with one power supply and cables. A second power supply with cables can be added to provide redundant DC power to the Broadmore. AC and DC power connections are at the rear of the unit. The DC cable is approximately 3.5 ft. long.

Factor	Consideration
Cable Management	Overall cable lengths can be optimized by equipment placement. • User equipment interface cable connections are made to the input/output modules at the rear of Broadmore chassis. • ATM interface connections is via fiber cable to the network interface modules at the front of Broadmore chassis. • Access and cable strain-relief tie-down points are provided. • Chassis front and rear cover removal is required.

Ordering Guide

- Contact Information ... *2-29*
- Broadmore 1700 Chassis ... *2-29*
- Broadmore 1700 Options and Spares ... *2-30*
- Network Interface Module (NIM) Options ... *2-31*
- Service Access Module (SAM) Options ... *2-34*

Contact Information

Inside Sales Direct: 800-365-2593
Fax: 303-218-5680
E-mail: inside-sales@carrieraccess.com
Products and Solutions: www.carrieraccess.com

Broadmore 1700 Chassis

Each Broadmore 1700 chassis comes equipped with one CPU and IOM, one APM and IOM, Dual –48VDC Power Inputs, and a Fan Tray. The following items are also included: CD-ROM with Users Guide and Software, Grounding Strap, Console Cable, Combination 19”/23” Rack Mount, and one year warranty.

Item	Part No.
Broadmore 1700 Chassis, –48 VDC	7665-1700

Broadmore 1700 Options and Spares

The following items are optional and are not required for basic operation. They can be included to enhance network availability or utilized as spares.

Item	Part No.
Broadmore 1700 –48 VDC Chassis (with APM and IOM only)	7665-17C
Broadmore 1700 AC Power Tray Converts voltage from 110/220 VAC (50-60 Hz) to –48 VDC. Includes one 240W AC Power Supply, one additional slot for a Redundant 240W AC Power Supply, one 48 VDC Inverter, and one Cable to connect to the Broadmore's –48 VDC Power Input.	7665-17PS
Broadmore 1700 AC Power Supply This is the 240W AC Power Supply used for redundancy in the Broadmore 1700 AC Power Tray.	7660-115
Alarm & Power Module (APM)	7660-023
Alarm & Power I/O Module (APM IOM)	7660-025
Bussman 7.5 Amp Fuse	GMT7.5
Bussman Fuse Cover	GMT-X
Broadmore 1700 Fan Tray	7660-024
Globe Motors Fan Filter	FFM745
Broadmore CPU Module with FIPS 140-2 validated Operating Software	7660-206
Broadmore CPU I/O Module (CPU IOM)	7660-411
CPU Disk-On-Chip Flash Module	750-0044
CPU-2 replacement battery. Panasonic VL1220-1HF, or equivalent	034-0016
LapLink Cable, PC to Broadmore 1700 serial port cable with DB9-F and DB25-F connectors on both ends	51670066-01

Network Interface Module (NIM) Options

- NIM Sets ... *2-31*
- Individual Modules ... *2-32*

The Broadmore 1700 chassis has two NIM slots available for redundancy. Both NIMs must be identical in redundant installations. When a CBI or HSSI-CBI module is used as a NIM, only one NIM can be installed (non-redundant configuration).

NIM Sets

Each set includes the NIM, IOM, and cable option where applicable.

Optical Service Sets	Part No.
OC-12c/STM-4c Single Mode Intermediate Reach Optics (SC) Module Set	7660-314
OC-12c/STM-4c Multi-Mode Optics (SC) Module Set	7660-313
OC-3c/STM-1c Single Mode Intermediate Reach Optics (SC) Module Set	7660-316
OC-3c/STM-1c Multi-Mode Optics (SC) Module Set	7660-317

ATM Service Sets	Part No.
Single Port ATM DS3 NIM/SAM Module Set	7660-318

Serial Service Sets	Part No.
High Speed Serial Interface Cell Bearing Interface (HSSI-CBI) Module Set, (1) 7660-523 SCSI-2 10ft Cable included	7660-320
Cell Bearing Interface (CBI) Module Set, (1) 7660-522 RS-530 5ft Cable Included	7660-327

Individual Modules

Each NIM and corresponding IOM is offered separately for sparing purposes.

Optical Service Modules	Part No.
OC-12c/STM-4c NIM, Single Mode Intermediate Reach Optics (SC)	7660-114
OC-12c/STM-4c NIM, Multi-Mode Optics (SC)	7660-113
OC-3c/STM-1c NIM, Single Mode Intermediate Reach Optics (SC)	7660-116
OC-3c/STM-1c NIM, Multi-Mode Optics (SC)	7660-117
OC-3c/12c IOM with BITS Input	7660-406

ATM Service Sets	Part No.
Single Port ATM DS3 NIM/SAM	7660-018
Single Port ATM DS3 IOM	7660-419

Serial Service Modules	Part No.
High Speed Serial Interface Cell Bearing Interface (HSSI-CBI)	7660-020
High Speed Serial Interface Cell Bearing Interface IOM	7660-420
Cell Bearing Interface (CBI) Module	7660-017
Cell Bearing Interface (CBI) IOM	7660-417

Service Access Module (SAM) Options

- SAM Sets ... *2-34*
- Individual Modules ... *2-36*
- Individual Cables ... *2-38*

The Broadmore 1700 chassis has 12 SAM slots available for user connectivity.

SAM Sets

Each set includes the SAM and corresponding IOM.

ATM Service Sets	Part No.
Single Port ATM DS3 NIM/SAM Module Set	7660-318
High Speed SAM (HSAM) (10–274.176 Mbps) Module Set, including HSAM, HSAM IOM, and HSAM Bridge	7660-342

TDM Service Sets	Part No.
DS3 Structured Single Port Module Set	7660-372
DS3 Unstructured Three Port Module Set	7660-334
E3 Unstructured Three Port Module Set	7660-345
DS1 (Structured & Unstructured) Eight Port Module Set	7660-304
E1 (Structured & Unstructured) Eight Port Module Set	7660-305

Serial Service Sets	**Part No.**
Four Port Multi-Bit Rate (MBR) (75 bps–10 Mbps) Module Set	
(4) 7660-502 DCE Cables included	7660-308C
(4) 7660-512 DTE Cables included	7660-308T
Four Port High-Bit Rate (HBR) (75 bps–24 Mbps) Module Set	
(4) 7660-524 DCE Cables included	7660-309C
(4) 7660-527 DTE Cables included	7660-309T
Eight Port Serial Mux Module Set (4) 7660-525 Cables Included	7660-340
Eight Port Serial De-Mux Module Set (4) 7660-526 Cables Included.	7660-341
Eight Port Serial Mux/De-Mux Module Set (8) Cables Included: (4) 7660-525 & (4) 7660-526	7660-740

Cell Bearing Service Sets	**Part No.**
High Speed Serial Interface Cell Bearing Interface (HSSI-CBI) Module Set, (1) 7660-523 SCSI-2 10ft Cable included	7660-320
Cell Bearing Interface (CBI) Module Set, (1) 7660-522 RS-530 5ft Cable Included	7660-327

Individual Modules

Each SAM and IOM is offered separately for sparing purposes.

ATM Service Sets	Part No.
Single Port ATM DS3 NIM/SAM	7660-018
Single Port ATM DS3 IOM	7660-419
High Speed (HSAM) (10–274.176 Mbps) Two Port SAM	7660-042
High Speed (HSAM) Two Port IOM	7660-442
HSAM Bridge	7660-443

TDM Service Modules	Part No.
DS3 Structured Single Port SAM	7660-672
DS3 Structured Single Port IOM	7660-416
DS3 Unstructured Three Port SAM	7660-034
E3 Unstructured Three Port SAM	7660-045
DS3/E3 Unstructured Three Port IOM	7660-409
DS1 (Structured & Unstructured) Eight Port SAM	7660-004
E1 (Structured & Unstructured) Eight Port SAM	7660-005
DS1/E1 Eight Port IOM	7660-404

Serial Service Modules	Part No.
Multi-Bit Rate (MBR) (75 bps–10 Mbps) Four Port SAM	7660-008
Multi-Bit Rate Four Port IOM	7660-408
High-Bit Rate (HBR) (75 bps–24 Mbps) Four Port SAM	7660-009

Serial Service Modules	Part No.
High-Bit Rate Four Port IOM	7660-418
Serial Mux (100 kbps–50 Mbps) Eight Port SAM	7660-040
Serial De-Mux (100 kbps–50 Mbps) Eight Port SAM	7660-041
Serial Mux/De-Mux Eight Port IOM	7660-408

Cell Bearing Service Modules	Part No.
Cell Bearing Interface (CBI) (56 kbps–16.384 Mbps) SAM	7660-017
Cell Bearing Interface IOM	7660-417
High Speed Serial Interface Cell Bearing Interface (HSSI-CBI) (56 kbps–52 Mbps) SAM	7660-020
High Speed Serial Interface Cell Bearing Interface IOM	7660-420

Individual Cables

MBR Cables	Part No.
MBR EIA-530 Cable, DB25 DCE 5ft	7660-502
MBR EIA-530 Cable, DB25 DTE 5ft	7660-512
MBR RS-232 Cable, DCE 5ft	7660-504
MBR RS-232 Cable, DTE 5ft	7660-514
MBR RS-449 Cable, DB37 DCE 5ft	7660-501
MBR RS-449 Cable, DB37 DTE 5ft	7660-511
MBR V.35 Cable, DCE 5ft	7660-503
MBR V.35 Cable, DTE 5ft	7660-513

Serial Cables	Part No.
Serial Mux RS-422A Cable Adapter, DB37 DTE 6 ft.	7660-525
Serial De-Mux RS-422A Cable Adapter, DB37 DCE 6 ft.	7660-526

Cell Bearing Interface Cables	Part No.
CBI RS-530 Cable, F-F, DCE 5ft	7660-522
CBI RS-530 Cable, M-M, DTE 5ft	7660-520
CBI RS-530 Cable Extender, M-F, 5ft	7660-521
HSSI-CBI RS-422/RJ48 Cable Adapter, DCE 6ft	7660-524
HSSI-CBI RS-422/RJ48 Cable Adapter, DTE 6ft	7660-527
HSSI-CBI SCSI-2 Cable, M-M, 10ft	7660-523

CHAPTER 3

Receipt of Product

In this Chapter

- Receipt ... *3-2*
- Unpacking ... *3-2*
- Inspection ... *3-3*

Receipt

All Broadmore components with FIPS 140-2 validated software are packaged and sealed at the factory with tamper-proof security tape.

Upon receipt, carefully examine the security sealing tapes on the shipping containers for any signs of tampering.

NOTE: Report any tampering to your security officer.

Inventory all material upon receipt to ensure that a complete shipment was received in accordance with the packing list.

NOTE: Report any damage sustained during shipment of equipment to the transporter immediately upon receipt.

Unpacking

The Broadmore 1700 chassis is shipped with the Alarm/Power module and fan tray installed. Additional modules ordered by the customer will normally be installed in the chassis prior to shipping. When requested, modules can be individually boxed, identified, and shipped separately in a second container.

WARNING! THE BROADMORE 1700 CHASSIS WEIGHS APPROXIMATELY 31 POUNDS WITHOUT MODULES INSTALLED. USE CARE IN REMOVING AND LIFTING THE CHASSIS FROM THE SHIPPING CONTAINER TO AVOID EQUIPMENT DAMAGE.

The chassis container will include an accessory kit with the following materials:

- users documentation on CD
- ground strap
- accessory cables
- miscellaneous hardware

Inspection

Perform a visual inspection of all components for obvious damage or irregularities. Pay special attention to the connectors, indicators, and switches on the individual circuit cards. Follow ESD procedures when removing cards from protective bags for this inspection. Carefully return the cards to their bags for storage until installation.

WARNING! SOME BROADMORE 1700 CIRCUIT BOARDS ARE ESD-SENSITIVE. THESE ASSEMBLIES ARE IN INDIVIDUAL STATIC DISSIPATIVE BAGS WITH AN ESD CAUTION LABEL ATTACHED. EMPLOY STANDARD ESD HANDLING PROCEDURES, INCLUDING USE OF A PROPERLY GROUNDED ESD WRIST STRAP BEFORE OPENING OR HANDLING THESE ITEMS. ONLY OPEN THESE BAGS AT AN APPROVED ESD WORKSTATION. CAREFULLY RETURN THE CARDS TO THEIR BAGS FOR STORAGE UNTIL INSTALLATION. FAILURE TO FOLLOW THESE PROCEDURES WILL VOID THE WARRANTY AND MAY RESULT IN COMPONENT DAMAGE.

Damage Reporting

Compare the contents of the shipping containers with the packing list provided. Immediately report any inconsistencies to Carrier Access at (800) 786-9929. The Customer Support Center will provide detailed instructions to resolve any issue or concern.

CHAPTER 4

Chassis Installation and Grounding

In this Chapter

- Precautions ... *4-2*
- Installation Factors ... *4-3*
- Rack Mounting ... *4-4*
- Chassis Grounding ... *4-7*
- AC Power Supply Tray ... *4-8*

Precautions

DANGER! EXERCISE NORMAL PRECAUTIONS FOR LIFTING HEAVY OBJECTS. USE TWO PEOPLE TO LIFT THE BROADMORE 1700. WHEN LIFTING, SUPPORT IT FROM THE BOTTOM AND TAKE CARE TO AVOID SHARP EDGES OR CORNERS.

WARNING! THE FRONT AND REAR COVERS OF THE CHASSIS MUST REMAIN IN PLACE DURING RACK-MOUNTING. THESE COVERS SHOULD ONLY BE REMOVED FOR MODULE INSTALLATION AND MAINTENANCE AS REQUIRED. THE COVERS SHOULD BE REPLACED AS SOON AS POSSIBLE AFTER SUCH TASKS ARE COMPLETED.

WARNING! THE BROADMORE 1700 MOUNTING BRACKETS SHALL BE CONNECTED TO THE CHASSIS USING ONLY THE SCREWS PROVIDED FOR INITIAL BRACKET INSTALLATION. THE INSTALLER SHALL BE RESPONSIBLE FOR PROVIDING A STABLE RACK THAT SUPPORTS THE WEIGHT OF THE BROADMORE 1700 AS INSTALLED.

WARNING! THE INSTALLER SHALL BE RESPONSIBLE FOR PROVIDING A PROPER CHASSIS GROUND CONNECTION.

Installation Factors

The Broadmore 1700 is designed and delivered ready for installation in a standard EIA 19" equipment rack. The mounting brackets can be repositioned for installation in a 23" rack. To ensure a smooth installation that meets all requirements, the following installation factors should be considered.

Factor	Consideration
Access Clearance	Broadmore 1700 requires the following access clearances for service and maintenance: • Front: 24″ • Rear: 4″ • Top: 1.75″ (Broadmore 1700 units may be stacked without this clearance.)
Power Source	DC Power: –48 VDC at 5 amperes is connected to the rear of the Broadmore chassis. A second –48 VDC source may be connected for redundancy. Optional AC Power Supply Tray: Converts 110/240 VAC (50-60 Hz) to –48 VDC. Rack mounted tray (3.5″ high) can hold two power supplies. Unit comes with one power supply and DC cable. A second power supply with cable can be added to provide redundant DC power to the Broadmore. AC and DC power connections are at the rear of the unit. The DC cable is approximately 3.5 ft. long.
Cable Management	Overall cable lengths can be optimized by equipment placement. • User equipment interface cable connections are made to the input/output modules at the rear of Broadmore chassis. • ATM interface connections is via fiber cable to the network interface modules at the front of Broadmore chassis. • Access and cable strain-relief tie-down points are provided. • Chassis front and rear cover removal is required.

Rack Mounting

- Tools ... *4-4*
- Mounting Brackets ... *4-5*
- Rack Mounting Procedure ... *4-6*

Tools

The following tools are recommended to install, configure, operate, and maintain the Broadmore 1700:

- #1 flathead screwdriver (for cover removal/installation)
- ¼-inch hex driver
- Rack-mounting hardware with compatible tools
- Other standard electronic installation tools as preferred

Mounting Brackets

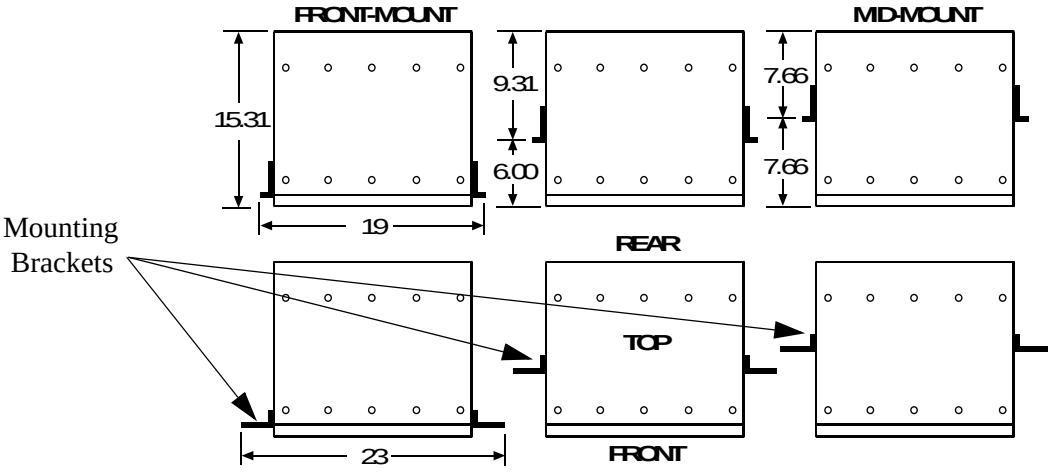
The Broadmore 1700 chassis 17.5" high and is shipped from the factory assembled for front-mounting in a standard 19" EIA or relay rack.

WARNING! THE BROADMORE 1700 MOUNTING BRACKETS SHALL BE CONNECTED TO THE CHASSIS USING ONLY THE SCREWS PROVIDED FOR INITIAL BRACKET INSTALLATION. THE INSTALLER SHALL BE RESPONSIBLE FOR PROVIDING A STABLE RACK THAT SUPPORTS THE WEIGHT OF THE BROADMORE 1700 AS INSTALLED.

The mounting brackets can be moved to accommodate 23" racks and other mounting positions. To install the chassis in 23" racks, remove the mounting brackets, rotate them 90 degrees, and reattach them to the chassis.

If desired, alternate mounting holes are provided for moving the mounting brackets forward or backward for other mounting configurations.

The six possible rack-mounting bracket configurations are shown below.



Rack Mounting Procedure

Rack-mounting the Broadmore 1700 chassis requires:

- two technicians
- clear access to front and rear of rack
- user-provided rack mounting hardware (screws)

DANGER! **EXERCISE NORMAL PRECAUTIONS FOR LIFTING HEAVY OBJECTS. USE TWO PEOPLE TO LIFT THE BROADMORE 1700. WHEN LIFTING, SUPPORT IT FROM THE BOTTOM AND TAKE CARE TO AVOID SHARP EDGES OR CORNERS.**

WARNING! **THE FRONT AND REAR COVERS OF THE CHASSIS MUST REMAIN ATTACHED TO PREVENT THE CHASSIS FROM BENDING DURING THE RACK-MOUNTING PROCEDURE.**

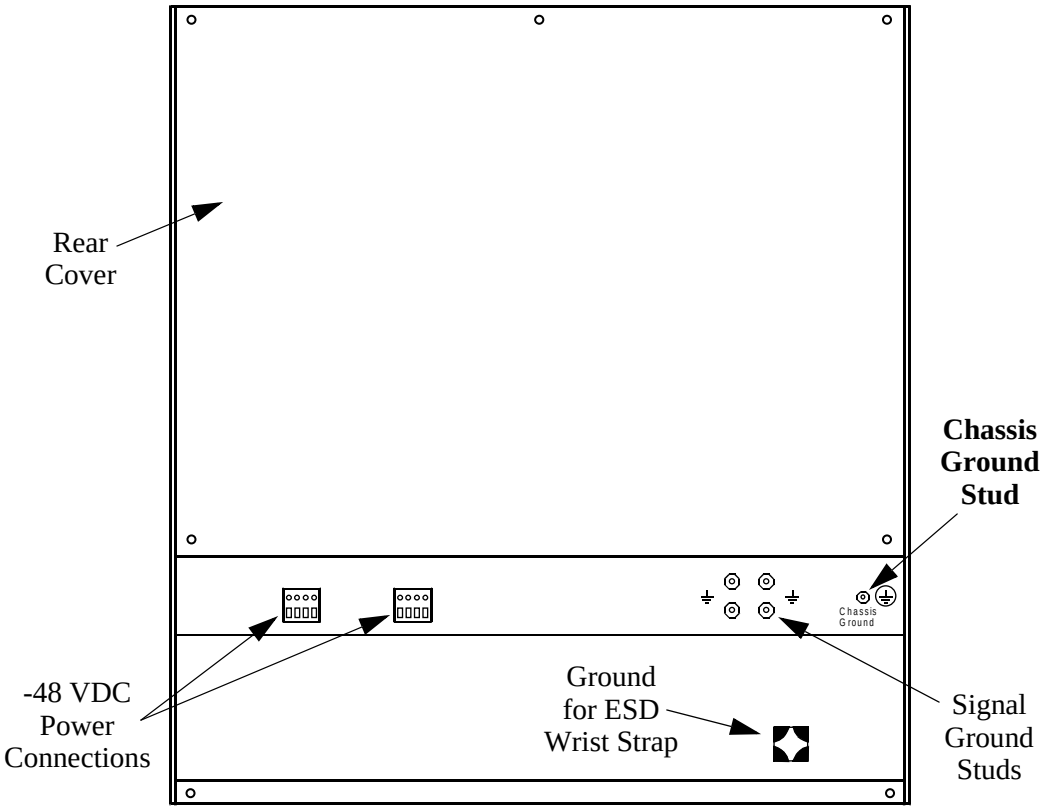
Perform the following steps:

1. Ensure that the front and rear chassis covers are firmly attached to provide mechanical support and prevent accidental damage.
2. Carefully lift the Broadmore 1700 and position it in the desired rack location. One technician should support the rear and the second should support the front.
3. Ensure that there is a 1.75 inch space above the chassis for adequate air flow.
4. Insert screws through the chassis and rack mounting holes. Every bracket hole must have a screw securely installed to ensure proper weight distribution and support.

Chassis Grounding

The Broadmore 1700 chassis has a chassis ground stud on the rear panel for connecting a ground wire to the equipment rack building ground.

WARNING! **THE INSTALLER SHALL BE RESPONSIBLE FOR PROVIDING A PROPER CHASSIS GROUND CONNECTION. CONNECT BUILDING GROUND TO THE CHASSIS GROUND STUD. DO NOT USE THE SIGNAL GROUNDING STUDS FOR THE BUILDING GROUND CONNECTION.**

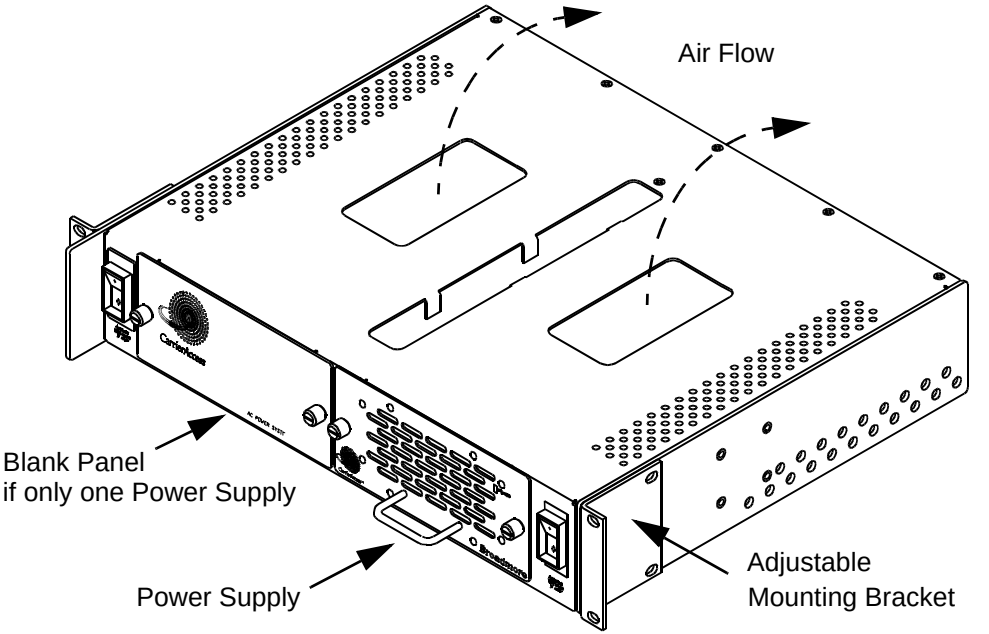


Perform the following steps:

1. Attach the ground wire supplied with the Broadmore chassis to the 6-32 chassis grounding stud located on the right rear of the chassis below the rear cover.
2. Attach the other end of the ground wire to the equipment rack building ground.

AC Power Supply Tray

The optional AC Power Supply Tray (7665-17PS) is a 3.5-inch high rack mounted unit that converts voltage from 110/220 VAC (50-60 Hz) to –48 VDC. The unit comes with one 240W AC Power Supply (7660-115), one AC power cord, and one 3.5 ft. DC cable for connecting to the Broadmore's –48 VDC power input. A second power supply with cables can be used to provide redundant DC power to the Broadmore chassis.



Rack-mounting the AC Power Supply Tray requires:

- clear access to front of rack
- user-provided rack mounting hardware (screws)

NOTE: No additional air space is needed above the AC Power Supply Tray when it is mounted directly below the Broadmore 1700 fan tray, which has a sloped bottom that does not interfere with air flow. Otherwise, provide at least 1.75 inch (1 RU) of free air space above the AC Power Supply Tray to ensure proper ventilation.

Perform the following steps:

1. Unpack and visually inspect the AC Power Supply Tray assembly.
2. For ease of rack installation, you can remove the power supply module(s) from the tray to make it lighter.
3. Adjust the rack mounting brackets for desired for a 19 or 23 inch rack and for front/rear mounting configuration. As shipped, the brackets will fit a 19” rack with front-mount configuration. The brackets can be removed and rotated 90 degrees to fit a 23” rack.
4. Rack mount the tray directly below the Broadmore 1700 using facility-provided mounting hardware.
5. Replace the power supply module(s) after rack-mounting the tray and tighten the module’s front panel screws.
6. If the tray came with only one power supply module and a redundant power supply is to be installed, remove the blank cover on the front of the tray, insert the second power supply, and tighten the front panel screws.

CHAPTER 5

Module and Fan Installation

In this Chapter

- Precautions ... *5-2*
- Module Installation Procedures ... *5-3*
- Fan Tray Installation Procedure ... *5-15*

Precautions

The Broadmore 1700 chassis is normally shipped with modules and fan tray installed at the factory. If they have been shipped separately, observe the following precautions when unpacking, handling, and installing these assemblies.

WARNING! **THE FRONT AND REAR COVERS OF THE CHASSIS SHOULD ONLY BE REMOVED FOR MODULE INSTALLATION AND MAINTENANCE AS REQUIRED. THE COVERS SHOULD BE REPLACED AS SOON AS POSSIBLE AFTER SUCH TASKS ARE COMPLETED.**

WARNING! **THE BROADMORE CONTAINS CIRCUIT CARDS AND COMPONENTS THAT ARE SUBJECT TO DAMAGE BY ELECTROSTATIC DISCHARGE (ESD). DO NOT REMOVE A COMPONENT FROM ITS PROTECTIVE PACKAGING UNTIL READY TO INSTALL IT. WEAR A WRIST GROUNDING STRAP AND ATTACH IT TO AN ESD CONNECTOR OR A METAL PART OF THE SYSTEM UNIT BEFORE HANDLING COMPONENTS. IF A WRIST STRAP IS NOT AVAILABLE, MAINTAIN CONTACT WITH THE SYSTEM UNIT THROUGHOUT ANY PROCEDURE REQUIRING ESD PROTECTION.**

NOTE: After installation is complete, ensure that blank panels cover all empty module slots to provide proper cooling when the fan is turned on.

Module Installation Procedures

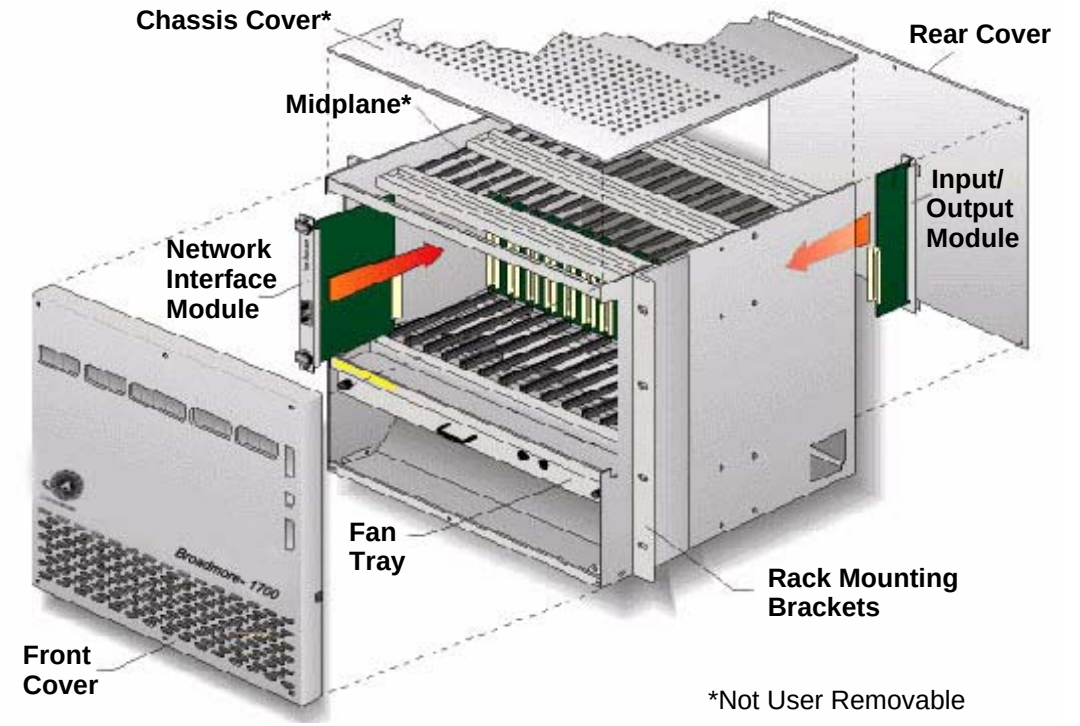
- Overview ... *5-4*
- Tools ... *5-5*
- Remove Chassis Covers ... *5-5*
- Module Locations ... *5-6*
- Installation Sequence ... *5-8*
- NIM Installation ... *5-9*
- SAM Installation ... *5-10*
- SAM Installation ... *5-10*
- CPU Installation ... *5-10*
- APM Installation ... *5-11*
- NIM IOM Installation ... *5-11*
- SAM IOM Installation ... *5-12*
- CPU IOM Installation ... *5-13*
- APM IOM Installation ... *5-13*
- Replace Chassis Covers ... *5-14*

Overview

The Broadmore 1700 chassis is normally shipped with modules installed at the factory. If they have been shipped separately, perform the following procedures to configure and install the modules.

NOTE: Modules with jumpers are normally configured during installation to meet user requirements. If the chassis is shipped with modules installed, the modules may need to be removed, configured, and reinstalled.

The Broadmore 1700 system architecture is based upon a midplane design allowing modules to be installed from the front and rear of the chassis, as shown below.



Tools

The following tools are recommended to install, configure, operate, and maintain the Broadmore 1700:

- #1 flathead screwdriver (for cover removal/installation)
- Dual jeweler's flathead/Phillips screwdriver
- ESD wrist strap
- Other standard installation tools as desired.

Remove Chassis Covers

1. Loosen captive screws securing the front and rear covers and set the covers aside.
2. Connect an ESD wrist strap to the front or rear ESD connector and follow standard ESD procedures while handling unit components.

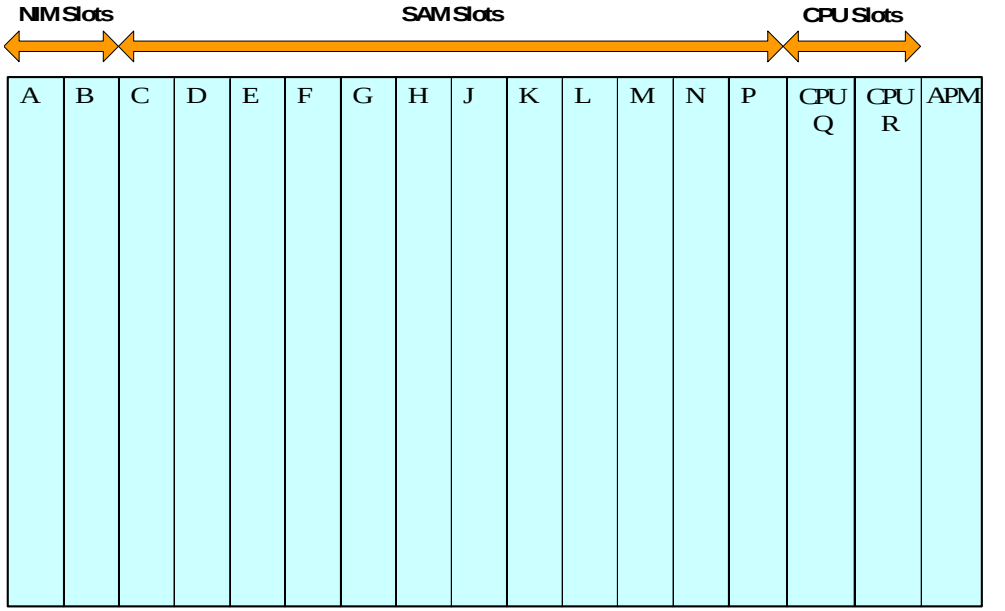
WARNING! USE ESD PRECAUTIONS: WEAR AN ESD GROUNDING STRAP WHILE HANDLING ANY MODULES OR ACCESSING THE INSIDE OF THE BROADMORE 1700. FAILURE TO FOLLOW ESD PROCEDURES MAY DAMAGE SENSITIVE COMPONENTS AND VOID THE WARRANTY.

Module Locations

Obtain the office records showing the module slot installation locations for this chassis. The network design engineer will assign module slots to ensure proper system operation. (For detailed system design and configuration information, see *"System Planning Factors" on page 2-16*.)

The Broadmore 1700 chassis is designed for modules to go into specific slots to support various user requirements and cell bus bandwidths. As a minimum requirement, the module slot installations must conform to the guidelines on the following page.

The Broadmore 1700 system architecture is based upon a midplane design allowing modules to be installed from the front and rear of the chassis. There are 17 vertical slots as viewed from the front with the cover removed, as shown below. Input/Output Module (IOM) slots on the rear panel are numbered in the reverse order, so that they align with the module slots on the front panel. Each NIM, SAM, CPU, and APM requires a matching IOM installed in the rear.



CAUTION! **WHEN USING REDUNDANT OPTICAL NIMS, BOTH NIMS MUST BE IDENTICAL. DO NOT MIX OC-3C AND OC-12C IN THE SAME CHASSIS.**

Module Type	Description	Available Slot(s)
CPU	CPU	Q
	Redundant CPU	R
NIM	OC-3c/STM-1c or OC-12c/STM-4c	B
	Redundant NIM (OC-3c or OC-12c)	A
	ATM DS3	B
	Redundant ATM DS3	A
	CBI (see note below)	B
	HSSI-CBI (see note below)	B
SAM	DS1 N×64	C-P
	E1 N×64	C-P
	CBI (see note below)	C-P
	HSSI-CBI (see note below)	C-P
	MBR	C-P
	HBR	C-P
	HSAM (double width)	D-E
	Serial Multiplexer	C-P
	Serial Demultiplexer	C-P
	Unstructured DS3	C,E,G,J
	Structured DS3 (up to 4 modules))	C-P
	Unstructured E3	C,E,G,J

NOTE: CBI and HSSI-CBI modules can be installed in both a NIM slot and several SAM slots, functioning together in the same chassis. The Broadmore 1700 chassis will only support one CBI or HSSI-CBI NIM, which must be installed in NIM slot B (there is no NIM redundancy support). Up to twelve CBI and HSSI-CBI SAMs can be installed when operated within the bandwidth limitation of the NIM. CBI and HSSI-CBI SAMs should be installed starting with slot C and work upwards.

Installation Sequence

The Broadmore 1700 is shipped in a minimum usable configuration. Unused slots are covered with blank panels except the slots for a single NIM, SAM, or CPU.

NOTE: After installation is complete, ensure that blank panels cover all empty module slots to provide proper cooling when the fan is turned on.

NIM, SAM, APM, and CPU cards are inserted from the front. IOM cards are inserted from the rear. All cards align in card guides for ease of installation and removal. Each card will have two retaining screws to hold it in place, one at the top and one at the bottom. These screws must not be used for seating or unseating the cards. Each card also has a set of ejectors, one on top and one at the bottom. These ejectors are used to unseat the card from the midplane connector for card removal.

NOTE: Modules with jumpers are normally configured before installing the modules in the chassis. However, modules are hot-swappable and may be removed at any time to verify or reconfigure the jumpers, then reinstalled. The following procedures include instructions for setting the jumpers.

Install modules in the following order:

- NIM (1 or 2)
- SAM (1 or more)
- CPU (1 or 2)
- NIM IOM (1 for each NIM)
- SAM IOM (1 for each SAM)
- CPU IOM (1 for each CPU)

NOTE: The APM and APM IOM are factory installed at the factory.

NIM Installation

1. If a Building Integrated Timing Supply (BITS) clock will be used, an impedance matching adjustment may be required before installing an OC-3, OC-12, or ATM DS3 NIMs. BITS impedance matching jumpers are located on the NIM component side below the midplane connectors. They are labeled **JMP1**, **JMP2**, **JMP3**, and **JMP4**. The following settings are available.

JMP3, JMP4 in (default)	100 ohms
JMP1, JMP2, JMP3, JMP4 in	75 ohms
All jumpers out	No termination

   BITS input is provided to the RJ48C BITS connector on the corresponding NIM IOM (see *"BITS Interface Connections" on page 6-7*).

NOTE: OC-3 or OC-12 or ATM DS3 NIMs go in slots A and B. (Both optical NIMs must be identical.) The NIM in slot B is the Working unit and the NIM in slot A is the Protection unit. When a CBI or HSSI is used as a NIM, it goes in slot B and slot A remains empty because redundancy is not supported.

2. Place the Working NIM in slot B from the front so that it slides smoothly in the top and bottom card guides and the card connector aligns with the midplane connector.

3. Firmly press the NIM into the chassis until the connectors seat against each other completely. Use pressure simultaneously at the top and bottom of the NIM to ensure a proper fit to the midplane.
4. Secure the screws on the top and bottom of the module, being careful not to over-tighten.
5. For redundant NIMs, repeat this process to install the Protection NIM in slot A.

SAM Installation

1. Place a SAM in the desired slot (C through P) from the front of the chassis so that it slides smoothly in the top and bottom card guides and the card connector aligns with the midplane connector.
   Note: The HSAM is a double-width module that is installed in slots D and E.
2. Firmly press the SAM into the chassis until the connectors seat against each other completely. Use pressure simultaneously at the top and bottom of the SAM to ensure a proper fit to the midplane.
3. Secure the screws on the top and bottom of the module, being careful not to over-tighten.
4. Repeat this process to install the other SAMs as desired.

CPU Installation

1. Place the CPU card in slot Q from the front of the chassis so that it slides smoothly in the top and bottom card guides and the card connector aligns with the midplane connector.
2. Firmly press the CPU card into the chassis until the connectors seat against each other completely. Use pressure simultaneously at the top and bottom of the CPU to ensure a proper fit to the midplane.
3. Secure the screws on the top and bottom of the card, being careful not to over-tighten.
4. In a similar fashion, install the second CPU in slot R

APM Installation

The APM is shipped factory installed in the right-most slot, labeled APM, which is to the right of slot S.

NIM IOM Installation

1. From the chassis rear, place the NIM IOM in slot B so that it slides easily in the top and bottom card guides with the connector aligned to the midplane connector.
2. Visually verify that the NIM IOM physically aligns with the NIM installed above.
3. Firmly press the NIM IOM into the chassis until the connectors completely seat.
4. Secure the retaining screws on the top and bottom of the module, being careful not to over-tighten.
5. Repeat this process for the NIM IOM in slot A.

HSAM Bridge Installation

The HSAM Bridge is a small double-width card designed to bridge two cellbus connectors on the midplane. This card must be installed before installing the HSAM IOM.

1. From the chassis rear, place the HSAM Bridge card across the top two midplane connectors in slots D and E.
2. Firmly press the HSAM Bridge to seat the connectors. (The bridge card will be held in place by the HSAM IOM.)

SAM IOM Installation

1. Before installing a CBI SAM IOM or HSSI-CBI SAM IOM, ensure that the jumpers on the component side of the cards are configured for the desired Resync output signal level. ("Closed" means that jumper is installed over both pins; "Open" means that jumper is installed over just one pin.)

The Resync output is configured by setting the jumpers on the card to select +5V (default), –5V, or contact closure only.

Active State (pin 1/2)	J8	J9	J10	J11
Normal (+5V)	Closed	Open	Open	Closed
Invert (–5V)	Open	Closed	Closed	Open
Contact Closure	Open	Open	Open	Open

2. From the chassis rear, place each SAM IOM so that it slides easily in the top and bottom card guides with the connector aligned to the midplane connector for the slot matching the corresponding SAM.
3. Visually verify that the SAM IOM physically aligns with the SAM installed above.
4. Firmly press the SAM IOM into the chassis until the connectors completely seat.
5. Secure the retaining screws on the top and bottom of the module, being careful not to over-tighten.
6. Repeat this process for each slot with a corresponding SAM installed.

CPU IOM Installation

1. Before installing the CPU IOM, an adjustment may be needed to configure the remote shutdown operation. There are two jumpers on each CPU IOM module labeled JMP1 and JMP2. These jumpers control how the remote shutdown contacts work. As installed at the factory, jumpers JMP1 and JMP2 connect pins 1-2 to pins 3-4 so that shorting either pair will reboot both CPUs. With both jumpers removed, shorting pins 1-2 will reboot the other-slot CPU; shorting pins 3-4 will reboot the same-slot CPU. For a redundant unit with two CPUs, it is recommended that both jumpers be removed so that the CPUs can be rebooted individually without disrupting system operation.
2. Visually verify that each CPU IOM physically aligns with its respective CPU installed above, slots Q and R respectfully.
3. Firmly press each CPU IOM into the chassis until the connectors completely seat.
4. Secure the retaining screws on the top and bottom of each module, being careful not to over-tighten

APM IOM Installation

The APM IOM is installed at the factory. This power module has fuses for each power source, as well as fuse holders for two spares.

Replace Chassis Covers

WARNING! **THE FRONT AND REAR COVERS OF THE CHASSIS SHOULD ONLY BE REMOVED FOR MODULE INSTALLATION AND MAINTENANCE AS REQUIRED. THE COVERS SHOULD BE REPLACED AS SOON AS POSSIBLE AFTER SUCH TASKS ARE COMPLETED.**

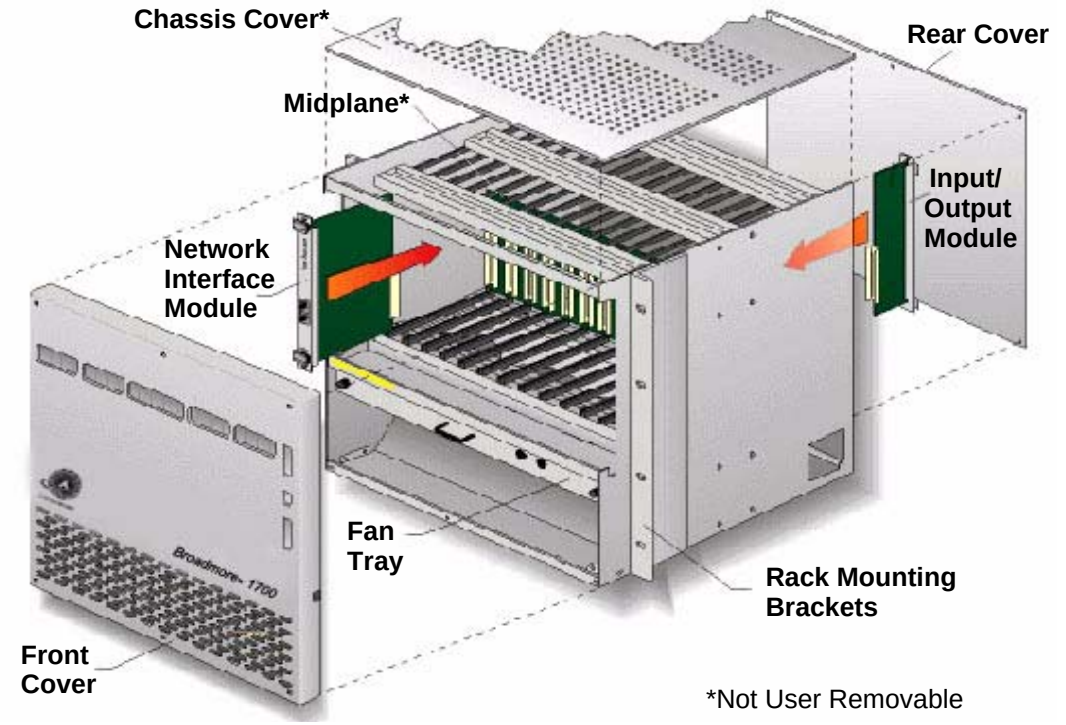
NOTE: After installation is complete, ensure that blank panels cover all empty module slots to provide proper cooling when the fan is turned on.

This completes module installation. Replace the front and rear covers unless additional installation or maintenance procedures are to be performed at this time.

Fan Tray Installation Procedure

- Tools ... *5-16*
- Remove Front Chassis Cover ... *5-16*
- Fan Tray Installation ... *5-16*
- Replace Chassis Cover ... *5-17*

The fan tray is normally shipped installed in the front of the chassis below the modules, as shown below. If it is not installed, follow the procedure below to install it.



Tools

The following tools are recommended to install, configure, operate, and maintain the Broadmore 1700:

- #1 flathead screwdriver (for cover and fan tray removal/installation)
- Dual jeweler's flathead/Phillips screwdriver
- ESD wrist strap
- Other standard installation tools as desired.

Remove Front Chassis Cover

Only the front cover must be removed to install the fan tray.

1. Loosen captive screws securing the front cover and set the cover aside.
2. Connect an ESD wrist strap to the front or rear ESD connector and follow standard ESD procedures while handling unit components.

WARNING! USE ESD PRECAUTIONS: WEAR AN ESD GROUNDING STRAP WHILE HANDLING ANY MODULES OR ACCESSING THE INSIDE OF THE BROADMORE 1700. FAILURE TO FOLLOW ESD PROCEDURES MAY DAMAGE SENSITIVE COMPONENTS AND VOID THE WARRANTY.

Fan Tray Installation

1. The fan tray installs from the front, directly below the modules. Fan tray guides are provided on each side of the chassis. Slide the fan tray into the guides and firmly push it into place so that connectors totally seat with the midplane.
2. Tighten the two front panel retaining screws using a flathead screw driver.

Replace Chassis Cover

WARNING! THE FRONT AND REAR COVERS OF THE CHASSIS SHOULD ONLY BE REMOVED FOR MODULE INSTALLATION AND MAINTENANCE AS REQUIRED. THE COVERS SHOULD BE REPLACED AS SOON AS POSSIBLE AFTER SUCH TASKS ARE COMPLETED.

This completes fan tray installation. Replace the front and rear covers unless additional installation or maintenance procedures are to be performed at this time.

CHAPTER 6

Electrical Installation

In this Chapter

- Precautions ... *6-2*
- Electrical Requirements ... *6-3*
- Cabling and Compliance Requirements ... *6-4*
- Alarm Port Connections ... *6-5*
- Optical Interface Connections ... *6-6*
- BITS Interface Connections ... *6-7*
- NIM/SAM IOM Connections ... *6-8*
- CPU IOM Connections ... *6-20*
- Power Supply Connections ... *6-21*
- Software ... *6-24*

Precautions

WARNING! THE BROADMORE 1700 IS INTENDED FOR INDOOR INSTALLATION ONLY. A PROPER CHASSIS GROUND CONNECTION IS REQUIRED. ITS ELECTRICAL COMMUNICATIONS INTERFACES SHALL NOT BE CONNECTED TO WIRING SYSTEMS THAT LEAVE THE BUILDING UNLESS APPROPRIATE INTERFACE DEVICES ARE USED. THE INSTALLER SHALL BE RESPONSIBLE FOR PROVIDING ADEQUATE LIGHTNING OR SURGE PROTECTION FOR WIRING THAT LEAVES THE BUILDING. THE INSTALLER SHALL BE RESPONSIBLE FOR PROVIDING APPROVED INTERFACE DEVICES IF CONNECTIONS ARE MADE TO PUBLIC COMMUNICATIONS NETWORKS.

WARNING! THE FRONT AND REAR COVERS OF THE CHASSIS SHOULD ONLY BE REMOVED FOR MODULE INSTALLATION AND MAINTENANCE AS REQUIRED. THE COVERS SHOULD BE REPLACED AS SOON AS POSSIBLE AFTER SUCH TASKS ARE COMPLETED.

WARNING! THE BROADMORE CONTAINS CIRCUIT CARDS AND COMPONENTS THAT ARE SUBJECT TO DAMAGE BY ELECTROSTATIC DISCHARGE (ESD). WEAR A WRIST GROUNDING STRAP AND ATTACH IT TO AN ESD CONNECTOR OR A METAL PART OF THE SYSTEM UNIT BEFORE HANDLING COMPONENTS. IF A WRIST STRAP IS NOT AVAILABLE, MAINTAIN CONTACT WITH THE SYSTEM UNIT THROUGHOUT ANY PROCEDURE REQUIRING ESD PROTECTION.

NOTE: After installation is complete, ensure that blank panels cover all empty module slots to provide proper cooling when the fan is turned on.

Electrical Requirements

- Tools ... *6-3*
- Power ... *6-3*
- Cable Management ... *6-3*

Tools

The following tools are recommended to install, configure, operate, and maintain the Broadmore 1700:

- #1 flathead screwdriver (for cover removal/installation)
- ¼-inch hex driver
- Dual jeweler's flathead/Phillips screwdriver
- PC with VT100 Emulation software program
- DC Volt-Ohm Meter (VOM)
- ESD wrist strap
- Other standard electronic installation tools as preferred

Power

DC power connections are made at the rear of the Broadmore chassis. The primary DC power source is –48 volts at 5 amperes and is connected to the "A" inputs. For redundancy, a second –48 VDC source may be connected to the "B" inputs.

For AC power applications, the optional dual AC redundant power supply tray is usually mounted below the Broadmore and requires 110/240 VAC, 50/60 Hz. AC power is connected at the rear of the power supply tray, and the two –48 VDC outputs are then wired to the DC power inputs on the Broadmore chassis.

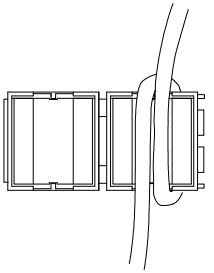
Cable Management

Overall cable lengths can be optimized through equipment location. Access and cable strain-relief tie-down points are provided.

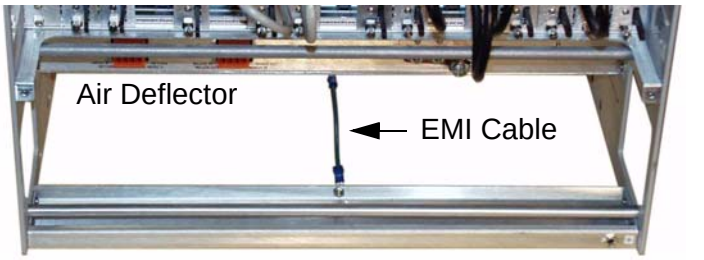
Cabling and Compliance Requirements

For FCC Part 15 Class A compliance for EMI/RFI suppression, the Broadmore 1700 requires that a ferrite bead (P/N 010-0051) must be attached to each DC power cable, Ethernet cable, HSAM cables, HBR cable, and alarm cable. FCC compliance also requires that an EMI cable be attached between the chassis and air deflector. Accessory kits are included with the Broadmore chassis and each HBR card. Use the following guidelines for FCC compliance.

- DC power, one or two cables. Attach one ferrite bead to each cable, using a single wrap so that the cable passes through the bead twice (see figure below left).
- Ethernet, one or two cables. Attach one ferrite bead to each cable, using a single wrap so that the cable passes through the bead twice.
- Alarm cable. Attach one ferrite bead to the cable, using a single wrap so that the cable passes through the bead twice.
- EMI cable. The EMI cable must be attached vertically across the bottom rear opening (see figure below right). Remove the existing screw at the top center of the air deflector at the rear of the unit. Discard the existing flat washer. Using the existing screw, attach the ring terminal of the EMI cable to the air deflector. Attach the ground clip to the other end of the EMI cable. Attach the ground clip with the EMI cable to the top center of the rear cable tray support channel.
- HSAM IOM, slots D and E. Attach one ferrite bead on each DTE and DCE cable bundle, using a single wrap so that the cable passes through the bead twice.
- HBR SAM IOM (P/N 7660-418), up to four cables. Each HBR accessory kit has 4 ferrite beads. Attach one ferrite bead to each RJ45 cable, using a single wrap so that the cable passes through the bead twice.



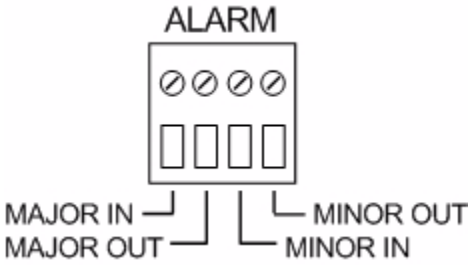
Ferrite Bead



Cable Tray

Alarm Port Connections

Alarms are sent to the control console and the system log. LEDs display alarm conditions. Additionally, the Broadmore 1700 has an alarm port on the chassis rear for connection to the user's remote indicators. The alarm port is a four-wire terminal block providing form "C" relay contact closure signals. Two wires are labeled "Major" (in and out) and two are labeled "Minor" (in and out) as shown below. The alarm connector on the lower back of the chassis is shown below. The connector is a compression type in which the wire is inserted in the lower opening and the compression screw above is tightened to secure the wire. A small flathead screwdriver is required to secure the wires.



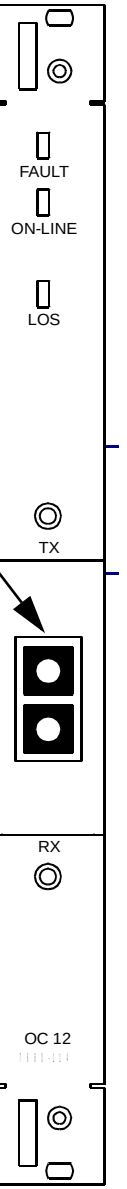
1. Run the alarm cables to the connection point on the chassis rear as shown above.
2. Insert the cable wires and tighten the compression screws to secure the wires.

Optical Interface Connections

The Broadmore 1700 interfaces to an ATM network via fiber optic cable using SC connectors on the front panel of the OC-12c and OC-3c NIMs.

1. Route the cable to the front of the chassis.
2. Route the cable into the chassis via the small square opening on the lower-right, front cover of the chassis.
3. Connect the cables to the labeled TX and RX connectors on the front of the NIM. (See example at right.)
4. Use the plastic cable guides and cable protector shipped with the Broadmore 1700 to secure the cable.

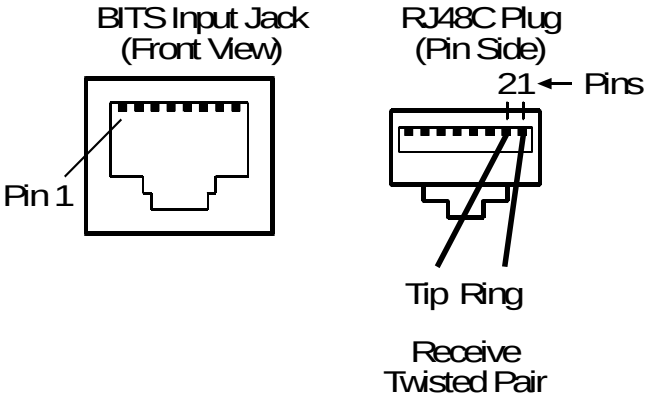
WARNING! UNTERMINATED OPTICAL CONNECTIONS MAY EMIT LASER RADIATION. DO NOT VIEW BEAM WITH OPTICAL INSTRUMENT.

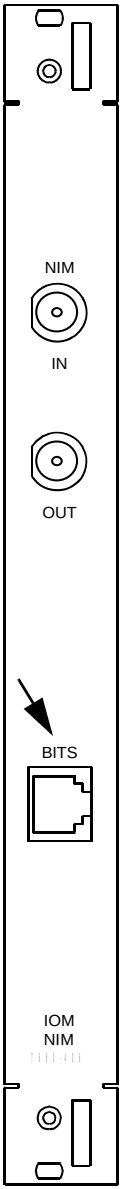


BITS Interface Connections

NIM IOMs include a Building Integrated Timing Supply (BITS) input connector for network timing. (The BITS input on an ATM DS3, CBI, or HSSI IOM is only used when a the corresponding NIM/SAM is installed as a NIM. If required, the BITS impedance jumpers on the OC-3, OC-12, and ATM DS3 NIMs are normally adjusted during *"NIM Installation" on page 5-9*.)

1. Use cable rated for Category 3 (CAT3) or better.
2. Route the cable to the lower-left, rear of the chassis.
3. Connect the cable to the RJ48C BITS connector on the rear of the chassis. (See example at right.)
4. Use the plastic cable guides and cable protector shipped with the Broadmore 1700 to secure the cable.





NIM/SAM IOM Connections

- General Instructions ... *6-8*
- ATM DS3 IOM Connections ... *6-9*
- Unstructured DS3-3/E3-3 IOM Connections ... *6-10*
- Structured DS3 IOM Connections ... *6-12*
- DS1/E1 IOM Connections ... *6-13*
- Multi Bit Rate IOM Connections ... *6-14*
- High Bit Rate IOM Connections ... *6-15*
- High Speed SAM IOM Connections ... *6-16*
- Serial Mux/Demux IOM Connections ... *6-17*
- Cell Bearing Interface (CBI) IOM Connections ... *6-18*
- High Speed Cell Bearing Interface IOM Connections ... *6-19*

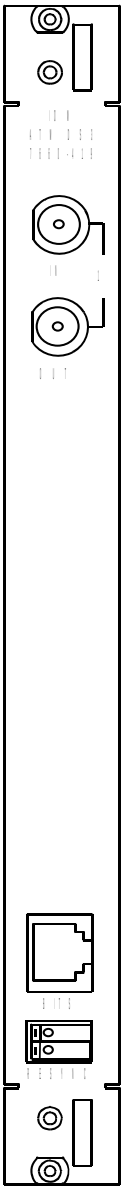
General Instructions

Network equipment (excluding fiber optic) and user equipment connect to the Broadmore 1700 via cables routed to connectors on the back of each IOM. The physical interface varies by type of IOM (see *"Module Descriptions" on page 1-11*). Adapter cables are available for most serial interface SAMs (see *"Cable Specifications" on page E-1*).

1. Route the equipment cables to the chassis rear.
2. Install the plastic edge protector around both rear access square openings. Note the chassis internal cable tray. Two bars are provided as anchor points for cable ties. This will serve as cable strain relief.
3. Route the cables into the chassis via the square openings on either side of the rear cable tray.
4. Connect the cables to the IOM connectors as labeled for each port and dress out the cables. Use the tray and bars to secure the cable.

NOTE: After completing the hardware installation, visually inspect all modules and connectors. Replace the front and rear covers, if not already in place.

ATM DS3 IOM Connections



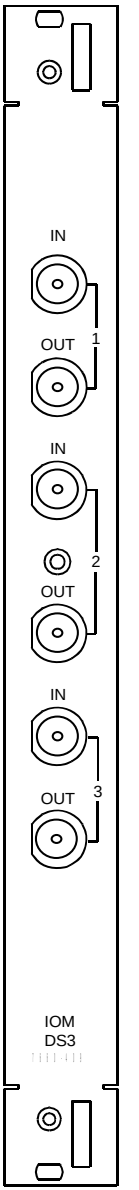
Each Unstructured ATM DS3 IOM provides one pair of BNC coaxial connectors for RG-59, 75 ohm cable.

1. Connect receiver RX input to the IN port.
2. Connect transmitter TX output to the OUT port.
3. If this card being used as a NIM IOM and a BITS input is desired, see *"BITS Interface Connections" on page 6-7*.

Unstructured DS3-3/E3-3 IOM Connections

Each Unstructured DS3 IOM provides three pairs of BNC coaxial connectors (labeled 1 to 3) for RG-59, 75 ohm cable.

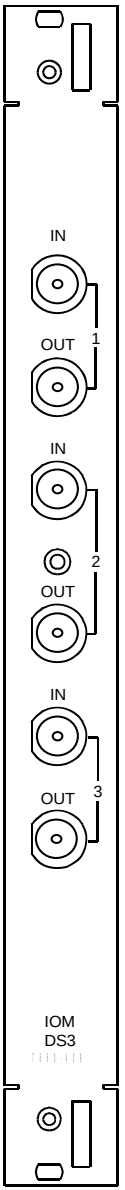
1. Connect receiver RX inputs to the IN ports.
2. Connect transmitter TX outputs to the OUT ports.



Unstructured E3-3 IOM Connections

Each Unstructured E3-3 IOM provides three pairs of BNC coaxial connectors (labeled 1 to 3) for RG-59, 75 ohm cable.

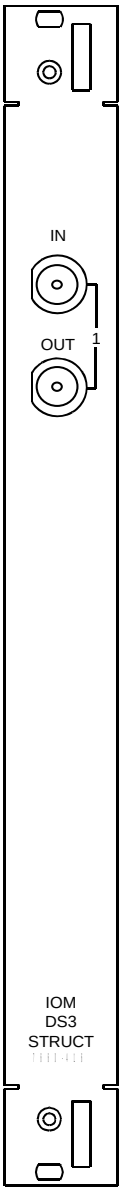
1. Connect receiver RX inputs to the IN ports.
2. Connect transmitter TX outputs to the OUT ports.



Structured DS3 IOM Connections

Each Structured DS3 IOM provides one pair of BNC coaxial connectors for RG-59, 75 ohm cable.

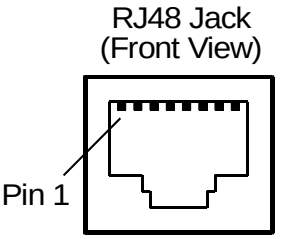
1. Connect receiver RX input to the IN port.
2. Connect transmitter TX output to the OUT port.

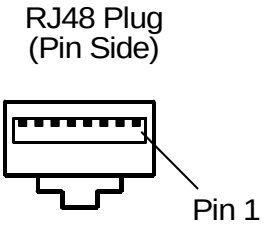


DS1/E1 IOM Connections

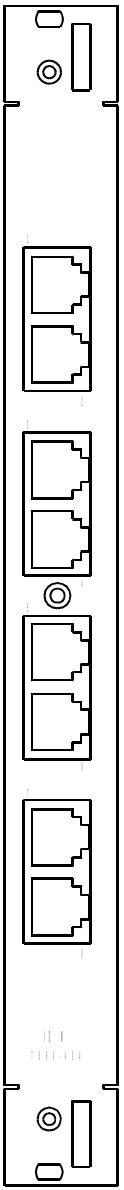
This IOM is used with the DS1 and E1 SAMs and provides eight RJ48 connectors (labeled 1 to 8).

1. Connect one end of RJ48 cable to desired port.
2. Connect other end of cable to user equipment.





Pin 1	RX Ring
2	RX Tip
3	n/c
4	TX Ring
5	RX Tip
6	n/c
7	n/c
8	n/c



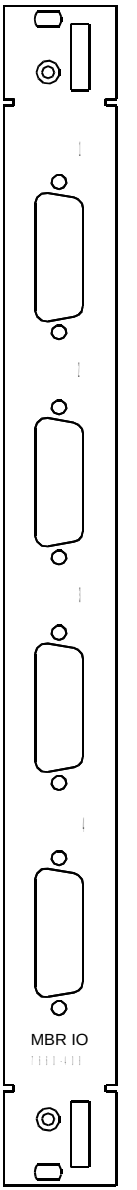
Multi Bit Rate IOM Connections

The IOM provides four serial interfaces (labeled 1 to 4) that can be configured as either DCE or DTE, synchronous or asynchronous.

The following cable adapters are available:

- 7660-502 – MBR EIA-530 Cable, DB25 DCE 5ft
- 7660-512 – MBR EIA-530 Cable, DB25 DTE 5ft
- 7660-504 – MBR RS-232 Cable, DCE 5ft
- 7660-514 – MBR RS-232 Cable, DTE 5ft
- 7660-501 – MBR RS-449 Cable, DB37 DCE 5ft
- 7660-511 – MBR RS-449 Cable, DB37 DTE 5ft
- 7660-503 – MBR V.35 Cable, DCE 5ft
- 7660-513 – MBR V.35 Cable, DTE 5ft

1. Connect the HDB26 (high density) cable connector to the desired IOM port.
2. Connect other end of cable to the user equipment.

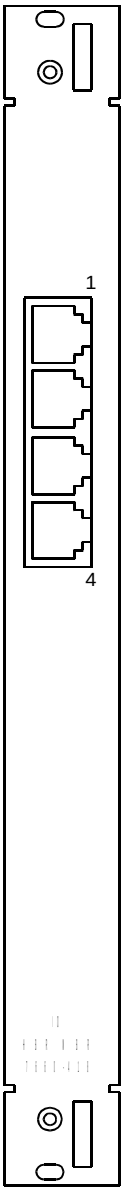


High Bit Rate IOM Connections

This IOM provides four serial interfaces (labeled 1 to 4) that can be configured as either DCE or DTE, synchronous only.

The following cable adapter is available:

- 7660-524 – Cable Adapter, HBR, RJ45 to DB25 DCE



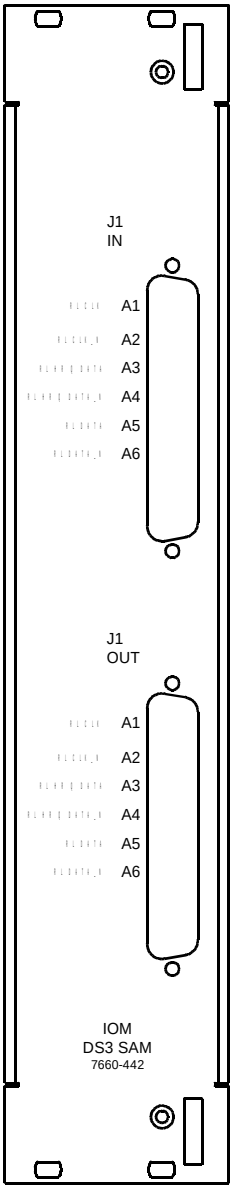
1. Connect the RJ45 cable connector to the desired IOM port.
2. Connect the DB25 cable connector to the user equipment.

High Speed SAM IOM Connections

The HSAM IOM provides two serial interfaces with LVDS signal type. J1 is the input and J2 is the output. The connectors are Combo-D female.

The Broadmore 1700 uses the following signal connections:

Signal Name	Data Rates (bps)	Connector	Input/ Output
In I Data	200K,137M,47M,10M	J1-A5	Input
In I DataNot	200K,137M,47M,10M	J1-A6	Input
In Q Data	137M	J1-A3	Input
In Q DataNot	137M	J1-A4	Input
In I/Q Clock	200K,137M,47M,10M	J1-A1	Input
In I/Q ClockNot	200K,137M,47M,10M	J1-A2	Input
Out I Data	200K,137M,47M,10M	J2-A5	Output
Out I DataNot	200K,137M,47M,10M	J2-A6	Output
Out Q Data	137M	J2-A3	Output
Out Q DataNot	137M	J2-A4	Output
Out I/Q Clock	200K,137M,47M,10M	J2-A1	Output
Out I/Q ClockNot	200K,137M,47M,10M	J2-A2	Output



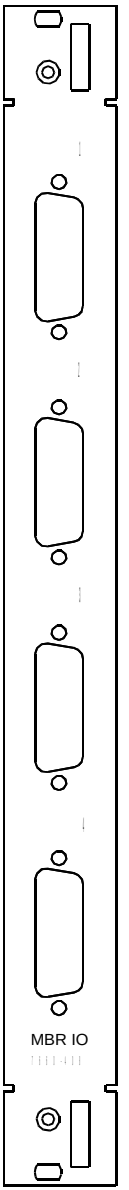
1. Connect the HSAM cable connector to the desired IOM port.
2. Connect the HSAM cable connector to the user equipment.

Serial Mux/Demux IOM Connections

The Serial Mux SAM and Serial Demux SAM both use the MBR IOM, which provides four serial interfaces (labeled 1 to 4). Different cable adapters support Serial Mux IOM and Serial Demux IOM applications. These adapters combine the four serial interfaces into one DB37 connector.

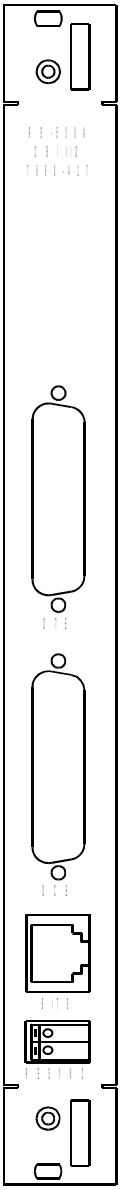
The following cable adapters are available:

- 7660-525 – Serial Mux RS-422A Cable Adapter, DB37 DTE 6 ft.
- 7660-526 – Serial De-Mux RS-422A Cable Adapter, DB37 DCE 6 ft.



1. Determine if the MBR IOM is being used with a Serial Mux SAM or Serial Demux SAM and obtain the appropriate cable adapter.
2. Connect the four HDB26 (high density) cable connectors (labeled P1 to P4) to the corresponding four IOM ports (labeled 1 to 4).
3. Connect other end of cable adapter to the user equipment.

Cell Bearing Interface (CBI) IOM Connections



This IOM has one DB25 DCE interface and one DB25 DTE interface but only one interface can be used at a time. The interface is software selectable as part of the provisioning process.

The BITS (Building Integrated Timing System) input is not used unless this card is installed as a CBI NIM IOM.

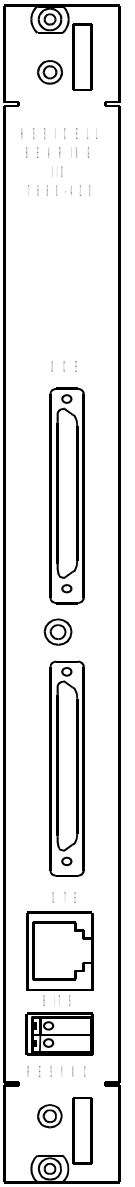
The RESYNC output is used to synchronize cryptographic equipment. The resync output behavior is controlled by setting jumpers on the card (see *"SAM IOM Installation" on page 5-12*).

The following cable adapters are available:

- 7660-522 – CBI RS-530 Cable, F-F, DCE 5ft
- 7660-520 – CBI RS-530 Cable, M-M, DTE 5ft
- 7660-521 – CBI RS-530 Cable Extender, M-F, 5ft

1. Connect the DB25 cable connector to the desired DTE or DCE connector on the IOM.
2. Connect the other end of the cable to the user equipment.
3. If this card being used as a CBI NIM IOM and a BITS input is desired, see *"BITS Interface Connections" on page 6-7*.
4. If this card is being used with crypto equipment and a resync output is desired, connect the Crypto Resync (RESYNC) output to cryptographic equipment such as KG-94, KIV-19, KG-194, KIV-7, KG-175, and KG75. The resync output behavior is controlled by setting jumpers on the card (see *"SAM IOM Installation" on page 5-12*). The spring terminal block accepts 26 to 18 AWG wire.

High Speed Cell Bearing Interface IOM Connections



This IOM serial interface has SCSI-2 (HDB50) type DTE and DCE interface connectors but only one may be used at a time. The interface is software selectable as part of the provisioning process.

The BITS (Building Integrated Timing System) input is not used unless this card is installed as a HSSI NIM IOM.

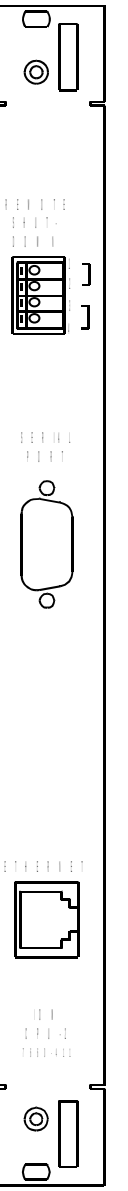
The RESYNC output is used to synchronize cryptographic equipment. The resync output behavior is controlled by setting jumpers on the card (see *"SAM IOM Installation" on page 5-12*).

The following serial interface cable adapters are available:

- 7660-524 – HSSI-CBI RS-422/RJ48 Cable Adapter, DCE 6ft
- 7660-527 – HSSI-CBI RS-422/RJ48 Cable Adapter, DTE 6ft
- 7660-523 – HSSI-CBI SCSI-2 Cable, M-M, 10ft

1. Connect the HDB50 cable connector to the desired DTE or DCE connector on the IOM.
2. Connect the other end of the cable to the user equipment.
3. If this card being used as a HSSI NIM IOM and a BITS input is desired, see *"BITS Interface Connections" on page 6-7*.
4. If this card is being used with crypto equipment and a resync output is desired, connect the Crypto Resync (RESYNC) output to cryptographic equipment such as KG-94, KIV-19, KG-194, KIV-7, KG-175, and KG75. The resync output behavior is controlled by setting jumpers on the card (see *"SAM IOM Installation" on page 5-12*). The spring terminal block accepts 26 to 18 AWG wire.

CPU IOM Connections



Remote Shutdown Connections

The CPU Remote Shutdown feature allows a connection to be made across one of the contact pairs to remotely close the contacts, which forces a reset of the CPU in the event that the CPU cannot be reset through software.

The behavior of these terminals are set by jumpers on the card (see *"CPU IOM Installation" on page 5-13*).

To use this feature, connect a cable to terminals 1-2 and/or 3-4 on the CPU IOM. (See example at right.) The spring terminal block accepts 26 to 18 AWG wire.

Serial Port Connections

This is a standard DB9 RS-232 DTE serial management port that can be connected to a serial terminal or PC running a terminal emulation program. Access to this port is determined by the security settings.

Ethernet Connections

This is a standard ethernet DTE management port that can be connected to an IP network. Access to this port is determined by the security settings.

Power Supply Connections

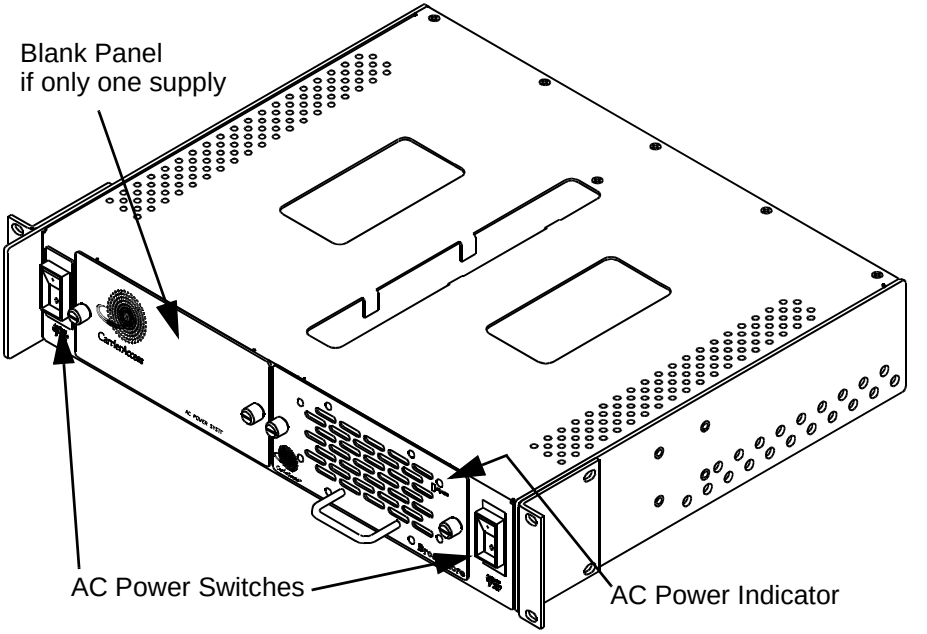
- Optional AC Power Supply Connections ... *6-21*
- Broadmore Power Input Connector ... *6-23*
- Connecting –48 VDC Power ... *6-23*

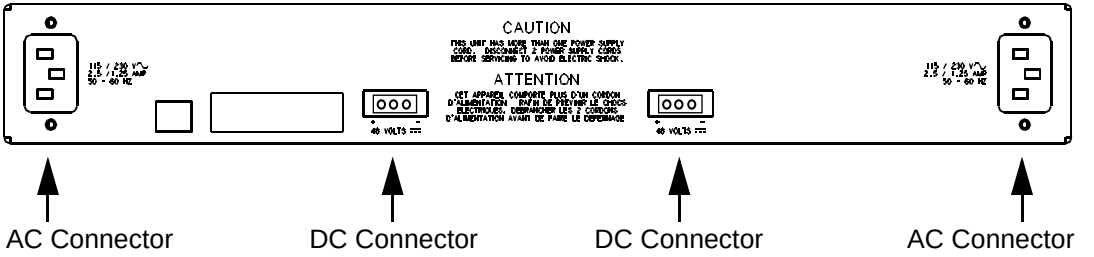
WARNING! **THE INSTALLER SHALL ENSURE THAT ALL POWER CONNECTIONS TO THE BROADMORE 1700 HAVE AN APPROVED SERVICE DISRUPTION FEATURE EXTERNAL TO THE BROADMORE 1700 FOR EMERGENCY USE. THIS MAY BE A FUSE, CIRCUIT BREAKER, CORRECTLY LABELED SWITCH, OR OTHER APPROPRIATE DEVICE.**

Optional AC Power Supply Connections

The optional redundant AC Power Supply Tray mounts in the rack directly below the Broadmore 1700 chassis (see *"AC Power Supply Tray" on page 4-8*). Each 40W power supply module in the tray converts voltage from 110/220 VAC (50-60 Hz) to –48 VDC. Each DC cable has pigtail wires for connecting –48 VDC power to the Broadmore chassis.

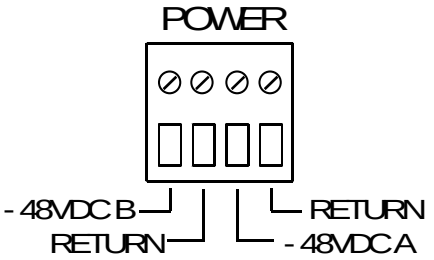
1. Locate the AC power cord provided with each power supply module and connect one end to the AC connector on the rear of each module.
2. Ensure the power switch on the front of each module is turned OFF and that the AC power indicator (LED) does not light.
3. Connect each AC power cord to a facility 110 VAC power receptacle. Cords may be connected to different AC sources for additional redundancy if desired.
4. Turn each power switch to the ON position. The green AC power indicator (LED) should illuminate beside each switch to indicate normal operation.
5. Turn each module power switch to the OFF position and verify that the power indicator is off.
6. Locate the DC power cord provided with each power supply module and connect the plug to the DC connector on the rear of each module.


Blank Panel
if only one supply
AC Power Switches
AC Power Indicator


CAUTION
ATTENTION
AC Connector
DC Connector
DC Connector
AC Connector

Broadmore Power Input Connector

Power is provided as –48 VDC to the connection points in the rear of the chassis. Voltage may be provided directly from the user's power source. Optionally, the Broadmore 1700 comes with a redundant AC power supply. The power connector on the lower back of the chassis is shown below. The connector is a compression type in which the wire is inserted in the lower opening and the compression screw above is tightened to secure the wire. A small flathead screwdriver is required to secure the wires.



Connecting –48 VDC Power

1. Identify the power cables and secure their power.
2. Use a Volt-Ohm Meter (VOM) to verify the voltage and polarity of the cable wires.
3. Turn off power to the cables and use a VOM to verify there is no voltage present.
4. Run the power cables to the connection point on the chassis rear as shown above. Note there are two power rail inputs, A and B, as labeled.
5. Insert the cable wires and tighten the compression screws to secure the wires.
6. Restore power to the cables. Use the VOM to verify voltage is present at the chassis connection points.
7. Turn off power to the cables until ready for the power-up sequence.

Software

The Broadmore 1700 is delivered with operating software pre-installed. The FIPS 140-2 validated Broadmore/SSHield Management software is installed on each CPU's flash memory. DSP software code also exists on each NIM and SAM and is unique for each type of module.

Two programs may be used to communicate with the CPU operating system. These are the Communication Access Multiplexer Management Interface (CAMMI) and Command Line Interface (CLI). This manual is based upon CAMMI. For information on the CLI, see *Command Line Interface on page 9-1*.

CHAPTER 7

Configuration

In this Chapter:

- Overview ... *7-2*
- Power Supply Redundancy ... *7-27*
- Module Redundancy ... *7-28*
- Module Configuration ... *7-36*
- PVC Connection ... *7-99*
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CAUTION! **FOR SECURE OPERATION, A SUPERUSER (CRYPTO OFFICER) SHOULD PERFORM THE INITIAL CONFIGURATION AND CREATE USER ACCOUNTS, AS DESCRIBED IN CHAPTERS 10 AND 11.**

The Broadmore 1700 must be correctly configured, using CAMMI, before ATM network communications can be established. This section provides background information, equipment requirements, and other prerequisites for accomplishing the actual system configuration.

Configuration information is retained in three subdirectories collectively referred to as the configuration database. Access to this database is to set variables to acceptable values for successful operation. The three subdirectories are:

- CAM\CONFIG\CURRENT – contains the complete set of startup configuration data for all modules. This data is saved when **Save Config. For PowerUp** is selected from the user interface.
- CAM\CONFIG\DEFAULTS – contains the defaults to be used for new cards and ports. Default values for new modules are automatically loaded if a startup configuration for that module does not exist.
- CAM\CONFIG***user-name*** – contains a snapshot of the system and module configuration data as of the time the SAVE CONFIGURATION command was issued. Data is stored in a subdirectory with user-supplied ***user name***.

Power-up

1. Power-up the chassis. The Broadmore 1700B is designed for continuous service. There is no on/off power switch since the system is designed to remain on at all times. Simply apply power.
2. Observe the LED indicators to ensure that the system is operating properly. See *"Summary of Front Panel LEDs" on page 8-50*.

Power-on diagnostics take approximately 30 seconds, after which the LEDs indicate the operational condition. Refer to Chapter 5, *Maintenance and Troubleshooting* if any problems arise. The rest of this chapter assumes normal operation

User Interface Requirements

Communicate with the CPU to complete configuration actions in one of several ways. An RS-232 serial connection is provided on the front of the CPU card (via the CPU IOM card will be a future release). This serial connection requires a VT100 compatible emulating software package running on the PC.

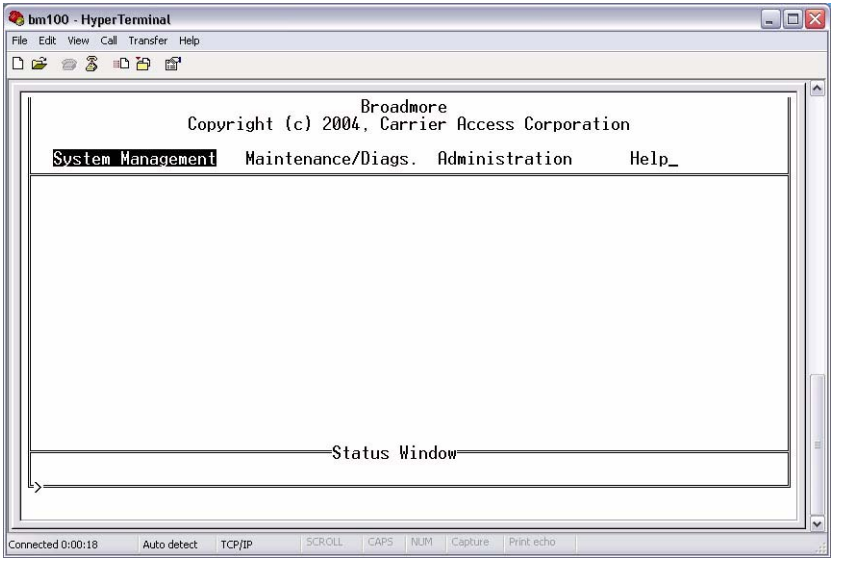
NOTE: We do not recommend using Microsoft Hyperterm due to unsatisfactory terminal emulation.

Remote execution can be accomplished via a Telnet client application configured as a VT100 terminal. An SNMP connection can be established via Ethernet to the CPU IOM, via CIP (RFC 1577), or via LANE.

NOTE: In a Broadmore system with redundant CPUs, the primary IP address is used to log into the online CPU and the secondary IP address is used to log into the standby CPU.

CAMMI is used throughout this manual. The CLI (Chapter 9) supports the same command set as CAMMI, however the CLI is a pure text interface.

Reset the terminal preference font if the borders are not solid lines. Depending on the terminal emulation application, ANSI BBS may also be more satisfactory.


bm100 - HyperTerminal
File Edit View Call Transfer Help
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help_
Status Window
Connected 0:00:18 Auto detect TCP/IP SCROLL CAPS NUM Capture Print echo

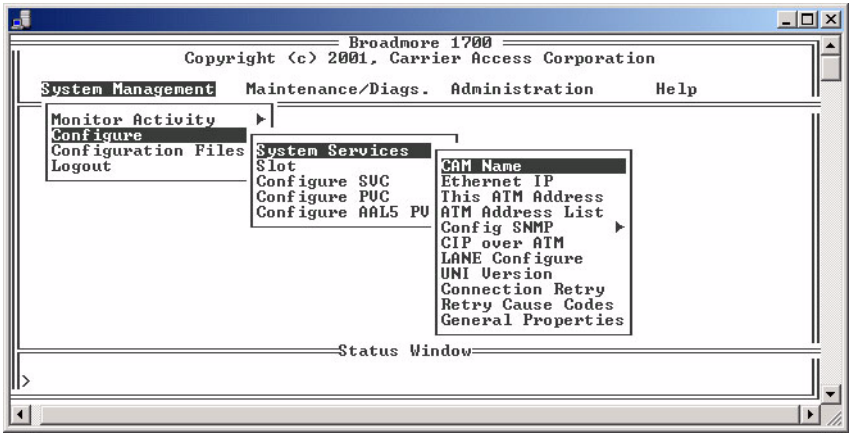
Screen Display Annotation

The symbol ↵ (**Enter** key) will be used throughout this manual to indicate selection. Typically the selection is from a list of choices on a drop-down menu. Often, there is a sequence of multiple drop-down menus where the selection process will be displayed as a series of ↵ symbols. For example, the CAMMI main menu is shown below.

Highlight **System Management** and ***press*** **Enter** to display the first submenu. ***Highlight*** **Configure** on the drop-down menu and ***press*** **Enter** to display the second drop-down menu. ***Highlight*** **System Services** on this menu and again ***press*** **Enter** to display the third drop-down menu. This type of sequence will be annotated as follows throughout this manual:

> *Select* **System Management** ↵
> *Select* **Configure** ↵
> *Select* **System Services** ↵

This example sequence displays the screen on the next page. Each item in the final window is followed by a " ▸ " to indicate that an additional entry screen follows.



Choose the second item, **Configure IP**, to display a screen for entering six lines of IP address information (not shown).

This method of annotating screen displays will make the text and logic easier to follow and less prone to error. Specific steps will be clear and easy to follow, leading to more efficient system operation.

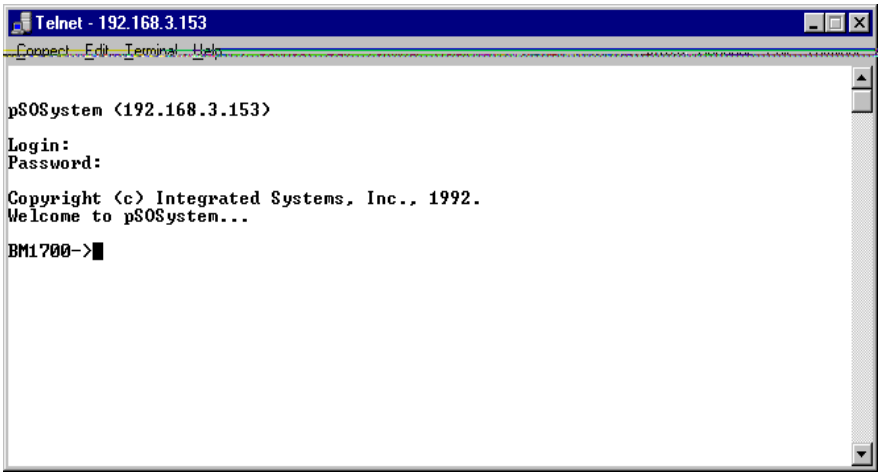
Key Map

The following key map correlation is provided to assist in navigating through the various menus. This may be required since some terminal emulation packages do not recognize the full keyboard character set.

Escape to Top	^
Up	[
Down	]
Page Up	{
Page Down	}
Home	(
End	)
Insert	:
Delete	@
Help (cli only)	?

CAMMI Access

To access the CAMMI main menu, log into the system with a valid user identification (**SYSADMIN**) and password (**INITIAL**). This default user ID/password is delivered with the system with supervisor access as explained in *"General Properties" on page 7-23*. User ID and password are case-sensitive when entered.



At this point, you may change the terminal interface baud rate. The default is 9600 baud and the system reverts to this at every reboot. The command to change the rate is setbaud <rate> where a valid <rate> is 300, 600, 1200, 2400, 4800, 9600, 19200,38400, 57600, or 115200.

NOTE: The baud rate default is 9600. The terminal will return to 9600 baud *on each system boot or reboot*. Operation above 19200 is not recommended.

Upon successful entry, you are asked to change the terminal baud rate to match the new setting. The system will then change its baud rate. You will not be able to communicate with the system until your terminal baud rate is changed to match.

System Services Configuration

Configure System Services to communicate with the ATM switch and set up parameters for Broadmore 1700 control. The data entry screens are accessed as shown.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Configure each item as explained below

CAM Name

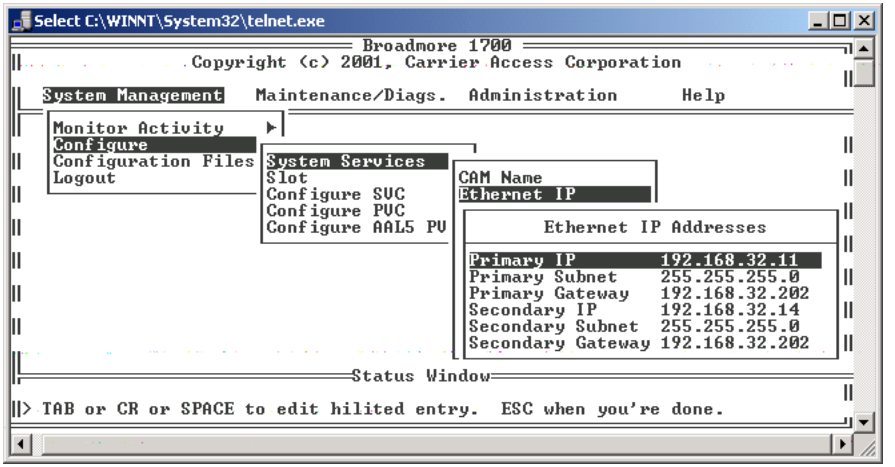
Each Broadmore 1700 can be named locally. A short (10-character) name and a long (64-character) name are available. These two names are independent as chosen by the operator. The long name appears at the top of CAMMI screens to provide on-screen identification. The short name appears as a shell prompt. The default long name is a null field and the default short name is **Broadmore>.**

From CAMMI, follow the sequence below to enter names.

Select **System Management** ↵
Select **Configure** ↵
Select **System Services** ↵
Select **CAM Name** ↵
Enter the desired long and short names ↵

Ethernet IP Configuration

Select Ethernet IP and the screen below appears. On initial boot-up, enter the IP address, which will not go into effect until the system is rebooted. Use the reset toggle switch on the front of the CPU to reboot the system.



Follow the pull-down menu selections to enter the Broadmore 1700's IP address. This address is then used for Telnet access to the CAMMI program.

NOTE: In a Broadmore system with redundant CPUs, the primary IP address is used to log into the online CPU and the secondary IP address is used to log into the standby CPU.

NOTE: Changes to the following settings only take effect upon system reboot:

- IP Configuration
- CIP Configuration
- LANE Configuration
- Redundancy (APS) Configuration
- UNI Version Configuration
- Cause Code changes
- ATM Address Changes

On initial setup, configure all of these items before doing a system reboot.

Subnet is the mask for the network. Subnet and Gateway are assigned by your facility's network administrator. Reboot the Broadmore 1700 to have this IP address take effect. The entire Broadmore 1700 will reboot with the new IP address. Log into the system with a valid user ID and password (ID ***SYSADMIN***, password ***INITIAL***).

1. Enter the IP, subnet, and gateway addresses (obtained from the network administrator). These settings take effect when the Broadmore 1700 is rebooted.
2. Follow the sequence below to reboot on a single CPU system.

   Select **Maintenance/Diags.** ↵
   Select **Reboot System** ↵

3. Log into the system, and return to the system services configuration screen to continue the process.

ATM Address

Follow the sequence below and choose **Change** on the user-defined ATM address screen.

CAUTION! AN ADDRESS CHANGE WILL CAUSE SERVICE DISRUPTION.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **ATM Address** ↵

A screen displays to enter the ATM address.

The Media Access Control (MAC) address is the initial default ATM address. Save the new ATM address after entry to update ATM access information. This newly saved address will not take effect until the system is rebooted. With signaling turned off, the ATM address will be displayed as all zeroes.

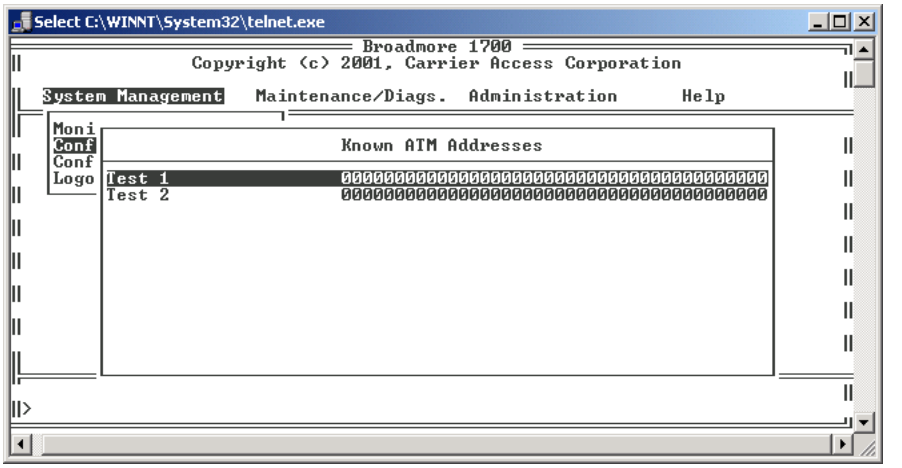
For dual CPU systems, define the ATM address to keep the same address during a CPU switchover. Otherwise, an address change will cause service disruption.

ATM Address List (optional)

The following steps are optional for SVC services and are not used for PVCs. The ATM address list provides a convenient way to store frequently called SVC addresses, instead of having to enter each address manually when needed. To use this optional feature, follow the sequence below to display user-defined ATM addresses.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **ATM Address List** ↵

This displays the Known ATM Addresses.



Follow the sequence below to Edit, Copy, Delete, or add a New user to the ATM list.

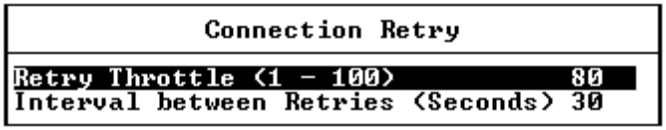
Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **ATM Address List** ↵ ↵
Select one of the following:

* **Edit** ↵, edit the description and/or ATM address
* **Copy** ↵, (to put a copy of the ATM address into the list)
* **Delete** ↵
* **New** ↵, enter the description and/or ATM address

With each selection, confirm your changes and press *Esc* to exit.

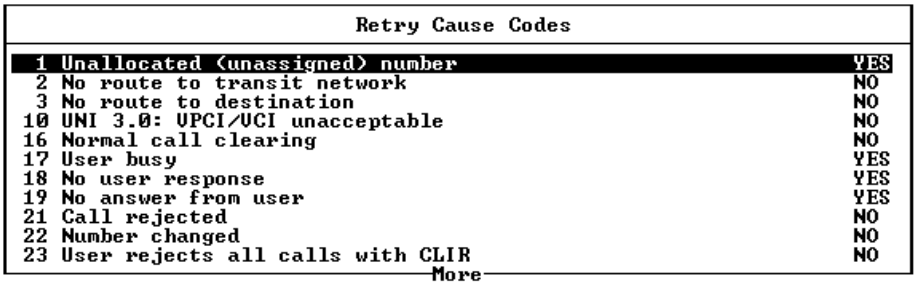
Connection Retry

Applicable to SVCs only, the retry throttle value is the number of SVC call setups/ teardowns that the Broadmore 1700 works on at one time. An initial throttle value of 80 and interval of 30 seconds are recommended. These values can then be adjusted to meet local requirements.



Retry Cause Codes

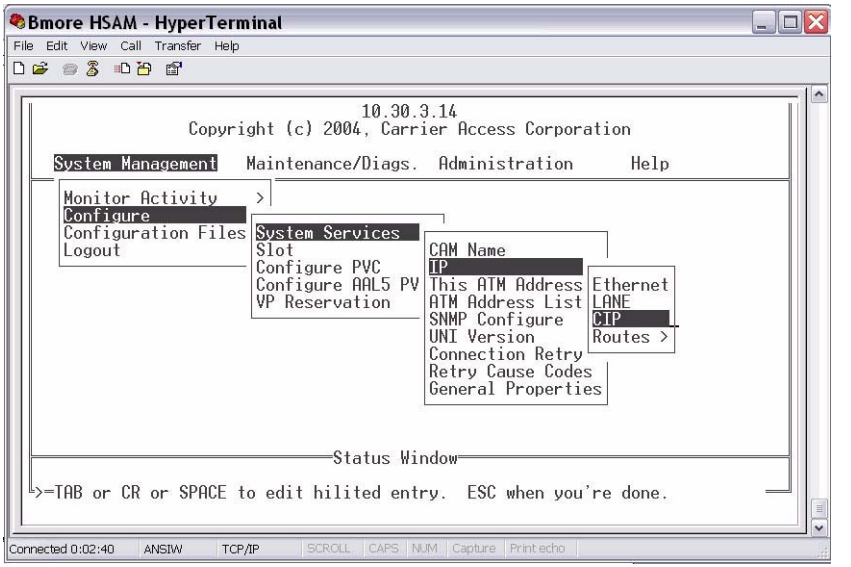
Applicable to SVCs only, each item in this list can be set to yes or no, based upon local requirements. These codes are set as factory defaults to the most common values. Changing them will overwrite the defaults. Only the “yes” values are retried.

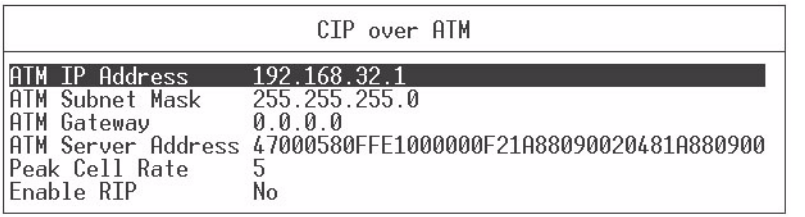

Retry Cause Codes
1 Unallocated (unassigned) number YES
2 No route to transit network NO
3 No route to destination NO
10 UNI 3.0: VPCI/VCI unacceptable NO
16 Normal call clearing NO
17 User busy YES
18 No user response YES
19 No answer from user YES
21 Call rejected NO
22 Number changed NO
23 User rejects all calls with CLIR NO
More

CIP over ATM (RFC 1577)

RFC 1577 support is also known as Classical IP (CIP) over ATM. CIP is provided as a path for controlling multiple Broadmore 1700s when IP connectivity over Ethernet is not available to all of the installed Broadmore 1700s. Control signals are sent to a Broadmore 1700 over the ATM if the Broadmore 1700 is not on the Ethernet with the control station. As a prerequisite, the configuration process must be completed.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **CIP over ATM** ↵





Configuration forms the Broadmore 1700s into a subnetwork, with either an ATM switch or workstation designated the server and others designated as clients. The server maintains a Logical IP Subnetwork (LIS), which cross-references logical IP addresses to ATM addresses. When the control station sends IP traffic (e.g., control signals) via the subnetwork IP address to a Broadmore 1700, the server uses LIS data to tell the sender an equivalent ATM address to use in place of the IP address. The IP datagrams are then sent over the ATM network to the desired Broadmore 1700, which takes appropriate action and responds. The control station has full communications with the Broadmore 1700 over ATM/Ethernet. Control is identical to control over Ethernet, and the ATM control path is essentially transparent to the user.

Follow the path shown to reach the CIP over ATM configuration items. These items are listed in the following table.

Item	Description
ATM IP Address	Enter the IP address within the subnet. Set to all zeros to disable CIP.
ATM Subnet Mask	The mask is the same for all Broadmore 1700s on the network.
ATM Gateway	The IP address of the ATM gateway. If not used, enter 0.0.0.0.
Server Address	The address of the server containing the LIS. This may be a workstation on the ATM network or ATM switch. Set to zero if you want to use AAL5 PVCs for CIP instead of SVCs.
Peak Cell Rate	This controls the bandwidth allocation to RFC 1577 support.
Enable RIP	Normally disabled. Only set to Yes for router operation as the CIP subnet server.

This configures the Broadmore 1700. Some Broadmore 1700s may need routing table additions to recognize and respond to the control station via the ATM subnetwork.

These Broadmore 1700s will be configured individually, based upon network topology. Each unit may or may not have an Ethernet connection.

In the case of no Ethernet connection, the unit routes Ethernet traffic to the ATM by default. The Ethernet ATM gateway setting is then a null field: 0.0.0.0.

Follow the sequence on the next page to verify that no gateway is defined. Do this for each applicable Broadmore 1700 without an Ethernet connection.

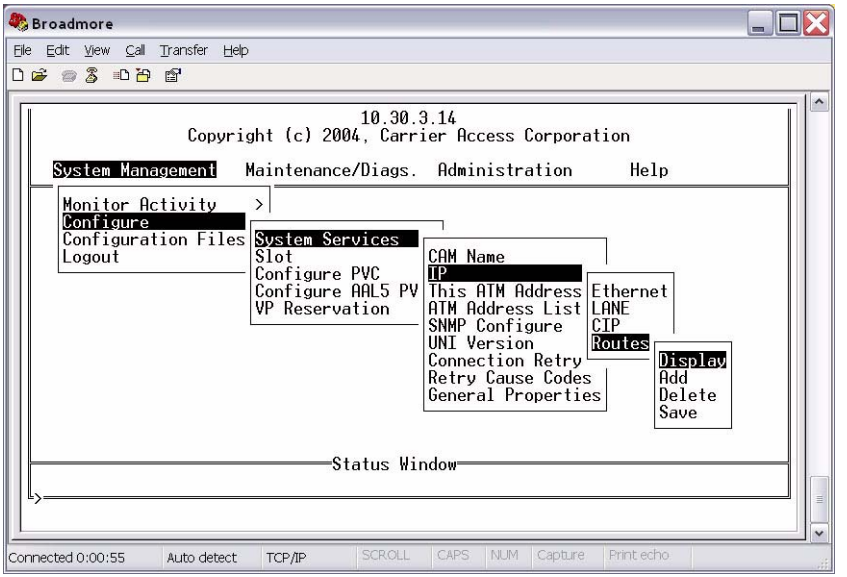
Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **Configure IP** ↵
Select **Gateway 0.0.0.0** (correct if necessary)

Static Routes

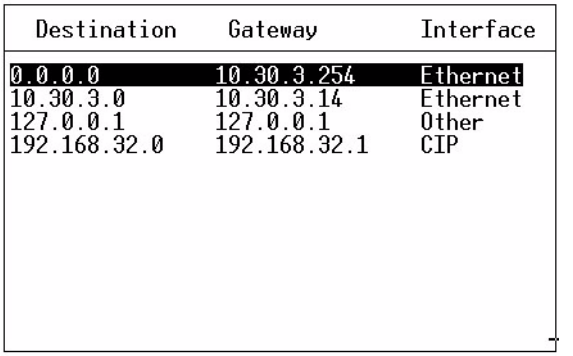
Each Broadmore 1700 has an additional Ethernet route to support RFC 1577 unless it is on the same Ethernet network as the master control station. Follow the steps below to add this routing. For sample configuration with static routes, see *"Sample Network with RFC 1577 Configuration" on page D-1*.

Follow the sequence below to Display, Add, Delete, or Save static routes.

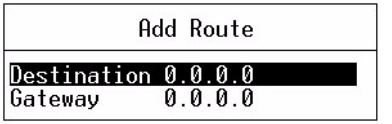
Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **IP** ↵
Select **Routes** ↵



Select **Display** to see the list of current static routes.



To add a new static route, select **Add** and then enter the Destination IP address and Gateway IP address. You can also select **Delete** to remove a static route. When finished, press Escape to exit, then select **Save** to save the changes.



Creating Static Routes with Shell Commands

It is also possible to use shell commands to create static routes, though it is much easier to use the CAMMI graphical user interface. The following example shows how to add static routes during the initial setup of the Broadmore.

1. Connect to the Broadmore 1700 via the serial port and log-in using the default user ID and password, ***SYSADMIN*** and ***INITIAL***.

2. This will give you the prompt: **Broadmore>**
   Enter **route –a** ↵ to display the current routing table.

3. Add a routing entry in the format
   route add xxx.xxx.xxx.xxx yyy.yyy.yyy.yyy ↵
   where xxx.xxx.xxx.xxx is the destination IP address (control station)
   yyy.yyy.yyy.yyy is the ATM subnet IP address of the Broadmore 1700 on the Ethernet segment serving as the control station.
4. Enter **savert** ↵ to save this entry. The new routing entry is immediately active.
5. Enter **route –a** ↵ to observe that the route has been added to the table.

NOTE: You may enter **route delete xxx.xxx.xxx.xxx yyy.yyy.yyy.yyy** to remove the entry from the routing table.

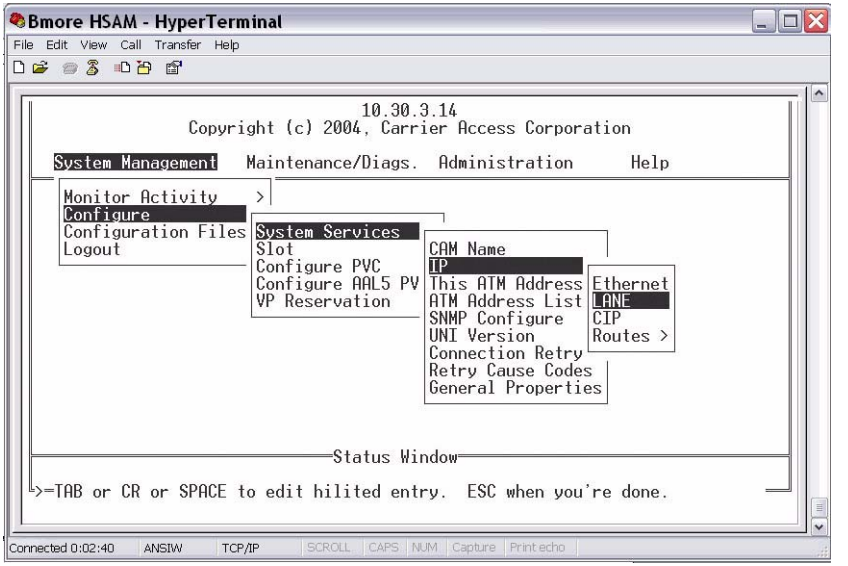
6. Enter **cammi** ↵ to return to CAMMI.

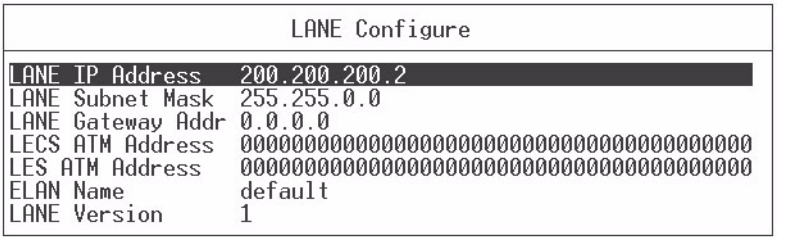
Repeat this procedure for each Broadmore 1700.

LANE Configuration

The Broadmore 1700 supports LANE version 1 and 2 acting as LAN Emulation Client, LEC. Follow the sequence below to display the LANE configuration screen shown. Six fields appear for data entry. Explanations are provided for each in the table below.

Select **System Management** ↵
Select **Configure** ↵
Select **System Services** ↵
Select **LANE Configure** ↵





NOTE: Using LANE may require static routes in a manner similar to CIP. Refer to the Static Routes section above and the example in Appendix D.

Item	Description
LANE IP Address	This is the IP Address for the Broadmore 1700. To get onto the ELAN, the address must be on a different logical subnet than the OSC's Ethernet IP Address. This address must be different than the ATM CIP Address subnet.
LANE Subnet Mask	This is the subnet mask to be used in conjunction with the Broadmore 1700's LANE IP Address.
LANE Gateway Addr	The IP address of the LANE gateway. If not used, enter 0.0.0.0.
LECS ATM Address	This address is optional. This is the ATM Address of the LAN Emulation Configuration Server, LECS. If the ATM Address of the LAN Emulation Server, LES, is known and is entered below, this LECS value may be left blank. If the LECS is needed and no address is entered here, then the "well known" ATM Address (as defined by ATM Forum's LANE standard) will be used by default. The "well known" default address is 4700 7900 0000 0000 0000 0000 0000 A03E 0000 0100.
LES ATM Address	This field is optional. The ATM Address of the LAN Emulation Server is entered here. If data is entered here, the ELAN name and the LECS ATM address will not be needed and will not be used by the system if they are entered below.
ELAN Name	This field is optional. If a name is entered, it will be sent to the LECS to locate the LES for this ELAN. If the ELAN Name is not specified, and no LES is specified, then, the default ELAN, as entered in the LECS, will be used. The Broadmore 1700 is never an LECS.
LANE Version	This value will be either **1** or **2**. Use the LANE version supported and active in the segment's LECS and in the other LE clients.

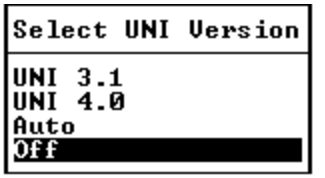
UNI Version

The signaling options are UNI 3.1, UNI 4.0, Auto, and Off. Follow the sequence below and make the appropriate choice from the screen shown.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **UNI Version** ↵

NOTE: You must select the UNI Version before configuring any SVCs. If there are active SVCs, the UNI Version will not appear in the menu until you release all the SVCs. This is done to prevent interrupting service on those circuits.

The UNI Version selection screen appears as shown below with the current selection highlighted.



CAUTION! UNI VERSION SHOULD BE OFF WHENEVER THE NIMS ARE NOT CONNECTED TO AN ATM SWITCH INTERFACE CONFIGURED FOR UNI SUPPORT. UNI VERSION SHOULD ALSO BE OFF IF A CBI OR HSSI-CBI NIM CARD IS INSTALLED BECAUSE IT HAS NO ATM SWITCH INTERFACE.

General Properties

- Max VP/VC ... *7-23*
- Bandwidth Meter ... *7-25*

Max VP/VC

The Max VP/VC option allows you to set the maximum number of Virtual Paths (VPs) and corresponding Virtual Channels (VCs). The maximum number of VCs allowed per VP is based on the number of VPs set. Table 7-1 shows the Max VP/VC settings and the valid values for VP and VC numbering.

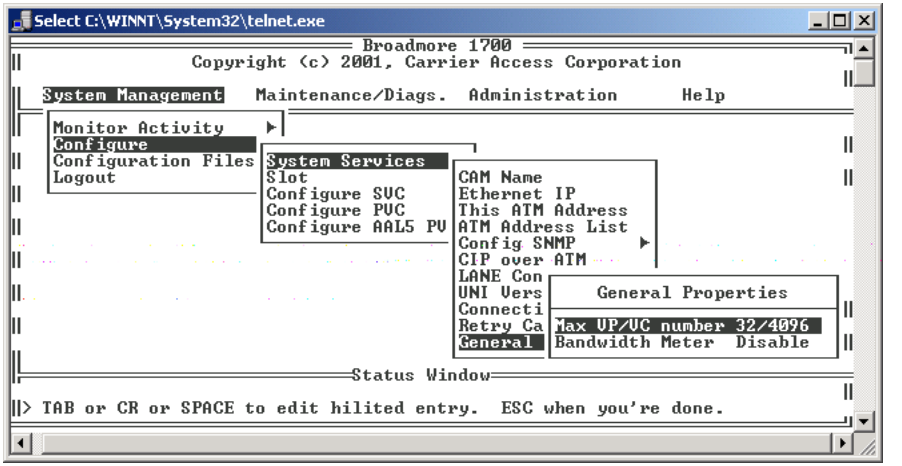
Table 7-1: Settings for Max VP/VC and Valid Values for VP/VC Numbering

Max VP/VC Setting	Maximum Number of VPs Allowed	Valid Values for VP Numbering	Maximum Number of VCs Allowed	Valid Values for VC Numbering*
2/65536	1	0 or 1**	65,535	1 - 65535
4/32768	4	0 - 3	32,767	1 - 32767
8/16384	8	0 - 7	16,383	1 - 16383
16/8192	16	0 - 15	8,191	1 - 8191
32/4096 (default)	32	0 - 31	4,095	1 - 4095
64/2048	64	0 - 63	2,047	1 - 2047
128/1024	128	0 - 127	1,023	1 - 1023
256/512	256	0 - 255	511	1 - 511

*VC numbering must start at 32 if the VP number is 0.
**For the ATM DS3, CBI, and HSSI-CBI modules, the VP number must be 0; 1 is invalid.

To set Max VP/VC, follow the sequence below and make the appropriate choice from the screen shown.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **General Properties** ↵
Select **Max VP/VC** ↵



Bandwidth Meter

The Bandwidth meter allows you to track the amount of bandwidth in use. Follow the sequence below and make the appropriate choice from the screen shown.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **General Properties** ↵
Select **Bandwidth Meter** ↵

NOTE: The Bandwidth meter uses the clock rate of the HSSI NIM as the bus rate. If the HSSI NIM is set for received timing, the bandwidth meter will not be using the correct bus rate because the clock is supplied by the HSSI at the other end. Be sure to check how the HSSI's clock is set before using the Bandwidth meter.

User Security Configuration

The Broadmore 1700 system has a user identification procedure for security. Each user has a unique password. Users are divided into four privilege levels providing access to selected command functions.

Browser – Limited to viewing current configurations, statistics, and logs.

Operator – Can perform all normal operations such as configuring modules, establishing connections and removing connections.

Sys_Admin – Can perform all operator functions plus FTP, diagnostics and test functions.

Super_User – Can perform all operator and maintenance technician functions plus add/delete/modify user access. A Super_User can add, delete, or change user access privileges (user ID, password, and level of access) from the Administration main menu drop-down.

Select **Administration** ↵
Select **Change User ID** ↵

Each user can change his/her personal password from the Administration main menu drop-down item **Change Password** (below). Follow the on-screen directions and enter the new password twice. Change the password for user ID SYSADMIN to something other than **INITIAL**.

Select **Administration** ↵
Select **Change Password** ↵

NOTE: For more information about Security features and privilege levels, see *"Security Management" on page 10-1* and *"Security Management (FIPS Mode)" on page 11-1*.

Power Supply Redundancy

The Broadmore 1700 has two power input connectors for receiving –48 VDC. The user may provide power to these connectors from different sources as one form of redundancy. Carrier Access offers an optional dual AC power supply with 110 VAC, and provides two sources of –48 VDC to the Broadmore 1700. This is power supply contains two independent modules, each sufficient to operate the Broadmore 1700. These modules are hot-swappable, and they may be connected to different sources for an additional degree of redundancy. There is no impact to the Broadmore 1700 as long as one (or both) of the modules is operating normally.

Module Redundancy

- Protection Definitions ... *7-28*
- NIM Redundancy ... *7-29*
- CPU Redundancy ... *7-32*

Protection Definitions

Automatic protection switching (APS) is performed in accordance with specifications except as noted. The following definitions are provided to clarify the terms used in NIM redundancy.

Item	Definition
APS	Automatic Protection Switching
Reverting	After redundancy switching, the system will switch back to the original primary NIM when repairs are completed.
Non-reverting	After redundancy switching, the protection NIM will remain the system primary NIM after repairs are completed. In this case, the repaired NIM becomes the protection NIM. This only applies to manual switching.
1+1 Protection	The signal is continuously bridged to the working and protection equipment so payloads are transmitted identically over both paths. The receiving equipment chooses either path. The user must set their equipment to bi-directional. The Broadmore 1700 supports bi-directional 1+1 APS for the NIM. The Broadmore 1700 does not support uni-directional 1+1 APS for the NIM.
1:*n* Protection	There is one backup for *n* critical components. Any of the *n* working channels can be bridged to a single protection line.

Item	Definition
1:1 Protection	A special case of 1:*n* protection where *n*=1. Each critical component has a dedicated backup, which assumes operation if the primary unit fails, so that connectivity is not adversely impacted.

NIM Redundancy

NOTE: NIM redundancy is not available with a CBI or HSSI-CBI NIM.

ATM redundancy is provided by installation of a second NIM. The Broadmore 1700 can then be configured to provide 1+1 or 1:1 NIM protection in accordance with the SONET-GR-253-CORE specification. The APS is per the SONET-GR-253-CORE specification. The user may select either reverting or non-reverting APS modes as explained in the configuration below.

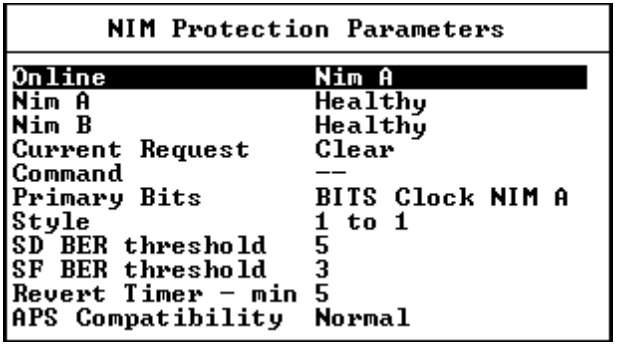
NOTE: The Broadmore will display only those features supported by the installed NIM. Consequently, some of the features described below may not apply to the NIMs you are configuring.

Configure redundancy following steps on the next page.

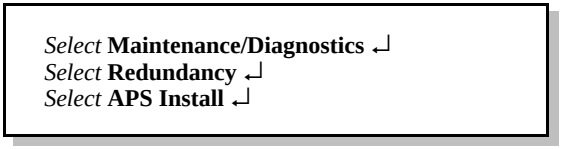
1. Review the hardware configuration. The Broadmore 1700 must have two NIMs installed to support redundancy, the master NIM in slot “B” and the APS NIM in slot “A”. If installing 1:1 protection, reverting or non-reverting, **contact the ATM switch vendor to ensure that 1:1 protection is supported.**

Select **Maintenance/Diagnostics** ↵
Select **Redundancy** ↵
Select **NIM** ↵
Select **APS Parms** ↵

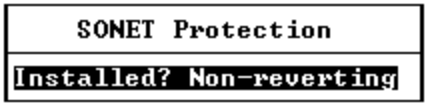
2. From the CAMMI main screen, follow the selection sequence above to display the redundancy screen.



3. Following the instructions on the bottom of the screen and toggle through available choices for each item.



4. Follow the sequence above to display the SONET screen below and make choices to meet your specific configuration. Use the space bar to toggle between **reverting** and **non-reverting**. The third choice, **Single NIM**, is used when only one NIM is installed.



5. In the redundancy configuration on the Protection Parameters shown above, the Command field choices are prioritized per the SONET GR-253-CORE specification. The available choices in priority order top-to-bottom are:

   Lockout
   Force A
   Force B
   Switch to A
   Switch to B
   Clear

   The Command Request field indicates the current request status and cannot be changed. The command "Clear" is not prioritized but it acts to remove the last request from its control station. This means that a command (i.e., choice) will not be activated if there is a higher priority current request shown in the Command Request status portion of the display. Broadmore generated requests such as "SD- Signal Degradation" are not shown on the screen but they fall in the priority list between "Force B" and "Switch to A". In other words, a command "Force B" will switch the Broadmore to NIM B regardless of signal degradation, while a command "Switch to B" will be ignored in the same circumstance if there is a current SD request. A Command Request can be received from any user interface.

6. Set the Parameters for Bit Error Rate (BER) Thresholds in the right part of the display. Enter a desired number "x" for each threshold, remembering that the degrade threshold (SD) should be a larger "x" than the failure threshold (SF) and thus a smaller number. The parameter entered is defined by the equation

   $$\text{Threshold} = 10^{-x}$$

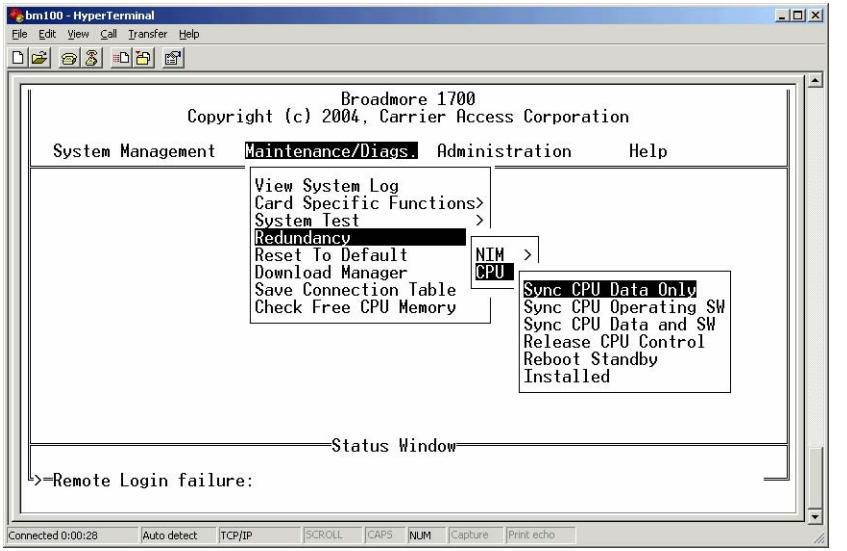
   where "x" is the user-entered number. If the redundancy chosen is revert, then the parameter "Revert Time" will be active and the number of minutes before the system automatically reverts to the original primary NIM may be entered.

7. Switching the protection installation changes module and call configurations. **Follow any change to redundant configuration by immediately rebooting the system.**

CPU Redundancy

CPU redundancy is automatically activated when a second CPU is detected in the system. If two CPUs are detected at initial boot, the first to boot becomes “online” and the other goes into the standby mode.

CPU redundancy allows the standby CPU to take control if the online CPU experiences a failure. Data files on the two CPUs are synchronized automatically when the standby CPU is powered up. This synchronization process occurs automatically when the standby CPU is powered up. Once both CPUs are functioning (one online and the other in standby) any subsequent changes to the system are mirrored (recorded in the online CPU and sent to the standby CPU). This process keeps the standby CPU up to date.



Select **Maintenance/Diagnostics** ↵
Select **Redundancy** ↵
Select **CPU** ↵

System Log

The system log is unique to each CPU. Entries are sent from each CPU and copied to the partner CPU. Each entry has a time stamp followed by an upper-case or lower-case slot letter. An upper-case letter (Q or R) indicates that the message originated from the current CPU. A lower-case letter (q or r) indicates that the message originated from the partner CPU. Thus, an entry in one log with an upper-case slot letter will have a similar entry in the other log with a lower-case slot letter.

The traplog.txt entries are sent from the online CPU in a fashion similar to the log entries. Entries from the standby CPU are suppressed so this file provides information for the online CPU only.

Select **Maintenance/Diagnostics** ↵
Select **View System Log** ↵
Select **CPU** ↵

IP Addresses

A Broadmore 1700 chassis may have one or two IP addresses (see *"Ethernet IP Configuration" on page 7-9*). The online CPU will have the primary address as configured in the **Config IP** menu. The standby CPU will have the secondary address as configured in the **Config IP** menu. If a CPU switchover occurs, the newly online CPU will change its IP address to the primary IP.

The Broadmore 1700 may also be configured with an IP address for CIP over ATM. This address is only valid for the online CPU.

If used, a LANE address is configured via the **LANE Configure** menu. LANE is only valid for the online CPU.

Select **System Management** ↵
Select **Configure** ↵
Select **System Services** ↵

Synchronizing CPU

CAUTION! MODULE REMOVAL AND INSERTION – ON A POWERED-UP SYSTEM, WAIT AT LEAST 15 SECONDS AFTER ANY MODULE REMOVAL OR INSERTION TO ALLOW THE SYSTEM TO STABILIZE. FAILURE TO FOLLOW THIS PROCEDURE MAY RESULT IN SYSTEM ERRORS REQUIRING TOTAL SYSTEM REBOOT. WHEN INSTALLING A REPLACEMENT CPU IN A REDUNDANT CPU SYSTEM, DO NOT REBOOT OR POWER DOWN THE SYSTEM BEFORE CPU SYNCHRONIZATION IS COMPLETE AS INDICATED BY A SOLID STATUS LIGHT.

CPU data sync occurs automatically and should not require user intervention (see *"CPU Sync" on page 8-23*). If there is a CPU sync problem, follow the steps below to synchronize data and operating software (SW) between the two CPUs. This should only be done when there is file manipulation unknown to the system, such as FTP of new files to the online CPU.

Three options are available for synchronizing CPU: **Sync CPU Data Only**, **Sync Operating SW**, and **Sync CPU Data and SW**. Synchronizing the operating software is very time-consuming and should only be done as a last resort when the standby CPU has no Ethernet connection to update the software by FTP.

Select **Maintenance/Diags** ↵
Select **Redundancy** ↵
Select **CPU** ↵
Select **Sync CPU Data Only** ↵

Release CPU Control

If desired, the online CPU can be made to release control (switch) to the standby CPU by performing the following steps. The online CPU will then become the standby CPU, and vice versa. This command may be used during maintenance procedures.

Select **Maintenance/Diags** ↵
Select **Redundancy** ↵
Select **CPU** ↵
Select **Release CPU Control** ↵

Reboot Standby CPU

If desired, the standby CPU can be rebooted by performing the following steps. This command may be used during maintenance procedures.

Select **Maintenance/Diags** ↵
Select **Redundancy** ↵
Select **CPU** ↵
Select **Reboot Standby** ↵

Install Single or Dual CPU

This configuration item only activates detection of the SNMP trap **Standby CPU missing**. If only one CPU is present, select **Single CPU**. If two CPUs are installed, select **Dual CPU**.

Select **Maintenance/Diags** ↵
Select **Redundancy** ↵
Select **CPU** ↵
Select **Installed** ↵

Module Configuration

- How to Configure Specific Modules ... *7-37*
- OC-12c/STM-4c ... *7-38*
- OC-12c/STM-4c BITS/Timing Redundancy ... *7-39*
- OC-3c/STM-1c ... *7-41*
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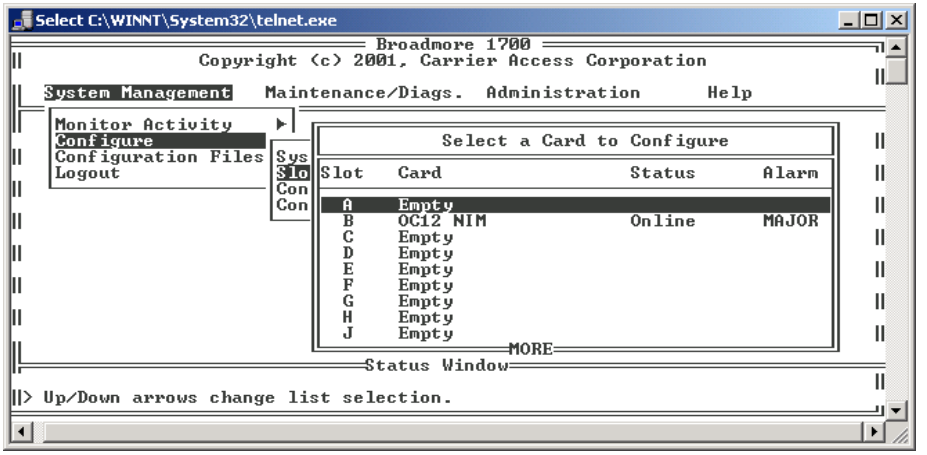
How to Configure Specific Modules

The NIM and SAM configuration process applies to each module installed in the system. The parameters vary by module as delineated below. For each module, start the procedure by following the pull-down sequence shown.

> *Select* **System Management** ↵
> *Select* **Configure** ↵
> *Select* **Slot** ↵
> *Select the slot with the module to be configured* ↵

The slot selection screen, as shown below, also displays the current alarm condition of the module in each slot.



NOTE: The list above displays only a portion of the available Retry Cause Codes. Scroll down to view additional codes.

OC-12c/STM-4c

Choose the slot (A or B) containing the OC-12c/STM-4c to be configured and a parameter input screen appears. The table below is a guide to parameter configuration.

Item	Options	Comment
Port Mode	**On-line** **Off-line** **Test Download Standby** **Configuration Request** **Broken**	This field is a status indicator and the items displayed are the result of configuration (or other) actions.
Framing Type	**SONET** **SDH**	SDH is for International ITU applications. SONET is for US applications.
Transmit Timing	**Recovered** **BITS** **Local**	Recovered means from SONET. BITS impedance matching is a hardware function; see NIM Installation, Chapter 3. BITS is tied to the local in-house timing source. Local is on-board Stratum 3 timing source.
SONET Tx	**Enable** **Disable**	If SONET Tx is enabled, framing is enabled; if SONET Tx is disabled, framing is removed (i.e., SONET Tx is all zeroes).
Loopback Mode	**Normal** **Terminal** **Local** **Remote**	Normal is no loopback. Terminal is a loopback from the ATM interface to the user equipment. Local is a loopback before NIM processing to the fiber link. Remote is a loopback from the receive fiber to the ATM before local processing.
ATM Payload Scramble Mode	**Both Scrambled** **Transmit Scrambled** **Receive Scrambled** **No Scramble**	A technique used to prevent a long string of zeros. Both ends of a connection must be configured the same for operation.
BITS Clock Alarm Loss	**Enable** **Disable**	Provides an alarm indication of loss of BITS clock at the NIM IOM when enabled. A corresponding BITS LOS SNMP trap is generated if SNMP is configured properly.

If a second OC-12c/STM-4c is installed, repeat this process for the configuration. Follow the module-specific information above, as appropriate.

OC-12c/STM-4c BITS/Timing Redundancy

Configuration of the timing options on a redundant OC-12c/STM-4c system requires correct settings on both NIMs, the DS3 port, and the NIM redundancy screen. Either of the Broadmore 1700 BITS inputs on NIM IOMs, in slots A and B, can be selected as the primary clock reference. Each BITS input has an enable/disable menu option on the coinciding OC-12c/STM-4c interface. When a condition exists that the primary reference source is not detected, the unit will switch to the other BITS input, if a signal is detected on that input. If a signal is not detected on the opposite BITS, the source clock will be derived from an alternate source propagated from the NIM to the cell bus. The source of the cell bus clock depends on the setting of the OC-12c/STM-4c transmit timing option. Two options are available, ***Local/BITS*** and ***Received***. The Local/BITS option derives clock from the on-board internal oscillator. The received option derives clock from the received SONET stream.

1. Follow the sequence below to set the redundancy primary BITS clock to either A or B.

Select **Maintenance/Diags** ↵
Select **Redundancy** ↵
Select **NIM** ↵
Select **Primary BITS** ↵
Choose **A** or **B**
In the case of a single BITS clock, select it as the primary source (NIM A or NIM B).

2. Follow the sequence below to set the clock mode configuration on the port of interest.

Select **System Management** ↵
Select **Configure** ↵
Select **Slot** with DS3 ↵
Select **Port** of interest ↵
Select **Operational Configuration** ↵
Select **Clock Mode** ↵
Choose the BITS option to allow the DS3 port to derive clock from either BITS or the cell bus

3. Follow the sequence below to set the OC12 transmit timing to either recovered or local/BITS. Recovered clock derives timing from the SONET stream. Local/BITS derives timing from the onboard oscillator. Repeat this step for both OC12s.

Select **System Management** ↵
Select **Configuration** ↵
Select **Slot** with OC12 ↵
Select **Transit Timing** ↵
Choose **Recovered** or **Local/BITS** ↵

Follow the sequence below to enable an alarm for loss of BITS clock. Do this for both OC-12c/STM-4cs.

Select **System Management** ↵
Select **Configuration** ↵
Select **Slot** with OC12 ↵
Select **BITS Clock Alarm Loss** ↵
Choose **Enable** or **Disable** ↵

The recommended configuration for maximum clocking stability is:

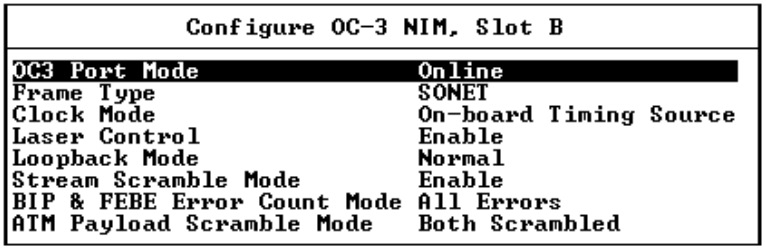
1. Connect BITS sources to NIM I/O A and NIM I/O B.
2. Select a Primary Reference Source (A or B), and then enable the clock loss alarm menu option on each OC-12c/STM-4c.
3. Configure the DS3 SAM to BITS clock mode.

   The equipment connected to the Broadmore 1700 OC-12c/STM-4c interface is configured to BITS and provides a BITS reference clock embedded in the SONET serial stream.
4. The Broadmore 1700 OC-12c/STM-4c Transmit timing option is set to recovered.

   In effect, the Broadmore 1700 OC-12c/STM-4c is loop timed off of the associated SONET device. Configuring the equipment in this manner allows the DS3 SAM to fallback on a clock derived from the associated SONET equipment's BITS, in the event of a BITS clock failure.

OC-3c/STM-1c

Choose the slot (A or B) containing the OC-3c/STM-1c to be configured and a screen similar to the one below will appear. The table below is a guide to parameter configuration.



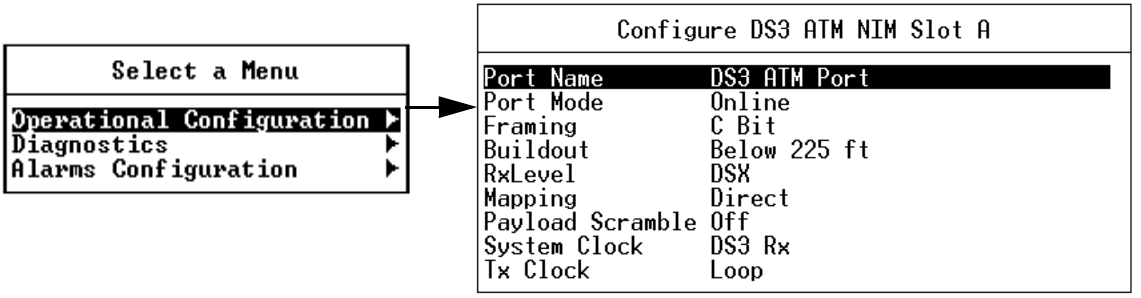
Highlight each item and use the space bar to cycle among available choices. Use the Esc key to return to the main menu after configuration is complete.

Item	Options	Comment
OC-3 Port Mode	**On-line** **Off-line** **Test Download Standby** **Configuration Request** **Broken**	This field is a status indicator and the items displayed are the result of configuration (or other) actions.
Framing Type	**SONET** **SDH**	SDH is for International ITU applications. SONET is for US applications.
Transmit Timing	**Recovered** **BITS** **Local/**	Recovered means from SONET. BITS impedance matching is a hardware function; see NIM Installation, Chapter 3. BITS is tied to the local in-house timing source. Local is on-board Stratum 3 timing source.
SONET Tx	**Enable** **Disable**	If SONET Tx is enabled, framing is enabled; if SONET Tx is disabled, framing is removed (i.e., SONET Tx is all zeroes).
Loopback Mode	**Normal** **Terminal** **Local** **Remote**	Normal is no loopback. Terminal is a loopback from the ATM interface to the user equipment. Local is a loopback before NIM processing to the fiber link. Remote is a loopback from the receive fiber to the ATM before local processing.
ATM Payload Scramble Mode	**Both Scrambled** **Transmit Scrambled** **Receive Scrambled** **No Scramble**	A technique used to prevent a long string of zeros. Both ends of a connection must be configured the same for operation.
BITS Clock Alarm Loss	**Enable** **Disable**	Provides an alarm indication of loss of BITS clock at the NIM IOM when enabled. A corresponding BITS LOS SNMP trap is generated if SNMP is configured properly.

If a second OC-3c/STM-1c is installed, repeat this process for the configuration. Again, follow the module-specific information above, as appropriate.

ATM DS3 NIM

Choose the slot containing the ATM DS3 NIM to be configured to display a screen for port selection. The table below is a guide to configuration parameters. Operational, Diagnostics, and Alarm configuration are the three menu options. Descriptions of the configurable items are found in the following tables. Highlight each item and use the space bar to toggle available options for each. Save the configuration after making changes.



The following tables show the options available for each item, separated by operational configuration, diagnostics, and alarm configuration.

CAUTION! BE CAREFUL NOT TO EXCEED THE ALLOWABLE ATM CELLBUS BANDWIDTH. FOR CONVENIENCE, OPERATORS ARE PERMITTED TO CHANGE PARAMETERS OF VARIABLE BIT RATE MODULES WITHOUT FIRST SETTING THE ACTIVE PVC OFFLINE, BUT THE BROADMORE WILL NOT ISSUE AN ALARM IF THE ALLOWABLE BANDWIDTH IS EXCEEDED. BANDWIDTHS ARE RECALCULATED WHEN A PVC IS SET ONLINE OR WHEN THE OPERATOR DISPLAYS THE BANDWIDTH METER (SEE PAGE 7-25).

Table 7-2: ATM DS3 NIM Configuration Items

Item	Options	Comment
Port Name		A descriptive field to identify the port
Port Mode	**On-line** **Off-line**	This is a status field that can be toggled to **online** or **offline**. When the port is in offline status, it is not available to make calls, and passive SVCs are refused.
Framing	**C-bit** **M13**	**C-bit** is a framing mode using DS2 stuff bits associated with M23 multiplexing for other purposes such as Far End Alarm Control (FEAC) Channel. **M13** is a mode that maps 28 DS1s directly into the DS3. The DS1s do not exist independently as is the case when M23 multiplexing is used.
Buildout	**Below 225 feet** **Above 225 feet**	Cable distance from NIM port to attached equipment. This value determines the signal strength.
RxLevel	**DSX** **Monitor**	**DSX** selects DS3 digital cross-connect levels . **Monitor** selects test set monitor levels.
Mapping	**Direct** **PLCP**	**Direct** is Direct Cell Mapping. **PLCP** is Physical Layer Convergence Protocol.
Payload Scramble	**On** **Off**	A technique used to prevent a long string of zeros. Both ends of a connection must be configured the same for operation.
System Clock	**Internal** **DS3 Rx**	**Internal** sets system clock to internal oscillator. **DS3 Rx** sets system clock to the received DS3 clock.
Tx Clock	**BITS/Internal** **Network Loop**	**BITS/Internal** sets Tx timing to the External BITS clock input or to the Internal Stratum 3e clock source. **Network Loop** derives the Tx clock from the attached DS3 user equipment.

Table 7-3: ATM DS3 NIM Diagnostics Configuration

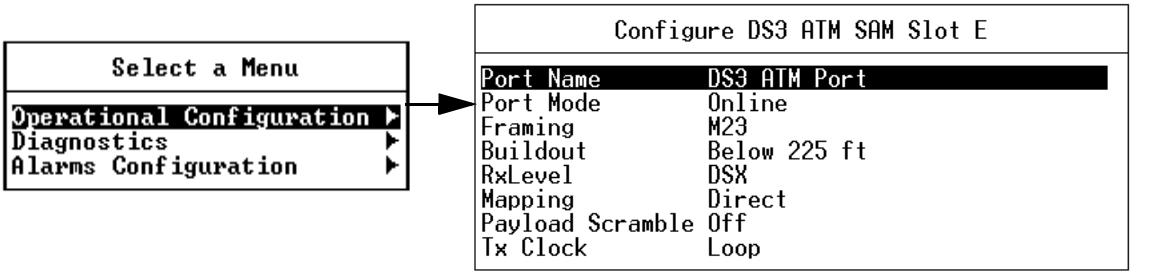
Item	Options	Comments
Loopback	**Normal** **Local** **Remote**	**Normal** is no loopback. **Local** sets a service side loopback (DS3) on the port. **Remote** sets a network side loopback (ATM) on the port.

Table 7-4: ATM DS3 NIM Alarms Configuration

Network Alarms	Options	Service Alarms
ATM Loss HEC	**Ignore/Major/Minor**	Header Error Control.
ATM Tx FIFO Overflow	**Ignore/Major/Minor**	An internal buffer overflow
ATM Rx FIFO Overflow	**Ignore/Major/Minor**	An internal buffer overflow
PLCP OOF	**Ignore/Major/Minor**	Out of Frame
PLCP Yellow	**Ignore/Major/Minor**	Remote Alarm Indicator (RAI)
DS3 LOS	**Ignore/Major/Minor**	Loss of Signal
DS3 OOF	**Ignore/Major/Minor**	Out of Frame
DS3 AIS	**Ignore/Major/Minor**	Remote Alarm Indicator (RAI)
DS3 Idle	**Ignore/Major/Minor**	Idle code received
DS3 FERF	**Ignore/Major/Minor**	Far End Receive Failure
DS3 Red	**Ignore/Major/Minor**	Service side error
BITS Clock Loss	**Ignore/Major/Minor**	BITS clock failure

ATM DS3 SAM

Choose the slot containing the ATM DS3 SAM to be configured to display a screen for port selection. The table below is a guide to configuration parameters. Operational, Diagnostics, and Alarm configuration are the three menu options. Descriptions of the configurable items are found in the following tables. Highlight each item and use the space bar to toggle available options for each. Save the configuration after making changes.



The following tables show the options available for each item, separated by operational configuration, diagnostics, and alarm configuration.

CAUTION! **BE CAREFUL NOT TO EXCEED THE ALLOWABLE ATM CELLBUS BANDWIDTH. FOR CONVENIENCE, OPERATORS ARE PERMITTED TO CHANGE PARAMETERS OF VARIABLE BIT RATE MODULES WITHOUT FIRST SETTING THE ACTIVE PVC OFFLINE, BUT THE BROADMORE WILL NOT ISSUE AN ALARM IF THE ALLOWABLE BANDWIDTH IS EXCEEDED. BANDWIDTHS ARE RECALCULATED WHEN A PVC IS SET ONLINE OR WHEN THE OPERATOR DISPLAYS THE BANDWIDTH METER (SEE PAGE 7-25).**

Table 7-5**: **ATM DS3 SAM Configuration Items

Item	Options	Comment
Port Name		A descriptive field to identify the port
Port Mode	**On-line** **Off-line**	This is a status field that can be toggled to **online** or **offline**. When the port is in offline status, it is not available to make calls, and passive SVCs are refused.
Framing	**C-bit** **M13**	**C-bit** is a framing mode using DS2 stuff bits associated with M23 multiplexing for other purposes such as Far End Alarm Control (FEAC) Channel. **M13** is a mode that maps 28 DS1s directly into the DS3. The DS1s do not exist independently as is the case when M23 multiplexing is used.
Buildout	**Below 225 feet** **Above 225 feet**	Cable distance from NIM port to attached equipment. This value determines the signal strength.
RxLevel	**DSX** **Monitor**	**DSX** selects DS3 digital cross-connect levels . **Monitor** selects test set monitor levels.
Mapping	**Direct** **PLCP**	**Direct** is Direct Cell Mapping. **PLCP** is Physical Layer Convergence Protocol.
Payload Scramble	**On** **Off**	A technique used to prevent a long string of zeros. Both ends of a connection must be configured the same for operation.
Tx Clock	**Internal** **Loop**	**Internal** sets Tx timing to the Internal/System clock. **Loop** derives the Tx clock from the attached DS3 user equipment.

Table 7-6: ATM DS3 SAM Diagnostics Configuration

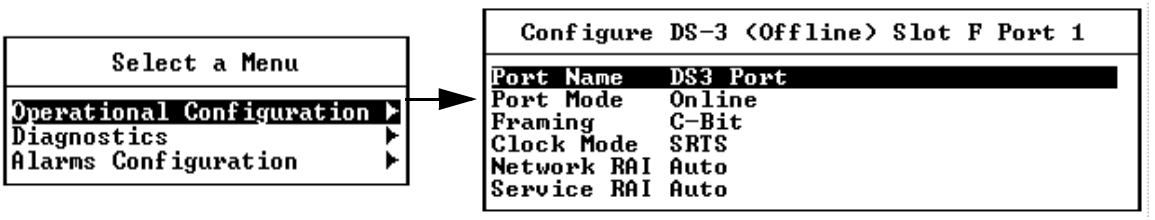
Item	Options	Comments
Loopback	**Normal** **Local** **Remote**	**Normal** is no loopback. **Local** sets a service side loopback (DS3) on the port. **Remote** sets a network side loopback (ATM) on the port.

Table 7-7: ATM DS3 SAM Alarm Configuration

Item	Options	Comments
ATM Loss HEC	**Ignore/Major/Minor**	Header Error Control
ATM Tx FIFO Overflow	**Ignore/Major/Minor**	An internal buffer overflow
ATM Rx FIFO Overflow	**Ignore/Major/Minor**	An internal buffer overflow
PLCP OOF	**Ignore/Major/Minor**	Out of Frame
PLCP Yellow	**Ignore/Major/Minor**	Remote Alarm Indicator (RAI)
DS3 LOS	**Ignore/Major/Minor**	Loss of Signal
DS3 OOF	**Ignore/Major/Minor**	Out of Frame
DS3 AIS	**Ignore/Major/Minor**	Remote Alarm Indicator (RAI)
DS3 Idle	**Ignore/Major/Minor**	Idle code received
DS3 FERF	**Ignore/Major/Minor**	Far End Receive Failure
DS3 Red	**Ignore/Major/Minor**	Service side error
BITS Clock Loss	**Ignore/Major/Minor**	BITS clock failure

Unstructured DS3 SAM

Choose the slot containing the DS3 SAM to be configured to display a screen for port selection. The table below is a guide to configuration parameters. Operational, Diagnostics, and Alarm configuration are the three menu options. Descriptions of the configurable items are found in the following tables. Highlight each item and use the space bar to toggle available options for each. Save the configuration after making changes.



The following tables show the options available for each item, separated by operational configuration, diagnostics, and alarm configuration.

Table 7-8: Unstructured DS3 SAM Configuration Items

Item	Options	Comments
Port Name		A descriptive field to identify the port
Port Mode	**On-line** **Off-line**	This is a status field that can be toggled to **online** or **offline**. When the port is in offline status, it is not available to make calls, and passive SVCs are refused.
Framing	**C-bit** **M13** **Unframed**	**C-bit** is a framing mode using DS2 stuff bits associated with M23 multiplexing for other purposes such as Far End Alarm Control (FEAC) Channel. **M13** is a mode that maps 28 DS1s directly into the DS3. The DS1s do not exist independently as is the case when M23 multiplexing is used. **Unframed** requires a 44.736 Mbps input.
Clock Mode	**SRTS** **Adaptive** **Network** **BITS** **Loop**	**NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.** **SRTS**-This method measures the Service Clock input frequency against a network-wide ATM synchronization clock and sends the difference signals in the AAL1 header to the destination Broadmore 1700 circuit emulation interface. The different signals are then combined with the network-wide ATM synchronization clock to re-create the input service clock. If more than one ATM clock synchronization is present in the network, an alternate timing recovery method must be selected. For this method to work, the ATM network must be synchronized to a single network-wide clock source. User equipment should be configured to internal clock. **Adaptive Timing** –This technique maintains a pre-defined fill level in the Circuit Emulation Re-assembly buffer to control the timing output to user equipment. This method of timing recovery does not require a Synchronous ATM network and is used in applications where network wide synchronization is not available. **(Synchronous) Network**-This method of clock recovery derives timing from the ATM Network and propagates it to the CE Service Interface. The ATM network must be synchronized to a single source clock. User equipment should be configured to recover timing from the Broadmore 1700 CE service interface. **(Synchronous) Loop**-The Broadmore 1700 CE interface will derive clock from the attached DS3 user equipment and transmit from the CE interface at that rate. The user equipment should be configured for Internal timing if the Broadmore 1700 CE service interface is configured for Synchronous loop. **BITS**- (Building Integrated Timing Supply)- distributes standard timing to equipment within the central office. The Broadmore 1700 accepts BITS via either NIM IOM. The fallback for the primary BITS clock is the other BITS clock. The fallback for no BITS is the internal clock of the NIM.

Item	Options	Comments
Network RAI	**Auto** **X-bits 1** **X-bits 0**	Indication (RAI) configuration. Allows user configuration of the X-bits associated with C-bit parity framing. Auto makes it transparent; the other choices set the X-bits to either 1 or 0.
Service RAI	**Auto** **X-bits 1** **X-bits 0**	Indication (RAI) configuration. Allows user configuration of the X-bits associated with C-bit parity framing. Auto makes it transparent; the other choices set the X-bits to either 1 or 0.

Table 7-9: Unstructured DS3 SAM Diagnostics Configuration

Item	Options	Comments
Automatic FEAC Alarms	**Activate/Deactivate**	Activates or deactivates Far End Alarm and Control Channel (FEAC) alarms. FEAC alarms can only be active when the port is configured for C-bit parity framing. Activate to detect and transmit RAI as applicable and detect FEAC channel activate/deactivate commands
Network FEAC Loopback	**Activate/Deactivate**	Activate sends a FEAC command to the far end network equipment to go into network loopback. Deactivate sends a FEAC command to go out of network loopback. Only active when port is configured for C-bit parity. The receive code generated is DS3 Out of Frame (0000000).
Service FEAC Loopback	**Activate/Deactivate**	Activate sends a FEAC command to the far end service equipment to go into service loopback. Deactivate sends a FEAC command to go out of service loopback. Only active when port is configured for C-bit parity.
Network BERT Test	**Activate/Deactivate**	BERT test can only be performed when the port is configured for C-bit parity framing. Causes the port to generate a $2e^{23}$ pseudo-random test pattern to the ATM network.
Service BERT Test	**Activate/Deactivate**	Same as network BERT test except it goes to the service equipment. These tests can be monitored from CAMMI following the selection sequence: *system management → monitor activity →slot statistics→ DS3 SAM → Port # → Port Counters*

Item	Options	Comments
Network AIS	**Activate/Deactivate**	Sends Alarm Indication Signal to the network when activated.
Service AIS	**Activate/Deactivate**	Sends Alarm Indication Signal to the connected local service equipment when activated.
Loopback	**Normal** **Local** **Remote**	Normal is no loopback. Local sets a service side loopback (DS3) on the port. Remote sets a network side loopback (ATM) on the port. See loopback diagram on page 8-26

Table 7-10: Unstructured DS3 SAM Alarm Configuration

Network Alarms	Options	Service Alarms
Cell Starvation	**Ignore/Major/Minor**	LOS
LOF	**Ignore/Major/Minor**	LOF
AIS	**Ignore/Major/Minor**	AIS
Idle	**Ignore/Major/Minor**	Idle
RAI	**Ignore/Major/Minor**	RAI
FEAC	**Ignore/Major/Minor**	FEAC
Sequence Errors	**Ignore/Major/Minor**	Line Code Violation
Excessive SNP Errors	**Ignore/Major/Minor**	Excessive SNP Errors
Excessive F-bit Errors	**Ignore/Major/Minor**	Excessive F-bit Errors
Excessive Parity Errors	**Ignore/Major/Minor**	Excessive Parity Errors
Excessive C-bit Errors	**Ignore/Major/Minor**	Excessive C-bit Errors
Excessive FEBE Errors	**Ignore/Major/Minor**	Excessive FEBE Errors

Follow the sequence below to set network alarms. DS3 alarms are set on a port basis with alarm definitions as shown.

Select **System Management** ↵
Select **Configure** ↵
Select **Slot** ↵
Select **Port** of interest ↵
Select **Alarm Configuration** ↵
Press **Network Alarms** or **Service Alarms** ↵
Use the space bar to toggle to the available options and press **Esc** to save the settings.

Table 7-11: Network Alarm Definitions

Network Alarm	Definition
Cell Starvation	Cell Starvation indicates there are no cells being received from the network side.
LOF	Loss Of Framing indicates that the framing alignment of the signal coming into the Broadmore 1700 from the ATM side has been lost.
AIS	This alarm indicates an Alarm Indication Signal is being received from the network side. When a network element receives a loss of signal, it is supposed to propagate an AIS alarm on its output. If this alarm is active, then an AIS alarm is being propagated to it. An AIS alarm is a validly framed DS3 signal consisting of a repeated 1010 data pattern.
Idle	An idle alarm means that an idle signal (validly framed DS3 signal with a repeated 1100 signal) is being detected on the network side.
RAI	The Remote Alarm Indicator (also known as a Yellow Alarm) is transmitted by setting the X bits to zero. This particular alarm indicates that a Yellow alarm has been sent across the ATM network and is being received by this DS3 port. In the DS3 port configuration screen, you can select the Network and Service side RAI as Auto, 1, or 0. Selecting “0” will cause the network RAI alarm to be activated, if the user has not selected to Ignore it.
FEAC	Far End Alarm and Control Channel.

Network Alarm	Definition
Sequence Errors	This alarm indicates that frames are out of sequence as they arrive at the DS3 port.
Excessive SNP Errors	SNP Errors are errors that occur in the Sequence Number Parity portion of the DS3 frame
Excessive F-bit Errors	The Excessive F-bit Errors alarm is triggered when the DS3 port receives F-bit errors at a rate of 1×10^{-4}
Excessive Parity Errors	The Excessive Parity Errors alarm will be triggered when the DS3 port receives parity errors at a rate of 1×10^{-4}
Excessive C-bit Errors	The Excessive C-bit Errors alarm will be triggered when the DS3 port receives C-bit errors at a rate of 1×10^{-4}
Excessive FEBE Errors	The Excessive FEBE (Far End Block Error) alarm is triggered when the DS3 port receives FEBEs at a rate of 1×10^{-4}

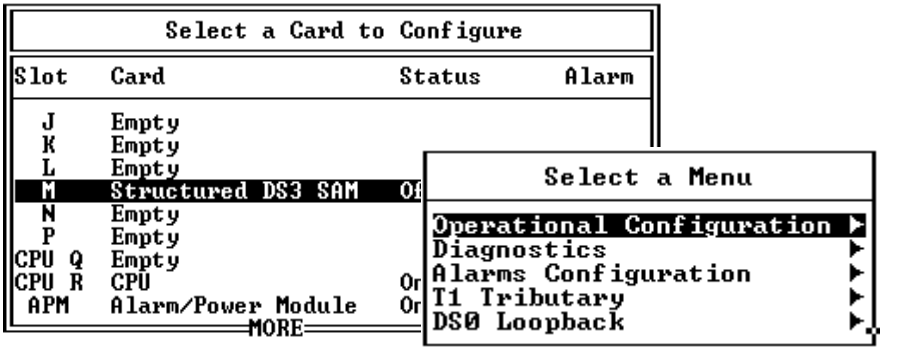
Table 7-12: Service Alarm Definitions

Service Alarm	Definition
LOS	Loss Of Signal indicates that there is no signal being input to the DS3 from the service side.
LOF	Loss of Framing indicates that the framing alignment of the signal coming into the Broadmore 1700 from the ATM side has been lost.
AIS	This alarm indicates that the DS3 port is receiving an Alarm Indication Signal from the attached equipment. AIS is a validly framed DS3 signal consisting of a repeated 1010 data pattern.
Idle	An idle alarm means that an idle signal (validly framed DS3 signal with a repeated 1100 signal) is being detected on the service side.
RAI	The Remote Alarm Indicator (also known as a Yellow Alarm) is transmitted by setting the X bits to zero. In this instance, the service side equipment is transmitting the RAI to the DS3 port.
FEAC	Far End Alarm and Control Channel.

Service Alarm	Definition
Line Code Violation	This alarm indicates that a long string of zeros is being received by the DS3 port from any attached equipment.
Excessive F-bit Errors	The Excessive F-bit Errors alarm is triggered when the DS3 port receives F-bit errors at a rate of 1×10^{-4}
Excessive Parity Errors	The Excessive Parity Errors alarm will be triggered when the DS3 port receives parity errors at a rate of 1×10^{-4}
Excessive C-bit Errors	The Excessive C-bit Errors alarm will be triggered when the DS3 port receives C-bit errors at a rate of 1×10^{-4}
Excessive FEBE Errors	The Excessive FEBE (Far End Block Error) alarm is triggered when the DS3 port receives FEBEs at a rate of 1×10^{-4}

Structured DS3 SAM

Choose the slot containing the structured DS3 SAM to be configured to display a screen for port selection. The table below is a guide to configuration parameters. Operational, Diagnostics, Alarm, T1 Tributary, and DS0 configuration are the menu options. Descriptions of the configurable items are found in the following tables. Highlight each item and use the space bar to toggle available options for each, and then save the configuration after making changes.



Use the following steps for configuring the SDS3.

Select **System Management** ↵
Select **Configure** ↵
Select **Slot** ↵
Select **Structured DS3 SAM** ↵
Select **one** of the following:
- *** Diagnostics** ↵
- *** T1 Tributary** ↵
- *** DS0 Loopback** ↵
- *** Operational Configuration** ↵
- *** Alarms Configuration** ↵

Use the space bar to toggle to the available options and press **Esc** to save the settings.

Table 7-13*: *Structured DS3 SAM Operational Configuration

Item	Options	Description
Port Name		A descriptive field to identify the port
Port Mode	**On-line** **Off-line**	This is a status field that can be toggled to offline, in which case the port will not be available to make calls and passive SVCs are refused.
Framing	**C-bit** **M13**	**C-bit** is a framing mode using DS2 stuff bits associated with M13 multiplexing for other purposes such as Far End Alarm Control (FEAC) Channel. **M13** is a mode that maps 28 DS1s directly into the DS3. The DS1s do not exist independently as is the case when M23 multiplexing is used.
Clock Mode	**Network** **BITS-Clock** **Loop**	**(Synchronous) Network**-This method of clock recovery derives timing from the ATM Network and propagates it to the CE Service Interface. The ATM network must be synchronized to a single source clock. User equipment should be configured to recover timing from the Broadmore 1700 CE service interface. **BITS-Clock** (Building Integrated Timing Supply)-distributes standard timing to equipment within the central office. The Broadmore 1700 accepts BITS via either NIM IOM. The fallback for the primary BITS clock is the other BITS clock. The fallback for no BITS is the internal clock of the NIM. **(Synchronous) Loop**-The Broadmore 1700 CE interface will derive clock from the attached SDS3 user equipment and transmit from the CE interface at that rate. The user equipment should be configured for Internal timing if the Broadmore 1700 CE service interface is configured for Synchronous loop.
Loopback	**Normal** **Local** **Remote**	Normal Mode is no loopback. Local Line Loopback sets a loopback to the service equipment.Remote Loopback sets a loopback to the ATM network.
Service RAI	**Auto** **X-bits 1** **X-bits 0**	Indication (RAI) configuration. Allows user configuration of the X-bits associated with C-bit parity framing. Auto makes it transparent; the other choices set the X-bits to either 1 or 0.

Table 7-14: Structured DS3 SAM Diagnostics Configuration

Item	Options	Description
Port Name		A descriptive field to identify the port
Port Mode	**On-line** **Off-line**	This is a status field that can be toggled to offline, in which case the port will not be available to make calls and passive SVCs are refused.
Framing	**C-bit** **M13**	**C-bit** is a framing mode using DS2 stuff bits associated with M13 multiplexing for other purposes such as Far End Alarm Control (FEAC) Channel. **M13** is a mode that maps 28 DS1s directly into the DS3. The DS1s do not exist independently as is the case when M23 multiplexing is used.
Clock Mode	**Network** **BITS-Clock** **Loop**	**(Synchronous) Network**-This method of clock recovery derives timing from the ATM Network and propagates it to the CE Service Interface. The ATM network must be synchronized to a single source clock. User equipment should be configured to recover timing from the Broadmore 1700 CE service interface. **BITS-Clock** (Building Integrated Timing Supply)-distributes standard timing to equipment within the central office. The Broadmore 1700 accepts BITS via either NIM IOM. The fallback for the primary BITS clock is the other BITS clock. The fallback for no BITS is the internal clock of the NIM. **(Synchronous) Loop**-The Broadmore 1700 CE interface will derive clock from the attached SDS3 user equipment and transmit from the CE interface at that rate. The user equipment should be configured for Internal timing if the Broadmore 1700 CE service interface is configured for Synchronous loop.
Loopback	**Normal** **Remote**	Normal Mode is no loopback. Remote Loopback sets a loopback to the service side.
Service RAI	**Auto** **X-bits 1** **X-bits 0**	Indication (RAI) configuration. Allows user configuration of the X-bits associated with C-bit parity framing. Auto makes it transparent; the other choices set the X-bits to either 1 or 0.

Item	Options	Description
Automatic FEAC Alarms	**Activate/Deactivate**	Activates or deactivates Far End Alarm and Control Channel (FEAC) alarms. FEAC alarms can only be active when the port is configured for C-bit parity framing. Activate to detect and transmit RAI as applicable and detect FEAC channel activate/deactivate commands
Service FEAC Loopback	**Activate/Deactivate**	Activate sends a FEAC command to the far end service equipment to go into service loopback. Deactivate sends a FEAC command to go out of service loopback. Only active when port is configured for C-bit parity.
Service AIS	**Activate/Deactivate**	Sends Alarm Indication Signal to the connected local service equipment when activated.

Table 7-15: Structured DS3 SAM Alarms

Service Alarms	Options
LOS	**Ignore/Major/Minor**
LOF	**Ignore/Major/Minor**
AIS	**Ignore/Major/Minor**
Idle	**Ignore/Major/Minor**
RAI	**Ignore/Major/Minor**
FEAC	**Ignore/Major/Minor**
Line Code Violation	**Ignore/Major/Minor**
Excessive F-bit Errors	**Ignore/Major/Minor**
Excessive Parity Errors	**Ignore/Major/Minor**
Excessive C-bit Errors	**Ignore/Major/Minor**
Excessive FEBE Errors	**Ignore/Major/Minor**

Follow the sequence below to set service alarms. DS3 alarms are set on a port basis with alarm definitions as shown.

Select **System Management** ↵
Select **Configure** ↵
Select **Slot** ↵
Select **Structured DS3 SAM** ↵
Select **Alarms Configuration** ↵
Select from the list of **Alarms** ↵
Use the space bar to toggle to the available options and press **Esc** to save the settings.

Table 7-16: Structured DS3 Service Alarm Definitions

Service Alarm	Definition
LOS	Loss Of Signal indicates that there is no signal being input to the DS3 from the service side.
LOF	Loss of Framing indicates that the framing alignment of the signal coming into the Broadmore 1700 from the ATM side has been lost.
AIS	This alarm indicates that the DS3 port is receiving an Alarm Indication Signal from the attached equipment. AIS is a validly framed DS3 signal consisting of a repeated 1010 data pattern.
Idle	An idle alarm means that an idle signal (validly framed DS3 signal with a repeated 1100 signal) is being detected on the service side.
RAI	The Remote Alarm Indicator (yellow alarm) is transmitted by setting the X bits to zero. In this instance, the service side equipment is transmitting the RAI to the DS3 port.
FEAC	Far End Alarm and Control Channel.
Line Code Violation	This alarm indicates that a long string of zeros is being received by the DS3 port from any attached equipment.

Service Alarm	Definition
Excessive F-bit Errors	The Excessive F-bit Errors alarm is triggered when the DS3 port receives F-bit errors at a rate of 1 x 10^{-4}
Excessive Parity Errors	The Excessive Parity Errors alarm will be triggered when the DS3 port receives parity errors at a rate of 1 x 10^{-4}
Excessive C-bit Errors	The Excessive C-bit Errors alarm will be triggered when the DS3 port receives C-bit errors at a rate of 1 x 10^{-4}
Excessive FEBE Errors	The Excessive FEBE (Far End Block Error) alarm is triggered when the DS3 port receives FEBEs at a rate of 1 x 10^{-4}

Table 7-17: Structured DS3 SAM DS1 Tributary Configuration

Item	Options	Description
Timing Recovery Service Clocking Mode (Refer to "DS1/E1 N×64 Service Clock Options" on page 4-51 for more details on these options.)	**SRTS Timing** **Synchronous Network** **Synchronous Loop**	Each option specifies a method of clock recovery that will be used for all recovery circuits on the port. **NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.**
Loopback	**Normal** **Remote**	Normal Mode is no loopback. Remote Loopback sets a loopback to the service side.
CBR Service Type	**Structured No CAS** **Structured with CAS** **Unstructured**	Structured No CAS allows DS0 (fractional) allocation without Channel Associated Signalling (CAS). Structured with CAS allows DS0 allocation with CAS enabled, using robbed-bit signalling. Unstructured assigns all the timeslots to one VP/VC. Structured No CAS and Structured with CAS can assign a timeslot or group of timeslots to a VP/VC. Unstructured allocates all 24 DS0s to the DS1, using the entire DS1 bandwidth regardless of data content.

Item	Options	Description
Frame Type	**Unframed** **Extended Superframe** **Superframe**	No framing occurs. Superframe groups 12 DS1 frames together. Extended Superframe groups 24 DS1 frames together. The equipment attached must be set to the same frame type as chosen here.
BERT	**Off** **21e-11 PRBS** **21e-15 PRBS** **21e-20 PRBS** **21e-23 PRBS**	BERT test can only be performed when the port is configured for C-bit parity framing. This causes the port to generate pseudo-random test patterns to the service side DS3 physical connection.

Table 7-18: Structured DS3 SAM DS0 Loopback Tributary Configuration

Item	Options	Description
DS0-0 (0-23)	**Normal** **Remote**	Normal Mode is no loopback. Remote Loopback sets a loopback to the service side.

DS1 Nx64 SAM

Choose the slot (C to P) containing the DS1 N×64 SAM to be configured and a screen for port selection appears. Select a port to configure and a specific port configuration screen, shown below, appears. The table below is a guide to parameter configuration. Each port on the N×64 SAM is configured separately.

```
        Configure NX64 (Offline) Slot C Port 1

Port Name                   Port 1
Port Mode                   Online
Service Clocking Mode       SRTS Timing
Clock Recovery Type         Undefined
Clock Recovery Interface    Undefined
Line Impedence              75 Ohms
Loopback                    Normal Mode
CBR Service Type            Unstructured
Frame Type                  Direct (Unstructured)
CBR Signal Format           HDB3
```

Table 7-19: DS1 Nx64 SAM Configuration Items

Item	Options	Description
Port Name		A descriptive field to identify the port
Port Mode	**On-line** **Off-line**	This is a status field that can be toggled to offline, in which case the port will not be available to make calls and passive SVCs are refused.
Service Clocking Mode (Refer to "DS1/E1 N×64 Service Clock Options" on page 4-51 for more details on these options.)	**Adaptive Timing** **Synchronous Network** **Synchronous Loop** **Synchronous NIM** **Synchronous Port** **SRTS Timing**	Each option specifies a method of clock recovery that will be used for all recovery circuits on the port. **NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.**
Clock Recovery Type (Used when Service Clocking Mode is Synchronous Port)	**Undefined** **Master** **Slave**	Selected by default when any Service Clocking Mode other than Synchronous Port is selected Selected when the clock is derived from the network or the service interface, and is propagated to the other SAM ports Selected when the clock is derived from another port on the SAM
Clock Recovery Interface (Used when Service Clocking Mode is Synchronous Port and Clock Recovery Type is Master)	**Undefined** **Transmit Line** **Receive Line**	Selected when any Service Clocking Mode other than Synchronous Port is selected; also when Synchronous Port is selected and Clock Recovery Type is Slave Selected when the clock is derived from the network cell stream Selected when the clock is derived from the service input
Line Buildout	**0-110 feet** **110-220 feet** **220-330 feet** **330-440 feet** **440-550 feet** **550-660 feet**	Cable distance from SAM port to attached equipment. This value determines the signal strength.
Loopback	**Normal** **Local** **Remote**	Normal Mode is no loopback. Local Line Loopback sets a loopback to the service equipment. Remote Loopback sets a loopback to the ATM network.

Item	Options	Description
CBR Service Type	**Structured No CAS** **Structured with CAS** **Unstructured**	Structured No CAS allows DS0 (fractional) allocation without Channel Associated Signalling (CAS). Structured with CAS allows DS0 allocation with CAS enabled, using robbed-bit signalling. Unstructured assigns all the timeslots to one VP/VC. Structured No CAS and Structured with CAS can assign a timeslot or group of timeslots to a VP/VC. Unstructured allocates all 24 DS0s to the DS1, using the entire DS1 bandwidth regardless of data content.
Frame Type	**Superframe** **Extended Superframe**	Superframe groups 12 DS1 frames together. Extended Superframe groups 24 DS1 frames together. The equipment attached must be set to the same frame type as chosen here.
CBR Signal Format	**B8ZS** **AMI**	Used to maintain ones density. B8ZS substitutes a known pattern of ones for zeros. AMI reverses the polarity of alternate ones, or marks to accomplish the same thing. Attached equipment must agree with this configuration setting.

Repeat this configuration for each N×64 SAM port to be connected. The Broadmore 1700 automatically provides default configuration values for each port and these are the values displayed when you initially configure a port.

E1 Nx64 SAM

Follow the same procedures as for the DS1 N×64 Module configuration. The major difference is that each E1 SAM port contains 32 64kbps channels as compared to the 24 64kbps channels in a DS1 port. In other words, the total bandwidth for an E1 port is 2.048Mbps compared to 1.544Mbps for a DS1 port. Structured service uses channel 0 for framing (PCM31). Structured service with CCS uses channel 16 for signaling along with channel 0 framing (PCM 30). Unstructured service uses all 32 channels together. Port configuration items are shown on the next page.

Table 7-20: E1 Nx64 Port Configuration Items

Configuration Item	Options	Description
Port Name		A descriptive field to identify the port
Port Mode	**Online** **Offline**	This is a status field that can be toggled to offline, in which case the port will not be available to make calls and passive SVCs are refused.
Service Clocking Mode (Refer to "DS1/E1 N×64 Service Clock Options" on page 4-51 for more details on these options)	**Adaptive Timing** **Synchronous Network** **Synchronous Loop** **Synchronous NIM** **Synchronous Port** **SRTS Timing**	Each option specifies a method of clock recovery that will be used for all recovery circuits on the port. **NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.**
Clock Recovery Type (Used when Service Clocking Mode is Synchronous Port)	**Undefined** **Master** **Slave**	Selected by default when any Service Clocking Mode other than Synchronous Port is selected. Selected when the clock is derived from the network or the service interface, and is propagated to the other SAM ports. Selected when the clock is derived from another port on the SAM.
Clock Recovery Interface (Used when Service Clocking Mode is Synchronous Port and Clock Recovery Type is Master)	**Undefined** **Transmit Line** **Receive Line**	Selected when any Service Clocking Mode other than Synchronous Port is selected; also when Synchronous Port is selected and Clock Recovery Type is Slave. Selected when the clock is derived from the network cell stream. Selected when the clock is derived from the service input.

Configuration Item	Options	Description
Line Impedance	**75 Ohms** **120 Ohms**	Set to match service equipment line impedance.
Loopback	**Normal Mode** **Local Line Loopback** **Remote Loopback**	Normal Mode is no loopback. Local Line Loopback sets a loopback to the service equipment. Remote Loopback sets a loopback to the ATM network.
CBR Service Type (Refer to "DS1/E1 N×64 Service Clock Options" on page 4-51 for more details on these options)	 **Unstructured** **Structured No CAS** **Structured with CAS**	These options select the type of cell coding that the port will use to segment and reassemble data. Unstructured allocates all 32 DS0s to the E1, using the entire E1 bandwidth regardless of data content. This option assigns all timeslots to one VP/VC. Structured No CAS allows DS0 (fractional) allocation without Common Channel Signalling (CCS), using channel 0 for framing information. This option assigns a single timeslot or a group of timeslots to a VP/VC. Structured with CAS allows DS0 allocation with CCS enabled, using channel 16 for the signalling information and channel 0 for framing information. This option assigns a single timeslot or a group of timeslots to a VP/VC.
Frame Type	**Direct (Unstructured)** **CRC4 or Non-CRC4**	Selected when the CBR Service Type is unstructured E1 equipment uses CRC +4 for additional protection against simulation of the frame alignment signal; also for enhanced monitoring capability.
CBR Signal Format	**HDB3** **AMI**	This option specifies the line coding of the port. HDB3 line coding substitutes a known pattern of ones (with bipolar violations) for a group of four zeros.

NOTE: The E1 needs an E1 version of the OC-3c/STM-1c to operate in BITS timing mode.

Table 7-21: DS1/E1 Nx64 Service Clock Options

Option	Definition	When to select
Adaptive Timing	This technique does not require a network-wide synchronization signal to regenerate the input Service Interface Clock at the destination Broadmore 1700 circuit emulation interface. This mode monitors a predefined fill level in the reassemble buffer at that depth. The penalty is an increase in jitter.	Unstructured and Structured Service modes.
Synchronous Network	This method requires that the ATM network be synchronized to a single network-wide clock source. This mode generates a T1 or E1 signal clock synthesized from the network clock. When using this option, the T1 or E1 user equipment should be configured to recover clock from the Broadmore 1700 Service Interface.	Preferred method of timing recovery for ATM networks.
Synchronous Loop	The Broadmore 1700 CE Service Interface will derive clock from the attached T1/E1 user equipment, and transmit bits from the CE Service Interface at the same rate.s	For any service type where the Service Interface clocks are traceable to the same Primary Reference clock
Synchronous NIM	The clock source is derived from the NIM's menu selection, Transmit Timing. The options are Received, which derives clock from the line input of the ATM interface, and Local/BITS, which derives clock from an external Primary Reference Source.	Unstructured and Structured Service modes.
Synchronous Port	The derivation of a clock source depends upon the entry selected for menu item, Clock Recovery Type. Selecting Master will enable the port to derive and propagate the clock to other ports on the SAM that are configured for Service Clocking Mode, Synchronous Port. Selecting Slave will enable the port to derive clock from a Master port on the same SAM.	Unstructured and Structured Service modes. Enabling any port in a group (1-4 or 5-8) to be the Master will make the other ports in the group Slaves.

Option	Definition	When to select
SRTS (Synchronous Residual Time Stamp)	**NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.** This method measures the Service Clock input frequency against a network-wide ATM synchronization clock and sends the difference signals in the AAL1 header to the destination Broadmore 1700 circuit emulation interface. The different signals are then combined with the network-wide ATM synchronization clock to re-create the input service clock. If more than one ATM clock synchronization is present in the network, an alternate timing recovery method must be selected. For this method to work, the ATM network must be synchronized to a single network-wide clock source.	With Unstructured Service only

Table 7-22: DS1/E1 Nx64 CBR Service Type Options

Option	Definition	When to select
Unstructured	This option does not rely on a particular data format. Bits received at the service interface from the user equipment are packed into cells without regard to framing. Boundaries for channel and frame alignment are transparent to the ATM service. A bit pattern (2.048 or 1.544 Mbps) sent in at one end is delivered unchanged at the destination.	The entire bandwidth, including framing bits, is assigned to one Virtual Circuit (VC). (Can be used for PCM 30 or PCM 31, and SDT is not required)
Structured No CAS	With this option, the ATM service interface must be able to identify, group, and assign each timeslot or group of timeslots a VC. The service interface uses the framing bit to detect channel boundaries. The selected channels are packed into cells with an additional control byte (a Structure Pointer) added in some cells. The SP indicates to the destination receiver the start of a block of bytes that begins with the lowest channel selected from the Channel Setup menu for a particular PVC.	This ATM service emulates a point-to-point fractional T1/E1 circuit without Channel Associated Signaling (CAS)

Option	Definition	When to select
Structured CAS	This option emulates a point-to-point fractional E1 Circuit that requires T1/E1 CAS support. CAS is required for voice applications to send line status (on-hook or off-hook). An additional block of bytes is appended to the payload. These bytes contain signaling information for the selected channels.	With any voice application that requires structured or fractional T1/ E1 service.

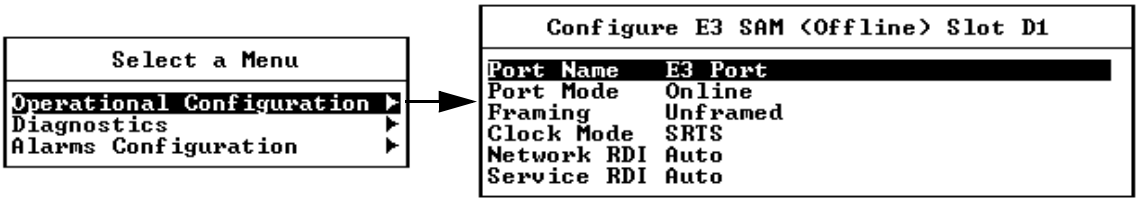
Select the slot containing the configured E1 SAM to display a statistics window showing transmit and receive cells, as well as the following alarms.

Table 7-23: DS1/E1 Nx64 Alarms

Alarm	Description
LOS (Loss of Signal)	A service interface error caused by a loss of carrier for 31 consecutive bits.
LCM (Line Code Mismatch)	The AMI/B8ZS Line Code menu selection does not match the configuration of the service side equipment.
Yellow (Section Alarm Indication Signal)	A service side error indicating that the upstream equipment connected to the T1/ E1 N×64 SAM has an Out-of-Frame alarm.
Red OOF (Red Out-of-Frame)	A service side error indicating that the receiver has lost frame alignment.
Line AIS	An error indicating that the upstream equipment has an Out-of-Frame error before the termination point.
Cell Loss	An ATM Network side error indicating that cells are not being received from the network.
Rx FIFO Overflow	An internal buffer overflow
Tx FIFO Overflow	An internal buffer overflow
PTA Reframe	An internal error

Unstructured E3-3 SAM

Choose the slot containing the Unstructured E3 SAM to be configured to display a screen for port selection. The table below is a guide to configuration parameters. Operational, Diagnostics, and Alarm configuration are the three menu options. Descriptions of the configurable items are found in the following tables. Highlight each item and use the space bar to toggle available options for each. Save the configuration after making changes.



The next three pages show the options available for each item, separated by operational configuration, diagnostics, and alarm configuration.

Table 7-24: Unstructured E3-3 SAM Operational Configuration

Item	Options	Comments
Port Name		A descriptive field to identify the port
Port Mode	**On-line** **Off-line**	This is a status field that can be toggled to **off-line,** in which case the port will not be available to make calls and passive SVCs are refused.
Framing	**G.751** **G.832** **Unframed**	**G.751** is a European framing standard used for PDH applications. **G.832** is a European framing standard set by the ITU for transporting SDH elements on PDH network. **Unframed** requires a 44.736 Mbps input.
Clock Mode	**SRTS** **Adaptive** **Network** **BITS Clock** **Loop**	**NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.** **SRTS** measures the Service Clock input frequency against a network-wide ATM synchronization clock and sends the difference signals in the AAL1 header to the destination Broadmore 1700 circuit emulation interface. The different signals are then combined with the network-wide ATM synchronization clock to re-create the input service clock. If more than one ATM clock synchronization is present in the network, an alternate timing recovery method must be selected. For this method to work, the ATM network must be synchronized to a single network-wide clock source. User equipment should be configured to internal clock. **Adaptive Timing** maintains a pre-defined fill level in the Circuit Emulation Re-assembly buffer to control the timing output to user equipment. This method of timing recovery does not require a Synchronous ATM network and is used in applications where network wide synchronization is not available. **(Synchronous) Network** method of clock recovery derives timing from the ATM Network and propagates it to the CE Service Interface. The ATM network must be synchronized to a single source clock. User equipment should be configured to recover timing from the Broadmore 1700 CE service interface. **(Synchronous) Loop** CE interface will derive clock from the attached DS3 user equipment and transmit from the CE interface at that rate. The user equipment should be configured for Internal timing if the Broadmore 1700 CE service interface is configured for Synchronous loop. **BITS-** (Building Integrated Timing Supply) distributes standard timing to equipment within the central office. The Broadmore 1700 accepts BITS via either NIM IOM. The fallback for the primary BITS clock is the other BITS clock. The fallback for no BITS is the internal clock of the NIM.

Item	Options	Comments
Network RDI	**Auto** **X-bits 1** **X-bits 0**	A Remote Defect Indication (RDI) is LOS or LOF detected on the attached equipment of the network side. **Auto** sets the RDI to automatically reflect whether or not an error is present. **1** sets an error to occur whether one is present or not. **0** indicates no errors even if one is present.
Service RDI	**Auto** **X-bits 1** **X-bits 0**	A Remote Defect Indication (RDI) is LOS or LOF detected on the attached equipment of the network side. **Auto** sets the RDI to automatically reflect whether or not an error is present. **1** sets an error to occur whether one is present or not. **0** indicates no errors even if one is present.

NOTE: The Unstructured E3 SAM needs an E1 version of the OC-3c/ STM-1c to operate in BITS timing mode.

Table 7-25: Unstructured E3-3 SAM Diagnostics Configuration

Item	Options	Comment
Network BERT Test	**Activate/Deactivate**	BERT test can only be performed when the port is configured for C-bit parity framing. Causes the port to generate a $2e^{23}$ pseudo-random test pattern to the ATM network.
Service BERT Test	**Activate/Deactivate**	Same as network BERT test except it goes to the service equipment. These tests can be monitored from CAMMI following the selection sequence: *system management* → *monitor activity* →*slot statistics*→ *DS3 SAM* → *Port #* → *Port Counters*
Network AIS	**Activate/Deactivate**	Sends Alarm Indication Signal to the network when activated.
Service AIS	**Activate/Deactivate**	Sends Alarm Indication Signal to the connected local service equipment when activated.
Loopback	**Normal** **Local** **Remote**	Normal is no loopback. Local sets a service side loopback (DS3) on the port. Remote sets a network side loopback (ATM) on the port. See loopback diagram on page 8-26

Table 7-26: Unstructured E3-3 SAM Alarm Configuration

Network Alarms	Options	Service Alarms
Cell Starvation	**Ignore/Major/Minor**	LOS
LOF	**Ignore/Major/Minor**	LOF
AIS	**Ignore/Major/Minor**	AIS
RDI	**Ignore/Major/Minor**	RDI
Sequence Errors	**Ignore/Major/Minor**	Line Code Violation
Excessive SNP Errors	**Ignore/Major/Minor**	Excessive F-bit Errors
Excessive F-bit Errors	**Ignore/Major/Minor**	Excessive Parity Errors
Excessive Parity Errors	**Ignore/Major/Minor**	Excessive FEBE Errors
Excessive FEBE Errors	**Ignore/Major/Minor**	

Follow the sequence below to set network alarms. DS3 alarms are set on a port basis with alarm definitions as shown.

Select **System Management** ↵
Select **Configure** ↵
Select **Slot** with DS3 ↵
Select **Port** of interest ↵
Select **Alarm Configuration** ↵
Select **Network Alarms** ↵
Adjust settings using the spacebar ↵
Select **Escape** and save settings as desired ↵
Press **Service Alarms** ↵
Repeat the procedure to set and save Service Alarms ↵

Table 7-27: Unstructured E3-3 SAM Network Alarms

Network Alarm	Definition
Cell Starvation	Cell Starvation indicates there are no cells being received from the network side.
LOF	Loss Of Framing indicates that the framing alignment of the signal coming into the Broadmore 1700 from the ATM side has been lost.
AIS	This alarm indicates an Alarm Indication Signal is being received from the network side. When a network element receives a loss of signal, it is supposed to propagate an AIS alarm on its output. If this alarm is active, then an AIS alarm is being propagated to it. An AIS alarm is a validly framed DS3 signal consisting of a repeated 1010 data pattern.
Idle	An idle alarm means that an idle signal (validly framed DS3 signal with a repeated 1100 signal) is being detected on the network side.
RAI	The Remote Alarm Indicator (also known as a Yellow Alarm) is transmitted by setting the X bits to zero. This particular alarm indicates that a Yellow alarm has been sent across the ATM network and is being received by this DS3 port. In the DS3 port configuration screen, you can select the Network and Service side RAI as Auto, 1, or 0. Selecting “0” will cause the network RAI alarm to be activated, if the user has not selected to Ignore it.
FEAC	Far End Alarm and Control Channel.
Sequence Errors	This alarm indicates that frames are out of sequence as they arrive at the DS3 port.
Excessive SNP Errors	SNP Errors are errors that occur in the Sequence Number Parity portion of the DS3 frame
Excessive F-bit Errors	The Excessive F-bit Errors alarm is triggered when the DS3 port receives F-bit errors at a rate of 1×10^{-4}
Excessive Parity Errors	The Excessive Parity Errors alarm will be triggered when the DS3 port receives parity errors at a rate of 1×10^{-4}
Excessive C-bit Errors	The Excessive C-bit Errors alarm will be triggered when the DS3 port receives C-bit errors at a rate of 1×10^{-4}
Excessive FEBE Errors	The Excessive FEBE (Far End Block Error) alarm is triggered when the DS3 port receives FEBEs at a rate of 1×10^{-4}

Table 7-28: Unstructured E3-3 SAM Service Alarms

Service Alarm	Definition
LOS	Loss Of Signal indicates that there is no signal being input to the DS3 from the service side.
LOF	Loss of Framing indicates that the framing alignment of the signal coming into the Broadmore 1700 from the ATM side has been lost.
AIS	This alarm indicates that the DS3 port is receiving an Alarm Indication Signal from the attached equipment. AIS is a validly framed DS3 signal consisting of a repeated 1010 data pattern.
Idle	An idle alarm means that an idle signal (validly framed DS3 signal with a repeated 1100 signal) is being detected on the service side.
RAI	The Remote Alarm Indicator (also known as a Yellow Alarm) is transmitted by setting the X bits to zero. In this instance, the service side equipment is transmitting the RAI to the DS3 port.
FEAC	Far End Alarm and Control Channel.
Line Code Violation	This alarm indicates that a long string of zeros is being received by the DS3 port from any attached equipment.
Excessive F-bit Errors	The Excessive F-bit Errors alarm is triggered when the DS3 port receives F-bit errors at a rate of 1×10^{-4}
Excessive Parity Errors	The Excessive Parity Errors alarm will be triggered when the DS3 port receives parity errors at a rate of 1×10^{-4}
Excessive C-bit Errors	The Excessive C-bit Errors alarm will be triggered when the DS3 port receives C-bit errors at a rate of 1×10^{-4}
Excessive FEBE Errors	The Excessive FEBE (Far End Block Error) alarm is triggered when the DS3 port receives FEBEs at a rate of 1×10^{-4}

Multi Bit Rate (MBR) SAM

Choose the slot (C to P) containing Multiple Bit Rate (MBR) SAM to be configured and a screen for port selection appears. Select a port to configure and a port configuration screen appears. The following table is a guide to parameter configuration. Each port on the MBR SAM is configured separately. The configurable items shown will vary based upon the physical interface chosen, so items from the table may not appear on the screen.

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Table 7-29: MBR SAM Configuration Items

Item	Options	Comments
Port Name	None	This is a user defined field. Enter text to clearly identify the port for your own use.
Port Mode	Online Offline	This a status field. Choices are not selectable.
Bits Per Second	75-10000000	Enter the speed for the attached signal input from 75 bps to 10 Mbps in 1 bps increments.
Physical Interface	RS-232 RS-449 EIA-530A EIA-530 V.35 No Cable Not Used	Toggle to the desired selection.

Item	Options	Comments
Operational Mode	Synchronous Asynchronous	Toggle to the desired selection.
Terminal Type	DTE DCE DTE Line E DCE Line E	Toggle to the correct selection. Choose the terminal type from the previous (Select a Terminal Type) menu. Note: Line E is no longer used or supported.
Data Polarity	Normal, Inverted	Toggle to the desired selection (input and output). This option inverts (complements) the ones and zeros in the data.
Clock Polarity	Normal, Inverted	Toggle to the desired selection (input and output). This option changes the phase of the clock signal with respect to the data. Clock Polarity and Transmit Timing apply to Synchronous mode and are ignored for Asynchronous mode.
Network Timing Recovery	Synchronous Adaptive Loop ST	Synchronous uses the network as a common timing source for input or output data. Adaptive is used for external timing when there is no common timing source. Loop uses the clock recovered from the received data as the transmit timing source. ST (Send Transmit) is used for timing recovery and is only available in DTE mode.
Tx Payload Size	1-47	Tx and Rx Payload determine the number of bytes per cell (1-47). It is used to reduce latency in low-bit-rate connections. Tx to Rx Payload must match on each path; i.e., local Rx to distant end Tx must match. Same for local Tx to distant end Rx. The two values may be asymmetrical.
Rx Payload Size	1-47	Tx and Rx Payload determine the number of bytes per cell (1-47). It is used to reduce latency in low-bit-rate connections. Tx to Rx Payload must match on each path; i.e., local Rx to distant end Tx must match. Same for local Tx to distant end Rx. The two values may be asymmetrical.
Line Loopback	Normal Loop	Normal Mode is no loopback. Line Loopback sets a loopback to the service equipment.
Suppress Cells on LOS	Yes No	If Yes, no ATM cells are sent when a LOS is detected.

Item	Options	Comments
Transmit Timing	Internal External	Toggle to the correct value. This field is valid for Synchronous mode and ignored for Asynchronous mode.
ST Clock	Normal Inverted	Toggle to the desired value. The Send Transmit Clock is only available when ST is selected for Network Timing Recovery.
Tx Latency (msec)		Bits Per Second, Latency, and Payload Size are interrelated. Choose two of the three and the third is automatically calculated. Normally, the speed (bps) is determined by the attached equipment.
Rx Latency (msec)		Rx latency, Rx Payload and Bits Per Second are interrelated like the Tx values.
Tx Payload Size	1-47	Tx and Rx Payload determine the number of bytes per cell (1-47). It is used to reduce latency in low-bit-rate connections. Tx to Rx Payload must match on each path; i.e., local Rx to distant end Tx must match. Same for local Tx to distant end Rx. The two values may be asymmetrical.
Rx Payload Size	1-47	(see Tx Payload size comments above)
Transmit Cells	Enable, Disable	
Cell Loss Alarm	Enable, Disable	
CTS	DCE: On, Off, Loop, Auto DTE: On, Ignore	Loop connects RTS to CTS
RTS	DCE: Honor, Ignore DTE: On, Off, Loop, Auto	Honor sets an alarm
DSR	DCE: On, Off, Loop DTE: On, Ignore	Loop connects DSR to DTR

Item	Options	Comments
DTR	DCE: Honor Ignore DTE: On, Off, Loop, Auto	Honor sets an alarm
RI	On Off	Set by the DCE end of the connection. Ignored by DTE end of connection
DCD	On Off	Set by the DCE end of the connection. Ignored by DTE end of connection.
Req DCE Loopback	Normal Loop	Appears on DTE configuration only, not on DCE port configuration.
Line Loopback	Normal, Loop	
Surpress Cells on LOS	Yes No	
Low CTS on Cell Starvation	Yes No	This option only functions when the CTS above is set to Auto

High Bit Rate (HBR) SAM

Choose the slot (C to P) containing the High Bit Rate SAM to be configured and a screen for port selection appears. Select a port to configure and a port configuration screen appears. The following table is a guide to parameter configuration. Each port on the HBR SAM is configured separately. The configurable items shown will vary based upon the physical interface chosen so items from the table may not appear on the screen.

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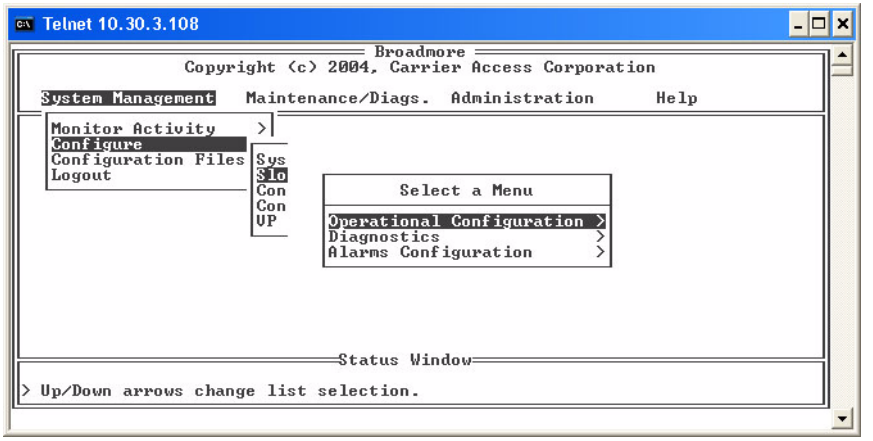
Table 7-30: High Bit Rate SAM Configuration Items

Item	Options	Comments
Port Name	None	This is a user defined field. Enter text to clearly identify the port for your own use.
Port Mode	Online, Offline	This a status field. Choices are not selectable.
Bits Per Second	56000-24000000	Enter the speed for the attached signal input from 75 bps to 24 Mbps in 1 bps increments.
Data Polarity	Normal, Inverted	Toggle to the desired selection (input and output). This option inverts (complements) the ones and zeros in the data
Clock Polarity	Normal, Inverted	Toggle to the desired selection (input and output). This option changes the phase of the clock signal with respect to the data. Clock Polarity and Transmit Timing apply to Synchronous mode and are ignored for Asynchronous mode.

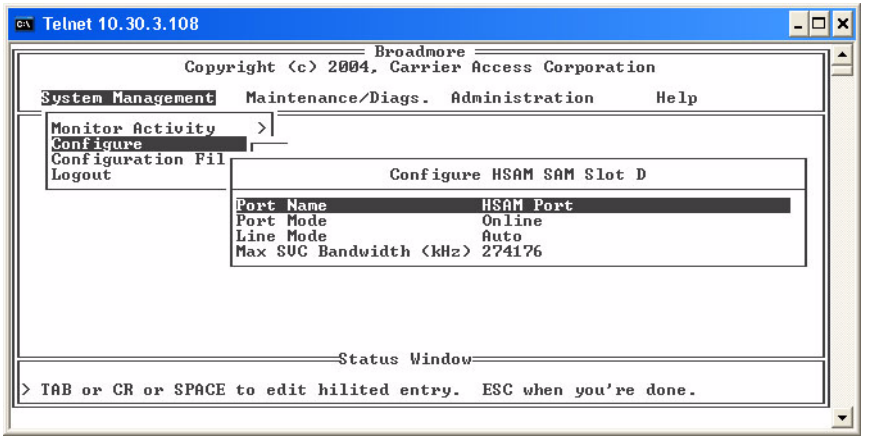
Item	Options	Comments
Network Timing Recovery	Synchronous Adaptive Loop	Synchronous uses the network as a common timing source for input or output data. Adaptive is used for external timing when there is no common timing source. Loop uses the clock recovered from the received data as the transmit timing source.
Tx Payload Size	1-47	Tx and Rx Payload determine the number of bytes per cell (1-47). It is used to reduce latency in low-bit-rate connections. Tx to Rx Payload must match on each path; i.e., local Rx to distant end Tx must match. Same for local Tx to distant end Rx. The two values may be asymmetrical.
Rx Payload Size	1-47	Tx and Rx Payload determine the number of bytes per cell (1-47). It is used to reduce latency in low-bit-rate connections. Tx to Rx Payload must match on each path; i.e., local Rx to distant end Tx must match. Same for local Tx to distant end Rx. The two values may be asymmetrical.
Line Loopback	Normal Loop	Normal Mode is no loopback. Line Loopback sets a loopback to the service equipment.
Suppress Cells on LOS	Yes No	If Yes, no ATM cells are sent when a LOS is detected.

High Speed SAM (HSAM)

Choose slot (D) containing the High Speed SAM (HSAM) to be configured and a screen menu appears.



From the menu, select Operational Configuration and the configuration screen appears.



The following table is a guide to parameter configuration.

Table 7-31: HSAM Operational Configuration Items

Item	Options	Comments
Port Name	None	This is a user defined field. Enter text to clearly identify the port for your own use.
Port Mode	Online Offline	This a status field. Choices are not selectable.
Line Mode	Auto Manual I Only Manual I & Q	Automaticly selects which data lines to use by the input rate. If the input rate is greater than 60 MHz, both line I and Q are used. If it less than 60 MHz, only line I is used. Forces HSAM to use only line I. Forces HSAM to use lines I and Q.
Max SVC Bandwidth (kHz)	274176 137088 47850 10000 200	Sets the bandwidth used by an SVC in the switch. This has nothing to do with selecting data lines or cellbuses. This only affects an SVC selection. If you are not using SVCs, this selection has no effect.

From the menu, select Alarms Configuration and the configuration screen appears.

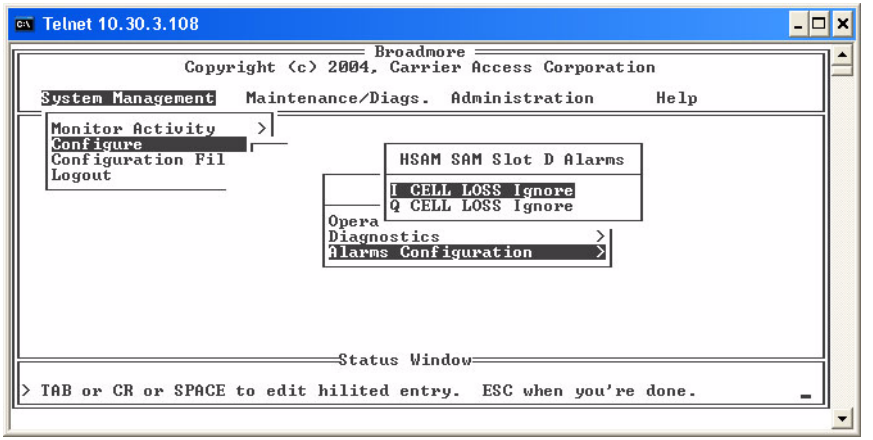


Table 7-32: HSAM Alarm Configuration Items

Item	Options	Comments
I CELL LOSS	Ignore Major Minor	Ignore the alarm. Use as major alarm. Use as minor alarm.
Q CELL LOSS	Ignore Major Minor	Ignore the alarm. Use as major alarm. Use as minor alarm.

From the menu, select Diagnostics and the configuration screen appears.

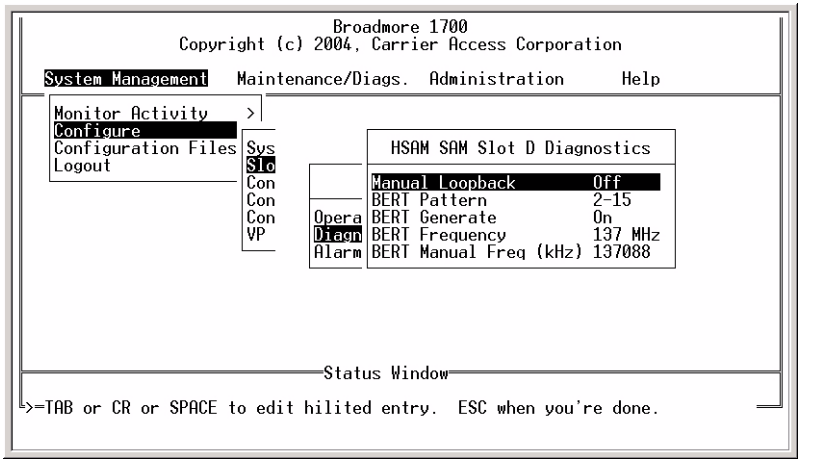


Table 7-33: HSAM Diagnostics Configuration

Item	Options	Comments
Manual Loopback	Off Local Remote	Turns loopback off. Turns on Local loopback. Turns on Remote loopback.
BERT Pattern	2 - 15 2 - 9	Sets BERT pattern to 2-15. Sets BERT pattern to 2-9.
BERT Generate	On Off	Turns BERT loopback on. Turns BERT loopback off. Note: When the BERT test is on, it is always sent in both directions (ATM and interface) and on both data lines (I and Q).
BERT Frequency	200 kHz 10 MHz 47 MHz 137 MHz Manual	Sets BERT frequency to 200,000 Hz. Sets BERT frequency to 10,000,000 Hz. Sets BERT frequency to 47,000,000 Hz. Sets BERT frequency to 137,000,000 Hz. Sets BERT frequency to “BERT Manual Freq”.
BERT Manual Freq (kHz)		Enter the frequency to use when “BERT Frequency” is set to Manual.

Serial Mux SAM Configuration

Choose the slot (C to P) containing the Serial Mux SAM to be configured and the configuration screen appears. The following table is a guide to parameter configuration. All input channels that are set On will be multiplexed into one VC for transmission over the ATM network.

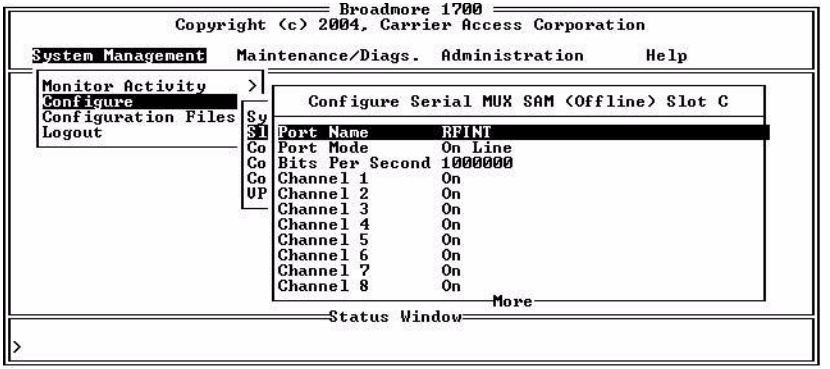


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Table 7-34: Serial Mux SAM Configuration Items

Item	Options	Comments
Port Name	None	This is a user defined field. Enter text to clearly identify the port for your own use.
Port Mode	Online, Offline	Toggle to the desired selection (Online or Offline).
Bits Per Second	100000-3000000	Enter the speed for the multiplexed signal from 100 kbps to 3 Mbps.
Channel (1 to 8)	On, Off	Toggle the desired channels On or Off. This option selects the channels to be multiplexed into the one serial output.
Clock Polarity	Normal, Inverted	Toggle to the desired selection (input and output). This option changes the phase of the clock signal with respect to the data. Clock Polarity and Transmit Timing apply to Synchronous mode and are ignored for Asynchronous mode.
TX Clock	TT, ST	Toggle to the desired transmit clock selection (Terminal Timing or Send Timing). TT uses clock from DCE user equipment. ST uses local or remote cell clock.
ST Clock	Local, Remote	Toggle to the desired Send Timing clock source (Local or Remote). Local uses internal cell clock. Remote uses cell clock recovered from remote Serial Demux.

Serial Demux SAM Configuration

Choose the slot (C to P) containing the Serial Demux SAM to be configured and a screen for port configuration screen appears. The following table is a guide to parameter configuration. All input channels multiplexed into the received VC will be demultiplexed onto the corresponding channel output ports.

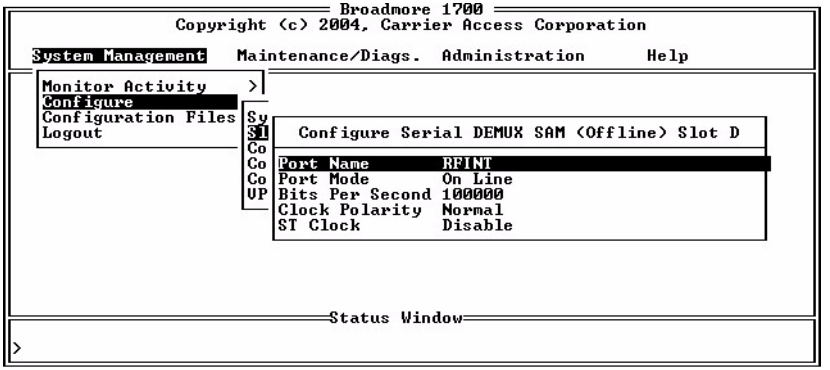

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Table 7-35: Serial Demux SAM Configuration Items

Item	Options	Comments
Port Name	None	This is a user defined field. Enter text to clearly identify the port for your own use.
Port Mode	Online, Offline	This a status field. Choices are not selectable.
Bits Per Second	100000-3000000	Enter the speed for the attached signal input from 100 kbps to 3 Mbps. This value must be the same as used in the corresponding Serial Mux providing the data.
Clock Polarity	Normal, Inverted	Toggle to the desired selection (Normal or Inverted). This option changes the phase of the clock signal with respect to the data.
ST Clock	Enable, Disable	Toggle to the desired Send Terminal clock selection (Enable or Disable). ST must be enabled on Demux if it is enabled on the Mux.

Cell Bearing Interface (CBI) NIM/SAM

Choose the NIM card slot (A or B) or SAM card slot (C to P) containing the Cell Bearing Interface SAM and a screen for port selection appears. Select a port to configure and a port configuration screen appears. The table below is a guide to parameter configuration. Each port on the CBI is configured separately. The configurable items shown will vary based upon the physical interface chosen so items from the table may not appear on the screen. For Operational Configuration, select DTE or DCE.

CAUTION! **BE CAREFUL NOT TO EXCEED THE ALLOWABLE ATM CELLBUS BANDWIDTH. FOR CONVENIENCE, OPERATORS ARE PERMITTED TO CHANGE PARAMETERS OF VARIABLE BIT RATE MODULES WITHOUT FIRST SETTING THE ACTIVE PVC OFFLINE, BUT THE BROADMORE WILL NOT ISSUE AN ALARM IF THE ALLOWABLE BANDWIDTH IS EXCEEDED. BANDWIDTHS ARE RECALCULATED WHEN A PVC IS SET ONLINE OR WHEN THE OPERATOR DISPLAYS THE BANDWIDTH METER (SEE PAGE 7-25).**

NOTE: The CBI NIM does not support UNI signaling, so be sure to set the UNI Version to **Off** in the Configuration menu.

Table 7-36: CBI Operational Configuration Items

Item	Options	Comments
Port Name	None	Enter text to clearly identify the port
Port Mode	Online Offline	Toggle to the correct selection.
Terminal Type	None	This field displays as either DTE or DCE and cannot be changed in this menu. Choose the terminal type from the previous (Select a Terminal Type) menu.
kBits Per Second	56 to 16384	Enter the speed for the attached signal input (for example, 230)

Table 7-36: CBI Operational Configuration Items (Continued)

Item	Options	Comments
Send Timing	Receive Timing (DD) Internal Source	DCE only.
LL/RL Mode	Normal Loopback Crypto Resync	DTE only.
Receive Timing	Terminal Timing (DB) Internal Source	DCE only.
Terminal Timing	Receive Timing (DD) Send Timing (DA)	DTE Only
Invert Terminal Timing (DB)	Normal Invert	DCE and DTE
Invert Send Timing	Normal Invert	This setting controls terminal timing in DCE and receive timing in DTE.
DCE Ready (CC)	On, Off	
DCE Rx Line Signal Defect	On, Off	
DTE Ready (CC)	On, Off	
DCE Rx Line Signal Defect	On, Off	
Data Scrambling	On, Off	This setting must match on both ends of the circuit in order to pass traffic.
Resync ON Time (.1*)	0 to 60	The Resync ON State duration period can be set from 0 to 6.0 seconds in 0.1 second increments, or disabled.
Resync min OFF Time (.1*)	0 to 600	The Resync minimum timeout period between resync events can be set from 0 to 60.0 seconds in 0.1 second increments, or disabled.

Table 7-37: CBI Diagnostics

Item	Options	Comments
Enable LL-TM Loopback	On, Off	Line loopback - test mode. When in DTE mode, sends a loopback request toward DCE. When in DCE mode, responds to request from DTE.
Manual Loopback	On, Off	Loop port signal back towards network

Table 7-38: CBI Alarms

Alarm	Options
LOSS-HEC	Ignore/Major/Minor
LOSS TX TIMING DA	Ignore/Major/Minor
LOSS TX TIMING DB	Ignore/Major/Minor
INACTIVE TX DATA BA	Ignore/Major/Minor
LOSS RX TIMING DD	Ignore/Major/Minor
INACTIVE RX DATA BB	Ignore/Major/Minor
INACTIVE RTS CA	Ignore/Major/Minor
INACTIVE CTS CB	Ignore/Major/Minor
DCE NOT RDY CC	Ignore/Major/Minor
DTE NOT RDY CD	Ignore/Major/Minor
TX FIFO OVFLO	Ignore/Major/Minor
RX FIFO OVFLO	Ignore/Major/Minor

Table 7-39: CBI Alarm Definitions

Service Alarm	Definition
LOSS-HEC	Loss of HEC alignment/cell delineation
LOSS TX TIMING	Loss of transmit timing, Circuit DA
LOSS TX TIMING	Loss of transmit timing, Circuit DB
INACTIVE TX DATA BA	Inactive transmit data, Circuit BA
LOSS RX TIMING DD	Loss of receive timing, Circuit DD
INACTIVE RX DATA BB	Inactive receive data, Circuit BB
INACTIVE RTS CA	Inactive RTS, Circuit CA
INACTIVE CTS CB	Inactive RTS, Circuit CB
DCE NOT RDY CC	DCE not ready, Circuit CC
DTE NOT RDY CD	DCE not ready, Circuit CD
TX FIFO OVFLO	An internal buffer overflow
RX FIFO OVFLO	An internal buffer overflow

HSSI Cell Bearing Interface NIM/SAM

Choose the NIM card slot (A or B) or SAM card slot (C to P) containing the High Speed Serial Interface (HSSI) Cell Bearing Interface (CBI) module and a screen for port selection appears. Select a port to configure and a port configuration screen appears. The table below is a guide to parameter configuration. Each port on the HSSI-CBI is configured separately. The configurable items shown will vary based upon the physical interface chosen so items from the table may not appear on the screen. For Operational Configuration, select DTE or DCE.

CAUTION! **BE CAREFUL NOT TO EXCEED THE ALLOWABLE ATM CELLBUS BANDWIDTH. FOR CONVENIENCE, OPERATORS ARE PERMITTED TO CHANGE PARAMETERS OF VARIABLE BIT RATE MODULES WITHOUT FIRST SETTING THE ACTIVE PVC OFFLINE, BUT THE BROADMORE WILL NOT ISSUE AN ALARM IF THE ALLOWABLE BANDWIDTH IS EXCEEDED. BANDWIDTHS ARE RECALCULATED WHEN A PVC IS SET ONLINE OR WHEN THE OPERATOR DISPLAYS THE BANDWIDTH METER (SEE PAGE 7-25).**

NOTE: The HSSI-CBI NIM does not support UNI signaling, so be sure to set the UNI Version to **Off** in the Configuration menu.

Table 7-40: HSSI-CBI Operational Configuration Items

Item	Options	Comments
Select a Terminal Type	DTE DCE	Choose terminal type to proceed to configuration menu with the following items.
Port Name	None	Enter text to clearly identify the port
Port Mode	Online Offline	Toggle to the correct selection.
Terminal Type	None	This field displays as either DTE or DCE and cannot be changed in this menu. Choose the terminal type from the previous (Select a Terminal Type) menu.

Table 7-40: HSSI-CBI Operational Configuration Items (Continued)

Item	Options	Comments
kBits Per Second	56 to 52000	Enter the speed for the attached signal input (for example, 3200).
Timing	Receive Timing Internal Source	DCE or DTE.
Bits Source Rate (kBits)	56 to 52000	Enter the speed for the attached signal input (for example, 3200).
Data Scrambling	On, Off	This setting must match on both ends of the circuit in order to pass traffic.
Resync ON Time (.1*)	0 to 60	The Resync ON State duration period can be set from 0 to 6.0 seconds in 0.1 second increments, or disabled.
Resync min OFF Time (.1*)	0 to 600	The Resync minimum timeout period between resync events can be set from 0 to 60.0 seconds in 0.1 second increments, or disabled.

Table 7-41: HSSI-CBI Diagnostics

Item	Options	Comments
Manual Loopback	On, Off	Loop port signal back towards network
DTE Send Loopback	On, Off	DTE Only
DCE Send Loopback	On, Off	DCE Only

Table 7-42: HSSI-CBI Alarms

Alarm	Options
LOSS-HEC	Ignore/Major/Minor
LOSS BITS CLK	Ignore/Major/Minor
TX FIFO OVFLO	Ignore/Major/Minor
RX FIFO OVFLO	Ignore/Major/Minor
INACTIVE RECV DATA	Ignore/Major/Minor
INACTIVE SEND DATA	Ignore/Major/Minor
DCE INACTIVE TERM TIMING	Ignore/Major/Minor
DTE INACTIVE SEND TIMING	Ignore/Major/Minor
DTE INACTIVE RECV TIMING	Ignore/Major/Minor

Table 7-43: HSSI-CBI Alarm Definitions

Service Alarm	Definition
LOSS-HEC	Loss of HEC alignment/cell delineation
LOSS TX TIMING	Loss of transmit timing, Circuit DA
LOSS TX TIMING	Loss of transmit timing, Circuit DB
INACTIVE TX DATA BA	Inactive transmit data, Circuit BA
LOSS RX TIMING DD	Loss of receive timing, Circuit DD
INACTIVE RX DATA BB	Inactive receive data, Circuit BB
INACTIVE RTS CA	Inactive RTS, Circuit CA
INACTIVE CTS CB	Inactive RTS, Circuit CB
DCE NOT RDY CC	DCE not ready, Circuit CC
DTE NOT RDY CD	DCE not ready, Circuit CD
TX FIFO OVFLO	An internal buffer overflow
RX FIFO OVFLO	An internal buffer overflow

PVC Connection

Follow the selection process shown below to add a new PVC. The **Establish a PVC Call** screen appears for data entry.

Select **System Management** ↵
Select **Configure** ↵
Select **Configure PVC** ↵
Select **Insert** (shift +:) ↵

```
                Establish a PVC Call

Connection Name          test port 2
Local Slot               B
Local Port or Tributary  2
Local Channel Map        N/A
Transmit VPI             0
Transmit VCI             34
Receive VPI              0
Receive VCI              34
CDV                      80
```

Table 7-44: PVC Configuration Items

Item	Definition
Connection Name	Press the space bar to select Connection Name and type a descriptive identifier (For example, **test port 2**).
Local Slot	The Local Slot is the chassis slot.
Local Port Number	Port number depends on the configuration.
Local Channel Map	Channel map depends on the configuration. Channel map only applies to the DS1 N×64, E1 N×64, and Structured DS3 SAMs. There is **no** channel mapping for the Unstructured DS3, Unstructured E3, HSAM, MBR, and HBR SAMs, and this value will be displayed as N/A.
VP/VC (Transmit/ Receive)	The maximum settings for VP/VC are allocated using the Max VP/VC feature in System Services (see *"Max VP/VC" on page 7-23*). Table 7-1 on page 7-23 shows the valid VP/VC values.
CDV	Cell Delay Variation (CDV) is the difference between the expected arrival time and the actual arrival time of the next cell. This value is expressed in number of cells. The value can vary from 0 to 255. An initial value of 80 to 100 cells is recommended.

The VPI/VCI are locally significant and need to match on both ends of the PVC. Therefore the VPI/VCI must be provisioned identically on the ATM switch port.

NOTE: The transmit and receive VPI/VCI must be configured through the ATM switch. The procedure for this will vary by switch. Consult the ATM switch documentation to accomplish this configuration.

SVC Connection

Follow the selection process shown below to add a new SVC. The **Establish a Call** screen appears for data entry as shown below.

Select **System Management** ↵
Select **Configure** ↵
Select **Configure SVC** ↵

```
LINK UP                    SVC Connection Table Editor                    12
UniquePart              ¦--Local---¦ ¦-FarEnd--¦          Cause
DestAddr      State     CrdPrt Chan  CrdPrt Chan  VP/VC  Code Name

00807CFFEC23 Rel'd    D 5 00FFF000 D 5 FFF00000  0/   0  38 DS1 trib 5
00807CFFEA23 Active   D 7 00FFFFFF --- --------  0/ 191   0
```

From here, use the ***Insert*** (or ***shift:***) key to access the input screen below.

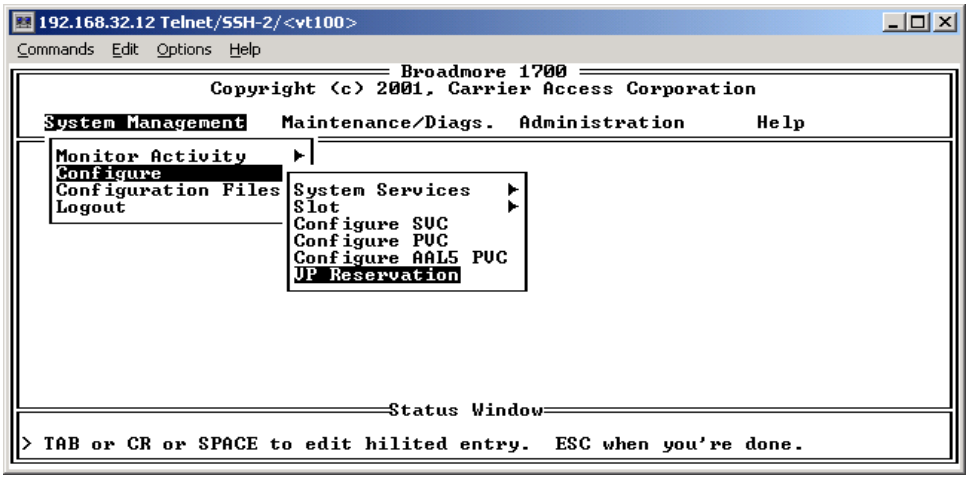
```
                          Establish a Call

Connection Name           DS1 trib 5
Local Slot                D
Local Port or Tributary   5
Local Channel Map         ---------xxxxxxxxxxxxxxxxxxxxxxxx
Destination ATM Address   47000580FFE1000000F21A880900807CFFEC2300
Remote Slot               D
Remote Port or Tributary  5
Remote Channel Map        11111111111100000000000000000000
CDV                       80
```

Item	Definition
Connection Name	Press the space bar to select Connection Name and type a descriptive identifier (For example, **test port 2**).
Local Slot	The Local Slot is the chassis slot.
Local Port Number	The Port number depends on the configuration.
Local Channel Map	The Channel map depends on the configuration. Channel map only applies to the DS1 N×64, E1 N×64, and Structured DS3 SAMs. There is **no** channel mapping for the Unstructured DS3, Unstructured E3, HSAM, MBR, and HBR SAMs, and this value will be displayed as N/A.
VP/VC (Transmit/ Receive)	The maximum settings for VP/VC are allocated using the Max VP/VC feature in System Services (see *"Max VP/VC" on page 7-23*). Table 7-1 on page 7-23 shows the valid VP/VC values.
Destination ATM Address	Enter the Destination ATM address. To view the ATM Address List, enter**?**.
Remote Slot	The Remote Slot is the chassis slot of the remote unit.
Remote Port Number or Tributary	The Remote Port Number or Tributary depends on the configuration.
Remote Channel Map	The Remote Channel Map depends on the configuration.
CDV	Cell Delay Variation (CDV) is the difference between the expected arrival time and the actual arrival time of the next cell. This value is expressed in number of cells. The value can vary from 0 to 255. An initial value of 80 to 100 cells is recommended.

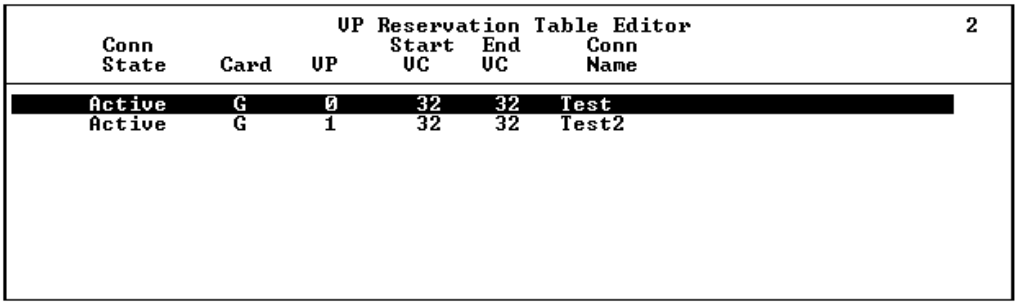
VP Reservation

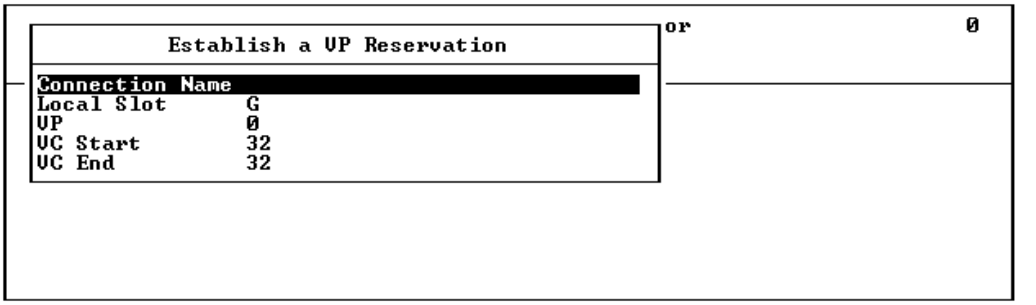
NOTE: The functionality described in this section is only available with Broadmore release 4.6 (or higher). To support this functionality, all ATM DS3, CBI, HSSI-CBI, OC-3c, or OC-12c modules in the chassis must be upgraded to the levels released with 4.6 (or higher). Firmware and instructions are provided on the upgrade CD.

VP Reservation allows you to reserve a block of up to 65,535 virtual channels (VCs) within a virtual path (VP). With this feature, you can set a VC range on a particular VP without having to set up all of the VC connections individually. You can define up to 40 VPs for reservation. This feature is available only on the ATM DS3, CBI, and HSSI-CBI modules.



The VP Reservation Table Editor is shown below. Press the Enter key to edit or clear an existing connection or create a new connection, then follow the on-screen instructions.





VP Reservation settings are described on the following page.

Item	Comments
Connection Name	Press the space bar to select Connection Name and type a descriptive identifier (For example, **test port 2**).
Local Slot	The Local Slot is the chassis slot.
VP	The range is 0 to one less than the value set for maximum VPs (see *"Max VP/VC" on page 7-23* for valid values). If the Max VP/VC value is set to 2/65536, the VP number must be set to 0; a value of 1 is invalid. **NOTE:** A maximum of 40 VPs can be defined for reservation.
VC Start	The default range is 32 to the value set for maximum VCs (see *"Max VP/VC" on page 7-23* for valid values). VC numbers can start at 1 if the VP is not set to 0.
VC End	The default range is 32 to the value set for maximum VCs (see *"Max VP/VC" on page 7-23* for valid values). VC numbers can start at 1 if the VP is not set to 0.

System Configuration

- Save Configuration ... *7-106*
- Restore Configuration ... *7-107*
- Delete Configuration ... *7-107*
- Set Power-on Default ... *7-108*
- Save Card Defaults ... *7-108*
- Restore Card Defaults ... *7-108*

The system configuration is quickly accessed via the configuration files as shown below. There is a special option to save a configuration for powerup. This configuration will automatically load with the application of power to the Broadmore 1700 after a power interruption.

Select **Save Configuration** to save the current configuration and choose an appropriate file name when asked. This should be done after each significant configuration change so that the configuration can be quickly re-established at a later time.

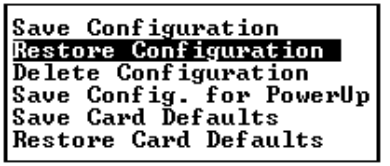
Save Configuration

Follow the selection sequence below to save the current configuration. Enter an appropriate file name (for example: 17may3pm). This file can then be used to return to the current system configuration.

Select **System Management** ↵
Select **Configuration Files** ↵
Select **Save Configuration** ↵

Restore Configuration

Select **Restore Configuration** to restore a previously saved configuration as shown below.



The Restore Configuration results in the tear-down of all calls, configuration of all modules, and establishment of all calls found in the named configuration. The UNI must be UP to restore a configuration file containing one (or more) SVC. Follow the sequence below to check UNI status. If necessary, configure UNI before restoring the configuration.

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Connections** ↵
Select **UNI Status** ↵

Delete Configuration

Follow the selection sequence below to delete the current configuration. Answer Yes to the confirmation notice. Use caution as this configuration cannot be restored once it has been deleted

Select **System Management** ↵
Select **Configuration Files** ↵
Select **Delete Configuration** ↵

Set Power-on Default

Select **Save Config. for PowerUp** to save a particular configuration for system power-up. Select **Save Config. for PowerUp** as shown below to retain the current configuration for PowerUp.

Select **System Management** ↵
Select **Configuration Files** ↵
Select **Save Config. For PowerUp** ↵

Save Card Defaults

Select **Save Card Defaults** to save the default configuration for the specific card.

Select **System Management** ↵
Select **Configuration Files** ↵
Select **Save Card Defaults** ↵
Select **Slot** ↵
Select **'Yes' to save the defaults** ↵

Restore Card Defaults

Select **Restore Card Defaults** to restore the default configuration for the specific card. This selection causes the following actions:

- Deletes all existing connections
- Reads the configuration file and restores the card port settings
- Reads the configuration file again and sets up any SVC/PVCs

Select **System Management** ↵
Select **Configuration Files** ↵
Select **Restore Card Defaults** ↵
Select **Slot** ↵
Select **'Yes' to restore the card defaults** ↵

Show Configuration

The show config command provides an extract of all network and service module configurations. This allows security officers to compare prior images to ensure no unplanned changes.

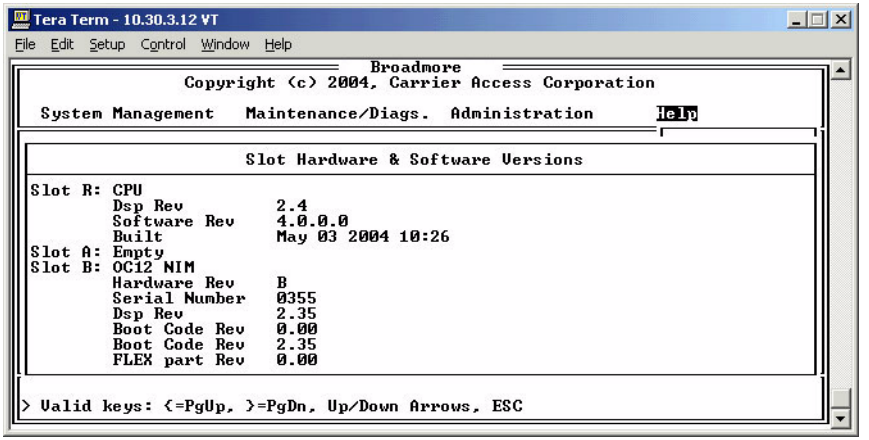
This command is a shell command. Issue this command from the top level prompt of the embedded operating system. Below is an example.

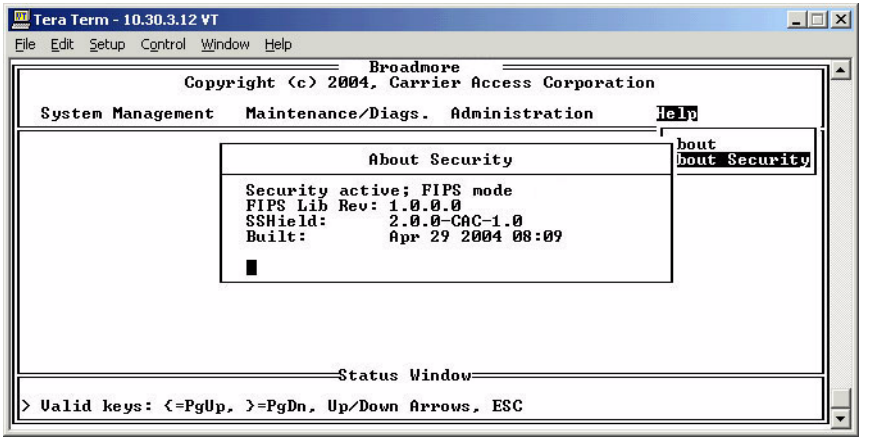
```
Broadmore->showconfig

--- OC3 NIM card slot A configuration:
OC3 Port Mode                       Online
Frame Type                          SONET
Transmit Timing                     Recovered
Receive Timing                      Recovered
SONET Tx                            Enable
Loopback Mode                       Normal
ATM Payload Scramble Mode           Both Scrambled
BITS Clock Loss Alarm               Disable

--- DS1 Nx64 SAM card slot B port 1 configuration:
Port Name                           Port 1
Port Mode                           Online
Service Clocking Mode               Synchronous Network
Clock Recovery Type                 Undefined
Clock Recovery Interface            Undefined
Line Buildout                       0 to 110 feet
Loopback                            Normal
CBR Service Type                    Unstructured
Frame Type                          Direct Unstructured
CBR Signal Format                   B8ZS


--- DS1 Nx64 SAM card slot B port 2 configuration:
Port Name                           Port 2
Port Mode                           Online
```

```
Service Clocking Mode              Synchronous Network
Clock Recovery Type                Undefined
Clock Recovery Interface           Undefined
Line Buildout                      0 to 110 feet
Loopback                           Normal
CBR Service Type                   Unstructured
Frame Type                         Direct Unstructured
CBR Signal Format                  B8ZS

--- DS1 Nx64 SAM card slot B port 3 configuration:
Port Name                          Port 3
Port Mode                          Online
Service Clocking Mode              Synchronous Network
Clock Recovery Type                Undefined
Clock Recovery Interface           Undefined
Line Buildout                      0 to 110 feet
Loopback                           Normal
CBR Service Type                   Unstructured
Frame Type                         Direct Unstructured
CBR Signal Format                  B8ZS

•
•
•


-------- Connections ------------------

--- SVC's


--- PVC's


--- VP Reservations
```

Help

The Help main menu item has two pull-down items: *About* and *About Security*. Choose *About* to obtain the hardware and software version data. This information is important when contacting customer service. Choose *About Security* to display the security mode and software version numbers.


Tera Term - 10.30.3.12 VT
File Edit Setup Control Window Help
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help
Slot Hardware & Software Versions
Slot R: CPU
Dsp Rev 2.4
Software Rev 4.0.0.0
Built May 03 2004 10:26
Slot A: Empty
Slot B: OC12 NIM
Hardware Rev B
Serial Number 0355
Dsp Rev 2.35
Boot Code Rev 0.00
Boot Code Rev 2.35
FLEX part Rev 0.00
> Valid keys: <=PgUp, >=PgDn, Up/Down Arrows, ESC



CHAPTER 8

Maintenance and Troubleshooting

In this Chapter

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- Troubleshooting ... *8-21*
- Repair/Replacement ... *8-36*
- General Maintenance ... *8-45*
- Summary of Front Panel LEDs ... *8-50*

Statistics

- Chassis Statistics ... *8-2*
- OC-12c/STM-4c NIM Statistics ... *8-3*
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- Alarm Overview ... *8-4*
- Slot Statistics for NIM/SAM Cards ... *8-4*
- 24-Hour Statistics ... *8-19*
- PLOA/AAL5 Statistics ... *8-20*

Statistics are available to monitor the Broadmore 1700 operation at the chassis, individual module, or connection level. Thus, the statistics provide a good initial indication of performance and a means to isolate any problems that may arise.

Chassis Statistics

Statistics are monitored by following the sequence shown below to view the chassis statistics shown.

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **ATM by Chassis** ↵

```
                    Chassis ATM Cell and AAL5 Statistics

#  Rx Cells    Tx Cells    Errors      #  Rx Cells    Tx Cells    Errors
A 14602427    15258240    4734        H 0           0           0
B 0           0           0           J 0           0           0
C 0           0           0           K 0           0           0
D 422102037   422241660   0           L 0           0           0
E 0           0           0           M 0           0           0
F 0           0           0           N 0           0           0
G 0           0           0           P 0           0           0
                                      Q 322350      108060      4
Q RxPkt:  143177        TxPkt:  69175        PktTimeout: 0
Q CelDrp: 0             BadVpc: 0            SizeError:  0
Q BadCrc: 1             EopErr: 0            NonZeroGFC: 3
```

OC-12c/STM-4c NIM Statistics

Slot statistics provide specific module level information. Follow the sequence below to select an OC-12c/STM-4c and view the statistics shown on the next page. The same procedure and statistics screen apply to the OC-3c/STM1c if installed.

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot statistics** ↵
Select **OC-12 NIM** (slot A shown) ↵

```
                    OC-12 NIM (Online) Slot A Statistics

                CV          ES          SES         SEFS/UAS    LF    SLOS
Near Section    0           21923       21923       21923
Near Line       0           0           0           0           0
Near Path       0           0           0           0           0
Far Line        0           0           0           0           0
Far Path        0           0           0           0           0
Transmitted Cells:          0
Received Cells:             0
Uncorrected HEC Errors:     0
Corrected HEC Errors:       0
Protect Switch Count:       1       BITS A : Absent    BITS B : Absent
Protect Switch Duration:    23896                          Seconds: 10
```

OC-3c/STM-1c NIM Statistics

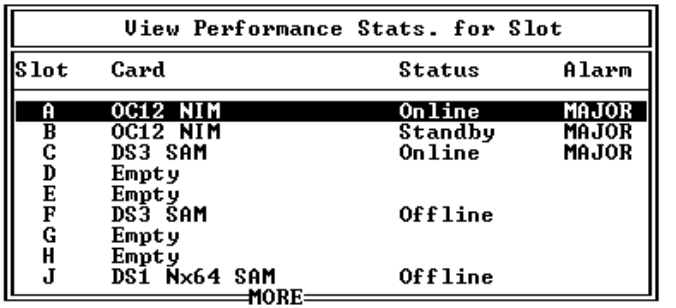
Statistics for the OC-3c/STM-1c are similar to those for the OC-12. A sample screen is shown below.

```
                    OC-3 NIM (Online) Slot A Statistics

                CV          ES          SES         SEFS/UAS    LF
Near Section    11730       13          7           7
Near Line       1758        6           0           0           0
Near Path       769         6           0           0           0
Far Line        3027        10          13          0           0
Far Path        1386        14          0           0           0
Transmitted Cells:          67589516
Received Cells:             4049642732
Uncorrected HEC Errors:     162
Corrected HEC Errors:       20                  APSChan APSMode
Protect Switch Count:       1             BITS A : Present  BITS B : Absent
Protect Switch Duration:    54220                 Seconds: 5
```

Alarm Overview

When the slot selection screen (below) appears, the last column provides an alarm overview. This screen is accessed from either the monitor activity or configuration path and gives an indication for each slot of any major or minor alarm. From ***Monitor Activity***, select the slot for more detailed alarm information. From ***Configuration***, select the slot for more detailed alarm configuration information.



Slot Statistics for NIM/SAM Cards

- DS1 Nx64 and E1 Nx64 Statistics ... *8-5*
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- HBR SAM Statistics ... *8-7*
- High Speed SAM Statistics ... *8-8*
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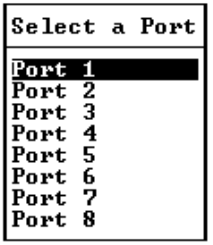
- Cell Bearing Interface (CBI) NIM/SAM Statistics ... *8-17*
- HSSI Cell Bearing Interface (HSSI-CBI) NIM/SAM Statistics ... *8-18*

Statistics are maintained for each SAM port and for each CBI and HSSI-CBI NIM port. The statistics for each of the ports is displayed on the following pages.

DS1 Nx64 and E1 Nx64 Statistics

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **DS1 Nx64** ↵
Select **Port** ↵




```
                Nx64 DS-1 SAM (Online) Slot D Statistics

Line Coding Violations:  0                DS-1 Status     ATM Summary Status
Line Data Bit Errors:    0              No Defect: Off     No Defect:   ON
Line Framing Bit Errors: 0              LOS:       ON      Cell Loss:   Off
                                        LCM:       Off     RxFIFOovfl: Off
Port 1 ATM Summary:                     Yellow:    Off     TxFIFOovfl: Off
    Received Cells:          1106882863 Red OOF:   Off     PtrReframe: Off
    Transmitted Cells:       1122625732 Line AIS:  Off
    Uncorrected SNP Cells:   0



Seconds: 11
```

NOTE: The statistics for the E1 N×64 SAM differ slightly from the DS1 SAM and are not shown.

MBR SAM Statistics

For the MBR control signals, “1” indicates detection and “0” indicates not detected.

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **MBR SAM** ↵
Select **Port** ↵
Select **Terminal Type** ↵

```
                    MBR SAM (Online) Slot B Statistics

Port     Tx Cells        Rx Cells        Error Cells
1        0               0               0
2        3395873         0               0
3        3395822         0               0
4        0               0               0

Port  DTR  DSR  CTS  RTS  LL   DCD  RI   LOS   Cell Starvation
1     0    0    0    0    0    0    0    Off     Off
2     0    1    1    0    0    1    1    Off     ALARM
3     0    0    1    0    0    1    0    Off     ALARM
4     0    0    0    0    0    0    0    Off     Off
Seconds: 2
```

HBR SAM Statistics

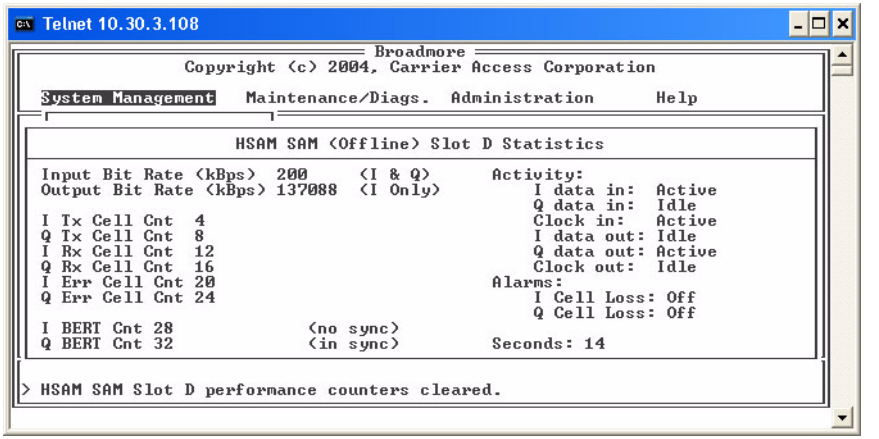
Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **HBR SAM** ↵

```
                    MBR SAM (Online) Slot B Statistics

Port     Tx Cells       Rx Cells       Error Cells
1        507            0              0
2        507            0              0
3        0              0              0
4        0              0              0

Port                                        LOS   Cell Starvation
1                                           ALARM  ALARM
2                                           ALARM  ALARM
3                                           Off    Off
4                                           Off    Off
Seconds: 23
```

High Speed SAM Statistics

For the HSAM control signals, “1” indicates detection and “0” indicates not detected.

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **HSAM** ↵

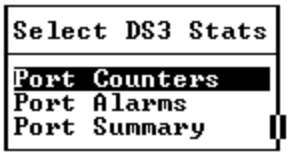

Telnet 10.30.3.108
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management
Maintenance/Diags.
Administration
Help
HSAM SAM (Offline) Slot D Statistics
Input Bit Rate (kBps) 200 (I & Q)
Output Bit Rate (kBps) 137088 (I Only)
I Tx Cell Cnt 4
Q Tx Cell Cnt 8
I Rx Cell Cnt 12
Q Rx Cell Cnt 16
I Err Cell Cnt 20
Q Err Cell Cnt 24
I BERT Cnt 28 (no sync)
Q BERT Cnt 32 (in sync)
Activity:
I data in: Active
Q data in: Idle
Clock in: Active
I data out: Idle
Q data out: Active
Clock out: Idle
Alarms:
I Cell Loss: Off
Q Cell Loss: Off
Seconds: 14
> HSAM SAM Slot D performance counters cleared.

Unstructured DS3 and Unstructured E3 SAM Statistics

Operational statistics are provided in accordance with RFC 1407. Statistics for the Unstructured DS3 and Unstructured E3 are shown in separate windows below.

Unstructured DS3 Statistics

For Unstructured DS3 SAMs, the most recent 24 hours of statistical data is maintained in a separate file. This data is presented in 15-minute increments by port in spreadsheet format.



Use the following steps to view the Unstructured DS3 SAM statistics

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **DS3 slot** ↵
Select **Port** ↵
Select one of the following statistics to view:
- *** Port Counters**
- *** Port Alarms**
- *** Port Summary**

Statistics for the Unstructured DS3 SAM are shown below.

```
                    DS3 SAM (Online) Slot C Port 1 Counters

Tx Cells 2991263019              Rx Cells 176919

Network Errors                   Service Errors                Major Alarm YES
SEQ Errors     0                 BPV Errors     0              Minor Alarm YES
SNP Errors     0                 EXZ Errors     0
F-BIT Errors   0                 F-BIT Errors   0
PARITY Errors  0                 PARITY Errors  0
C-BIT Errors   0                 C-BIT Errors   0
FEBE Errors    0                 FEBE Errors    0
BERT Errors    0                 BERT Errors    0
```

The alarm display below has two columns to differentiate network and service errors.

```
                    DS3 SAM (Online) Slot C Port 1 Alarms

Network Cell Starvation  On       Service LOS                 On
        LOF              On               LOF                 On
        AIS              Off              AIS                 Off
        IDLE             Off              IDLE                Off
        RAI              Off              RAI                 On
        FEAC Alarm       Off              FEAC Alarm          Off
        XS SEQ Errors    Off              Line Code Violation On
        XS SNP Errors    Off              XS F-BIT Errors     Off
        XS F-BIT Errors  Off              XS Parity Errors    On
        XS Parity Errors Off              XS C-BIT Errors     On
        XS C-BIT Errors  Off              XS FEBE Errors      On
        XS FEBE Errors   Off
```

```
                    DS3 SAM (Online) Slot C Port 1 Summary

S-PES  0                   N-PES  0                 UNDER FLOW  364
S-PSES 0                   N-PSES 0                 OVER FLOW   0
S-SEFS 364                 N-SEFS 364
S-UAS  364                 N-UAS  362
S-AS   0                   N-AS   0
S-LCV  1661580             N-SNP  0
S-PCV  830790              N-PCV  0
S-CCV  830790              N-CCV  0
S-CES  0                   N-CES  0
S-CSES 0                   N-CSES 0
S-LES  362                 N-SEQ  0
Seconds Recorded 362
```

The standard error terms (such as PES for P-Bit Error Seconds) have a preceding letter, either S or N. The S represents the Service side of the DS3, and the N for the Network side. The display is divided into two columns, service and network, for clarity. Definitions are provided below for reference.

Term	Definition
AS	Available Seconds
CCV	C-bit Coding Violation
CES	C-bit Errored Seconds
CSES	C-bit Severely Errored Seconds
LCV	Line Coding Violation
LES	Line Errored Seconds
PCV	P-bit Coding Violation
PES	P-bit Errored Seconds
PSES	P-bit Severely Errored Seconds
SEFS	Severely Errored Framing Seconds
SEQ	Sequence Errors
SNP	Sequence Number parity
UAS	Unavailable Seconds

Unstructured E3 SAM Statistics

For Unstructured E3 SAMs, the most recent 24 hours of statistical data is maintained in a separate file. This data is presented in 15-minute increments by port in spreadsheet format

Use the following steps to view the Unstructured E3 SAM statistics

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **E3 SAM** ↵
Select **Port** ↵
Select one of the following statistics to view:
- *** Port Counters**
- *** Port Alarms**

Statistics for the Unstructured E3 SAM are shown below.

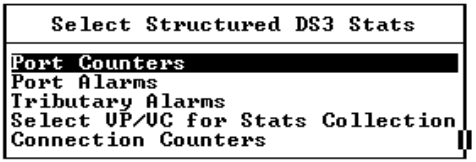
E3 SAM (Offline) Slot D Port 1 Counters

Tx Cells 75160		Rx Cells 75160			
Network Errors		Service Errors		Major Alarm NO	
SEQ Errors	0	BPV Errors	0	Minor Alarm NO	
SNP Errors	0	EXZ Errors	0		
F-BIT Errors	0	F-BIT Errors	0		
BIP Errors	0	BIP Errors	0		
FEBE Errors	0	FEBE Errors	0		
BERT Errors	0	BERT Errors	0		

```
                    E3 SAM (Offline) Slot D Port 1 Alarms

Network Cell Starvation  Off       Service LOS                  Off
        LOF              Off               LOF                  Off
        AIS              Off               AIS                  Off
        RDI              Off               RDI                  Off
        XS SEQ Errors    Off               Line Code Violation  Off
        XS SNP Errors    Off               XS F-BIT Errors      Off
        XS F-BIT Errors  Off               XS BIP Errors        Off
        XS BIP Errors    Off               XS FEBE Errors       Off
        XS FEBE Errors   Off
```

Structured DS3 SAM Statistics

Operational statistics are provided in accordance with RFC 1407. Counters, alarms, and a summary are shown in separate windows following the sequence below. For Structured DS3 SAMs, the most recent 24 hours of statistical data is maintained in a separate file. This data is presented in 15-minute increments by port in spreadsheet format.



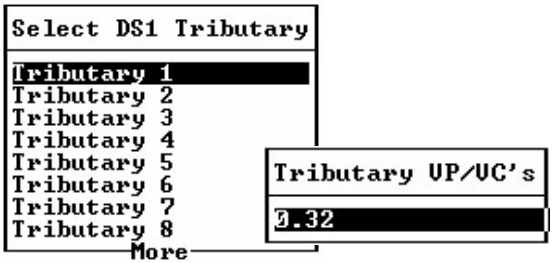
Use the following steps to view the Structured DS3 SAM statistics. Statistics for the Structured DS3 SAM statistics are shown below.

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **Structured DS3 SAM** ↵
Select one of the following statistics to view:
* **Port Counters**
* **Port Alarms**
* **Tributary Alarms**
* **Select VP/VC for Statistics Collection**
* **Connection Counters**

```
                Structured DS3 SAM (Offline) Slot E Counters

Service Errors

BPV Errors       0                    Major Alarm NO
EXZ Errors       0                    Minor Alarm NO
F-BIT Errors     0
PARITY Errors    0                    Tx Cells    0
C-BIT Errors     0                    Rx Cells    0
FEBE Errors      0
```

```
          Structured DS3 SAM (Offline) Slot M Alarms

LOS                      Off
LOF                      Off
AIS                      Off
IDLE                     Off
RAI                      Off
FEAC Alarm               Off
Line Code Violation      Off
XS F-BIT Errors          Off
XS Parity Errors         Off
XS C-BIT Errors          Off
XS FEBE Errors           Off
```

```
                Structured DS3 SAM (Offline) Slot M Alarm Detail

   AIS  Cell Starve     AIS  Cell Starve     AIS  Cell Starve
 1                   12                   23
 2                   13                   24
 3                   14                   25
 4                   15                   26
 5                   16                   27
 6                   17                   28
 7                   18
 8                   19
 9                   20
10                   21
11                   22
```



Structured DS3 SAM (Online) Slot E VP=0 VC=32			
Rx Cells:	0	Tx Cells:	7611
Seq Errors:	0	Cond Cells:	0
Snp Errors:	0	Suppressed Cells:	0
Dropped Cells:	0		
Lost Cells:	0		
Overruns:	0		
Ptr Reframes:	0		
Ptr Parity Errors:	0		
Misinserted Cells:	0		
Underruns:	0		

Structured DS3 SAM (Online) Slot E VP=0 VC=32			
Rx Cells:	0	Tx Cells:	7611
Seq Errors:	0	Cond Cells:	0
Snp Errors:	0	Suppressed Cells:	0
Dropped Cells:	0		
Lost Cells:	0		
Overruns:	0		
Ptr Reframes:	0		
Ptr Parity Errors:	0		
Misinserted Cells:	0		
Underruns:	0		

```
                Structured DS3 SAM (Offline) Slot C Bert test detail

   SYNC        BER count      SYNC        BER count      SYNC        BER count
 1 PAT Sync        82181  12  NO Sync            0  23 PAT Sync       118501
 2  NO Sync            0  13 PAT Sync        73972  24  NO Sync            0
 3 PAT Sync        85316  14  NO Sync            0  25 PAT Sync        83267
 4  NO Sync            0  15 PAT Sync        74711  26  NO Sync            0
 5 PAT Sync        65138  16  NO Sync            0  27 PAT Sync        92485
 6  NO Sync            0  17 PAT Sync        90835  28  NO Sync            0
 7 PAT Sync        81501  18  NO Sync            0
 8  NO Sync            0  19 PAT Sync        89911
 9 PAT Sync        79214  20  NO Sync            0
10  NO Sync            0  21 PAT Sync        91917
11 PAT Sync        86540  22  NO Sync            0
```

```
                    SDS3 SAM (Offline) Slot C Port Summary

PES  0                      PSES   0
CES  0                      F-CES  213
CSES 0                      F-CSES 213
CCV  845963346              F-CCV  390728613
SEFS 0
LCV  1463724225
PCV  75245484
LES  213
UAS  213                    F-UA   0


Seconds Recorded 213
```

Cell Bearing Interface (CBI) NIM/SAM Statistics

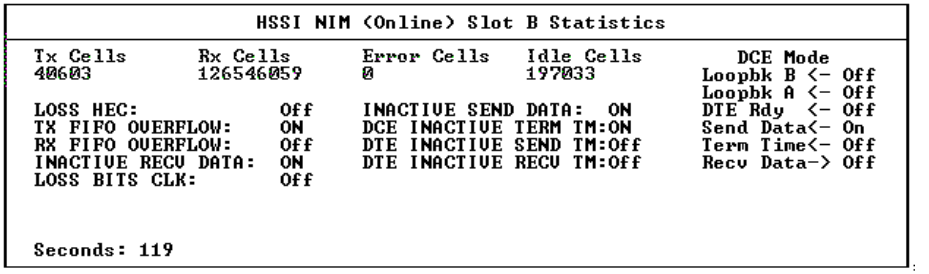
Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **CBI NIM** ↵ *or* **CBI SAM** ↵

```
                    CBI NIM (Online) Slot B Statistics

 Tx Cells       Rx Cells       Error Cells    Idle Cells         DTE Mode
 40603          126546059      0              197033          Term Time<- Act
                                                              Recv Time<- Act
 LOSS HEC:              Off     INACTIVE RTS CA:      Off      Recv Data<- Act
 LOSS TX TIMING DA:     Off     INACTIVE CTS CB:      Off      Send Time-> Act
 LOSS TX TIMING DB:     Off     DCE NOT RDY CC:       Off      Send Data-> Act
 INACTIVE TX DATA BA:   Off     DTE NOT RDY CD:       Off      RTS       -> On
 LOSS RX TIMING DD:     Off     TX FIFO OVERFLOW:     Off      CTS       <- On
 INACTIVE RX DATA BB:   Off     RX FIFO OVERFLOW:     Off      DCE Ready<- On
 LOSS BITS CLK:         Off

 Seconds: 18
```

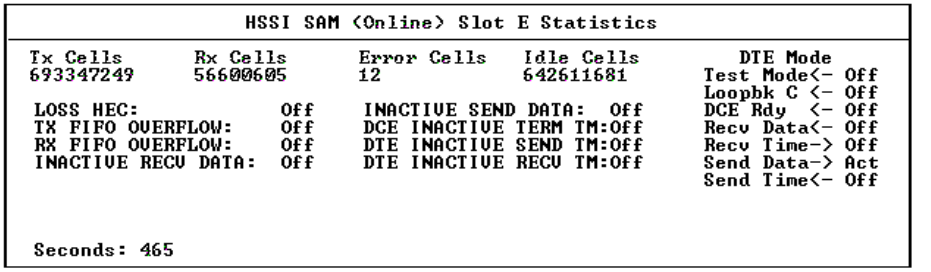
```
                    CBI SAM (Online) Slot H Statistics

 Tx Cells       Rx Cells       Error Cells    Idle Cells         DCE Mode
 693347249      56600605       12             642611681
                                                              Send Time<- Act
 LOSS HEC:              Off     INACTIVE RTS CA:      Off      Send Data<- Act
 LOSS TX TIMING DA:     Off     INACTIVE CTS CB:      Off      Recv Time-> Act
 LOSS TX TIMING DB:     Off     DCE NOT RDY CC:       Off      Recv Data-> Act
 INACTIVE TX DATA BA:   Off     DTE NOT RDY CD:       Off      CTS       -> On
 LOSS RX TIMING DD:     Off     TX FIFO OVERFLOW:     Off      RTS       <- On
 INACTIVE RX DATA BB:   Off     RX FIFO OVERFLOW:     Off      DTE Ready<- On


 Seconds: 465
```

HSSI Cell Bearing Interface (HSSI-CBI) NIM/SAM Statistics

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **Slot Statistics** ↵
Select **HSSI NIM** ↵ *or* **HSSI SAM** ↵



```
                    HSSI NIM (Online) Slot B Statistics

 Tx Cells       Rx Cells       Error Cells    Idle Cells        DCE Mode
 40603          126546059      0              197033        Loopbk B <- Off
                                                             Loopbk A <- Off
 LOSS HEC:              Off     INACTIVE SEND DATA:  ON       DTE Rdy  <- Off
 TX FIFO OVERFLOW:      ON      DCE INACTIVE TERM TM:ON       Send Data<- On
 RX FIFO OVERFLOW:      Off     DTE INACTIVE SEND TM:Off      Term Time<- Off
 INACTIVE RECV DATA:    ON      DTE INACTIVE RECV TM:Off      Recv Data-> Off
 LOSS BITS CLK:         Off


 Seconds: 119
```



```
                    HSSI SAM (Online) Slot E Statistics

 Tx Cells       Rx Cells       Error Cells    Idle Cells        DTE Mode
 693347249      56600605       12             642611681     Test Mode<- Off
                                                             Loopbk C <- Off
 LOSS HEC:              Off     INACTIVE SEND DATA:  Off      DCE Rdy  <- Off
 TX FIFO OVERFLOW:      Off     DCE INACTIVE TERM TM:Off      Recv Data<- Off
 RX FIFO OVERFLOW:      Off     DTE INACTIVE SEND TM:Off      Recv Time-> Off
 INACTIVE RECV DATA:    Off     DTE INACTIVE RECV TM:Off      Send Data-> Act
                                                             Send Time<- Off


 Seconds: 465
```

NOTE: Errored Cells refer to HEC (Header Error Control) errored cells.

24-Hour Statistics

Statistics for the most recent 24-hour period are maintained for DS3 modules. Statistics for each port are maintained in a file that may be transferred via **FTP** to a PC and viewed, using a spreadsheet program such as Microsoft Excel. The files are in the **STATS** directory, and named according to the following sequence:

STATS\DS3C1.STA
STATS\DS3C2.STA
STATS\DS3C3.STA
STATS\DS3F1.STA (and so on)

The C1 represents slot C port 1, C2 represents slot C port 2, and F1 represents slot F port 1.

Statistics are maintained according to the following:

A file is created when a PVC or SVC is activated on a port.

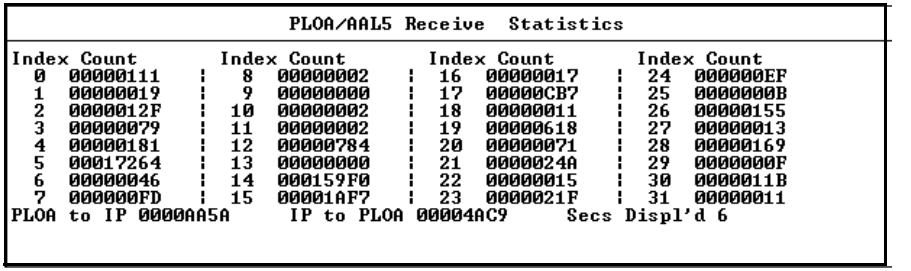
- Statistics for a port are not recorded after the associated PVC is released.
- Statistics are PVC and SVC dependent. When a PVC or SVC is established on a port previously in use, the old statistics are moved to a “discard directory” and new statistics are maintained.
- The discard directory only retains statistics for the most recently released PVC or SVC on any given port.

Use a standard spreadsheet program (such as Excel) to open a statistics file. Adjust the column widths as necessary to match the printer/paper size in use so that the data may be displayed on a single sheet of paper.

PLOA/AAL5 Statistics

Follow the sequence below to select and view the PLOA/AAL5 statistics shown.

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **PLOA/AAL5 Statistics** ↵



PLOA/AAL5 Receive Statistics

Index	Count	Index	Count	Index	Count	Index	Count
0	00000111	8	00000002	16	00000017	24	000000EF
1	00000019	9	00000000	17	00000CB7	25	0000000B
2	0000012F	10	00000002	18	00000011	26	00000155
3	00000079	11	00000002	19	00000618	27	00000013
4	00000181	12	00000784	20	00000071	28	00000169
5	00017264	13	00000000	21	0000024A	29	0000000F
6	00000046	14	000159F0	22	00000015	30	0000011B
7	000000FD	15	00001AF7	23	0000021F	31	00000011

PLOA to IP 0000AA5A IP to PLOA 00004AC9 Secs Displ'd 6

Troubleshooting

- LED Alerts ... *8-22*
- Error Codes ... *8-22*
- Redundancy ... *8-22*
- CPU Sync ... *8-23*
- Problem Isolation ... *8-24*
- Port Loopback ... *8-25*
- Failure Recovery ... *8-27*
- Alarm Response/Reset ... *8-28*
- Flowchart ... *8-28*

Troubleshooting is the process of isolating the cause of a problem so that corrective action can be taken. Steps in this process narrow the focus of attention to the problem area. Documentation at each step in the process provides a valuable aid for further analysis.

NOTE: The steps below should be completed by a qualified technician. These steps assume that the technician will follow basic circuit troubleshooting logic or contact Customer Service when in doubt. Keep written records for each action taken to aid in re-creation if necessary.

LED Alerts

Front panel LED displays provide alerts by module as summarized in the table on *"Summary of Front Panel LEDs" on page 8-50*. Each LED is labeled for identification.

LEDs are basic three-color displays: **red, amber, green.** The basic LED interpretation follows accepted practice of *green* for normal operation; **amber** for minor alert; and *red* for major alert.

Error Codes

The software system will recognize and return both system and setup errors. These error codes provide troubleshooting clues for the user and Customer Service use in solving configuration and system errors. If a software error is returned to the display screen, follow the procedure below:

1. Record the error exactly as it appears on the screen.
2. Complete the troubleshooting flow chart in this chapter to isolate and/or correct any problem before going to step 3.
3. Contact Customer Service for assistance. Have the error message and other pertinent information (e.g., system log) readily available.

NOTE: The remainder of this chapter is based upon the assumption that there are no system or setup software errors. Therefore, it is important to clear all such errors before proceeding.

Redundancy

The Broadmore 1700 configuration has redundancy that serves to minimize system downtime. Troubleshooting alerts for redundant components are clearly indicated on the LEDs as explained below:

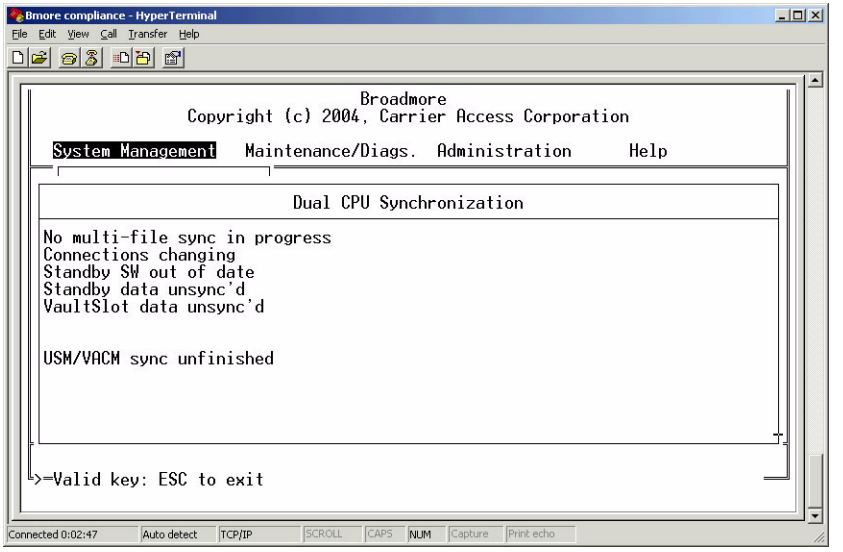
- Power supply: a problem to either module is indicated by LED and log entry. The unit can be replaced without impact to system operation.
- NIM: a problem will give a fault on the online NIM with log entry and the backup assumes control so a module replacement can be done without impact to system operation.
- CPU Sync: a problem synchronizing files between redundant CPUs (see below).

CPU Sync

Follow the sequence below to view the CPU synchronization status between redundant CPUs. This screen shows the progress of a normal CPU Sync and will refresh every few seconds. The messages usually do not indicate any problem but can be useful if troubleshooting is required. When a CPU sync is in progress, the screen will list the number of files remaining and the current file name being synchronized. The files listed on the left side are those that would inhibit a controlled switchover.

NOTE: See also *"Synchronizing CPU" on page 7-34*.

Select **System Management** ↵
Select **Monitor Activity** ↵
Select **CPU Sync** ↵



Problem Isolation

Once you have determined that a problem exists, perform the following steps to isolate the problem for repair:

1. Check to ensure power is available to all modules.
2. Review the LED displays to determine if any module is in an alarm state. Evaluate the alarms and isolate the problem to the extent possible.
3. Review the slot configuration and look at the individual ports. Evaluate any information to further isolate the problem.

   Select **System Management** ↵
   Select **Monitor** ↵
   Select **Slot** ↵
   Select the slot of interest ↵
   Select individual ports ↵
   Observe **Data**

4. Check wiring to ensure connected equipment cables are properly installed and secured. Do not assume the problem is internal to the Broadmore 1700.
5. Check module(s) **Configuration** from the **System Management** pull-down menu. Choose the slot of interest and verify that the configuration is correct for each port.
6. Use loopbacks (below) to pinpoint circuit connectivity problems.
7. Use the flowcharts that follow to pinpoint hardware problems.

Port Loopback

The loopback function is the primary troubleshooting aid for isolation of circuit connectivity problems, both internal and external to the Broadmore 1700. Loopback is available on both the NIM and SAM. There are three loopback options on each SAM module, four on each NIM, see Figure 8-1. These loopbacks are set as a card configuration function (see *"Module Configuration" on page 7-36* for details).

Loopbacks provide a means to verify that circuit paths are functioning correctly. For example, setting the SAM remote loopback, will take user equipment transmit data (Tx) and send it to user equipment receive data (Rx). If a check of the user equipment indicates good Tx and Rx, the problem is not between the SAM and user equipment but somewhere else in the circuit. This logic can then be extended through the entire circuit until problems are isolated and corrected. The NIM has four loopback options:

1. Normal: no loopback.
2. Local: The user equipment data is looped back after processing by the SONET circuitry on the NIM.
3. Remote: The ATM network data is looped back before passing through the NIM
4. Terminal: The user equipment data is looped back after passing through the NIM framer, but before the Line Interface Unit, LIU.

CAUTION! THE TERMINAL OPTION IS RESERVED FOR CARRIER ACCESS ENGINEERS AND SHOULD NOT BE USED.

The SAM has three loopback options:

1. Normal state is no loopback.
2. Remote: user equipment data is looped back after passing through the SAM LIU.
3. Local: ATM network data is looped back to the network after local processing by the NIM but before passing through the SAM LIU to user equipment.

These loopback options are shown graphically on the next page. The top view shows a simple circuit without any loopbacks. The middle view shows NIM loopbacks breaking the circuit connectivity. SAM loopbacks are shown in the following figure.

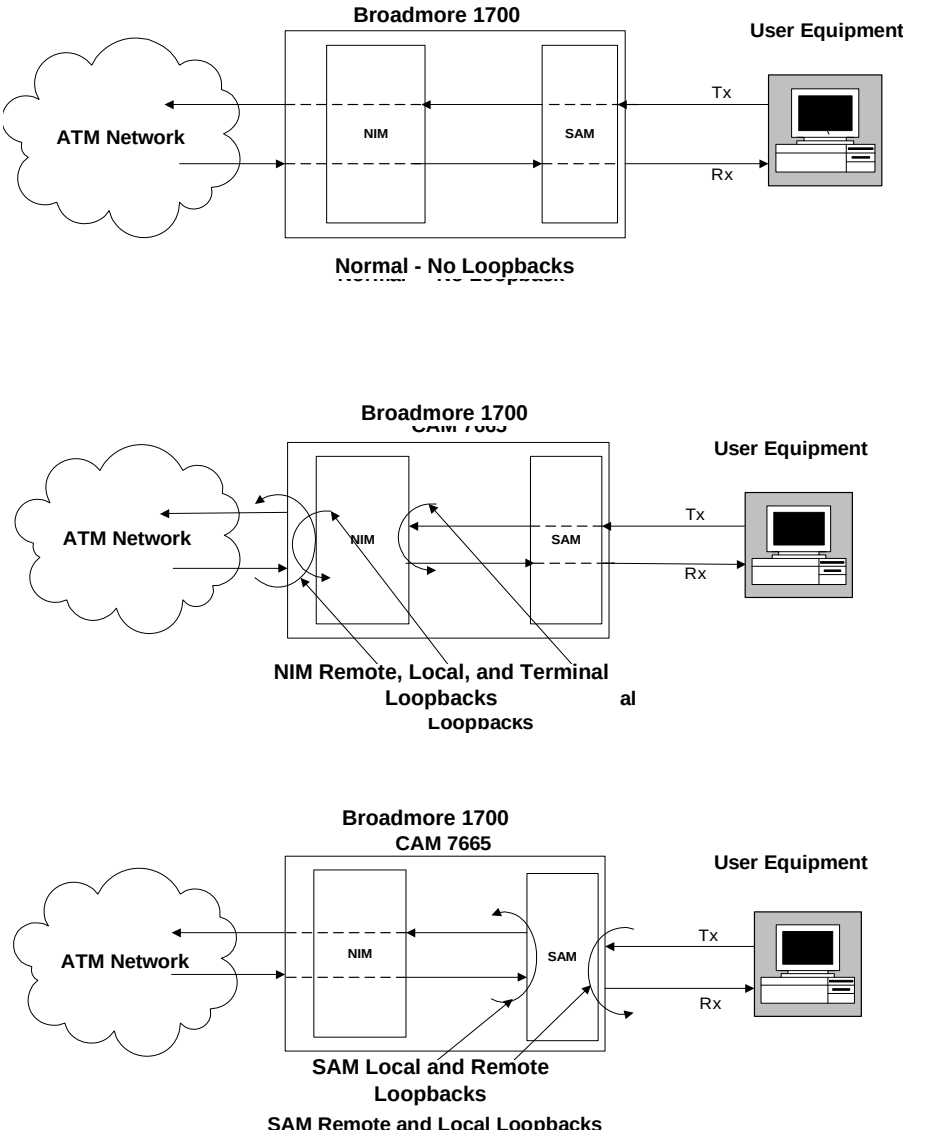


Figure 8-1: Loopback Options

Failure Recovery

Failure recovery is the sequence of events necessary to bring the Broadmore 1700 back to fully operational status after an unexpected service interruption. Throughout the recovery process, specific problems should be corrected following the troubleshooting flowchart (see Figure 8-2). The steps in failure recovery are:

1. Ensure sufficient stable electrical power is available to both the Broadmore 1700 and the control station. Observe successful completion of POST.
2. Establish communications from the control station to the Broadmore 1700. Login and view alarm LEDs. Correct all hardware problems before proceeding.
3. The Broadmore 1700 will automatically load the configuration that was saved for power-up. PVC and originated SVC connections will be re-established to the ATM backbone. Far-end originated SVCs must be re-established from the far end. Alternate previous system configurations may be loaded if they were saved. This is accomplished following the sequence below.

   Select **System Management** ↵
   Select **Configuration Files** ↵
   Select the saved file to restore ↵

4. Monitor activity for individual slots and for the ATM connection.
5. Verify configuration of the NIM(s). Validate parameter settings with the ATM switch if in doubt. Check the timing options and other parameters. Change as necessary to eliminate any errors.
6. Verify configuration of the installed SAMs. Monitor individual slots.
7. Monitor activity for the ATM connection. Select individually defined circuits to edit or connect and monitor the respective port.

The failure recovery process is a logical sequence of events to restore connectivity. With monitoring and corrective action at each step, the process includes:

- Providing power to the Broadmore 1700 and control station
- Establishing connectivity between the Broadmore 1700 and control station
- Loading the Broadmore 1700 configuration
- Establishing ATM connectivity
- Establishing individual circuit connectivity

Alarm Response/Reset

Alarms are designed into the Broadmore 1700 to provide the initial indication of a communications problem and to help isolate the problem. For example, a major alarm from a SAM identifies the specific module that will focus response efforts. The following flowchart approach to troubleshooting is based upon the initial alarm indication. The alarm response is a three-step process:

1. Troubleshoot to isolate the cause.
2. Complete corrective action to eliminate the alarm condition.
3. Return system to full operation and document the events.

Alarms are designed as real-time alerts. Thus, elimination of the alarm condition will automatically reset the associated alarm.

Flowchart

The top-level troubleshooting flowchart is shown on the below. LED indicators are the basis for entry into the flowchart, which will lead to the most likely problem(s) and recommended solution(s). The control system alarm indication can also be used as an entry point. In either case, use of this flowchart provides a logical approach to troubleshooting in the event that a problem is encountered.

CAUTION! SOME STEPS IN THE FOLLOWING FLOWCHART MAY CAUSE DISRUPTION IN SERVICE.

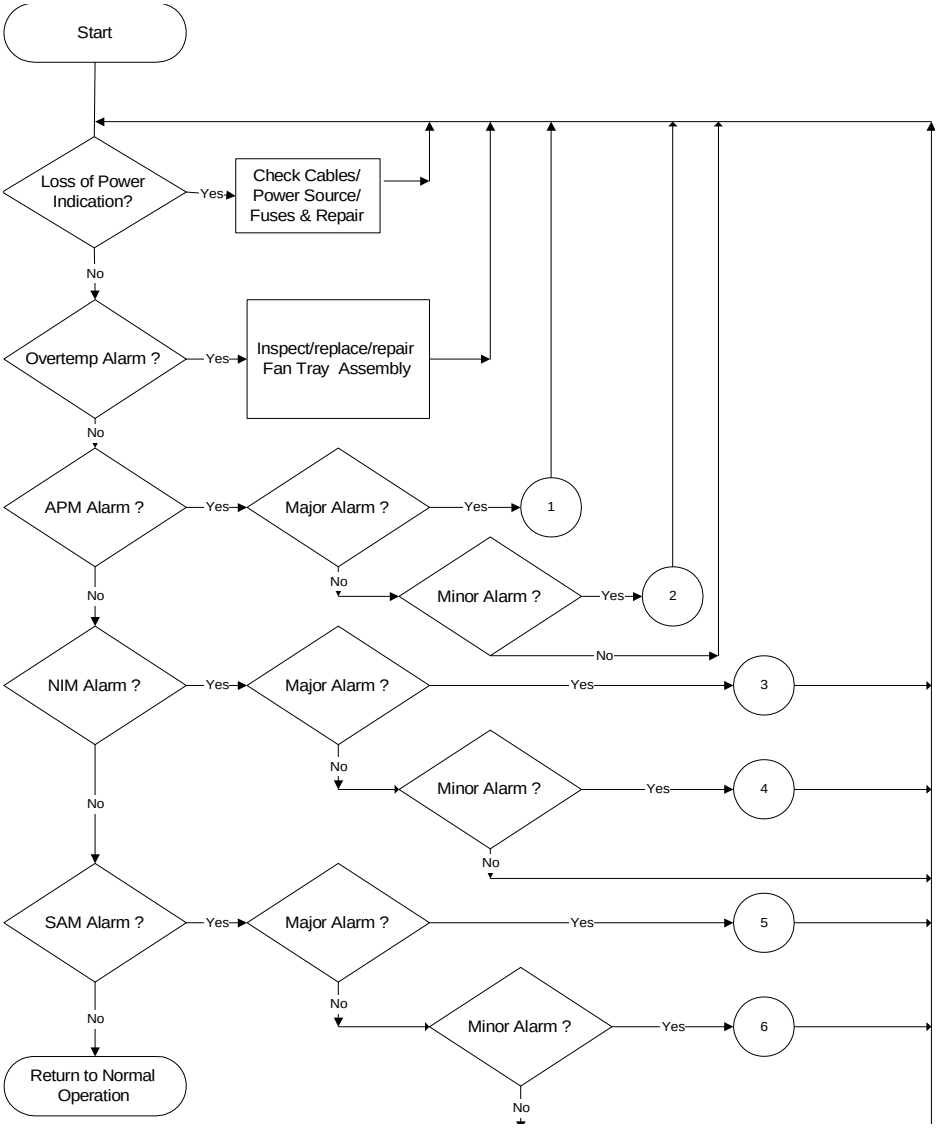


Figure 8-2: Troubleshooting Flowchart Based On LEDs

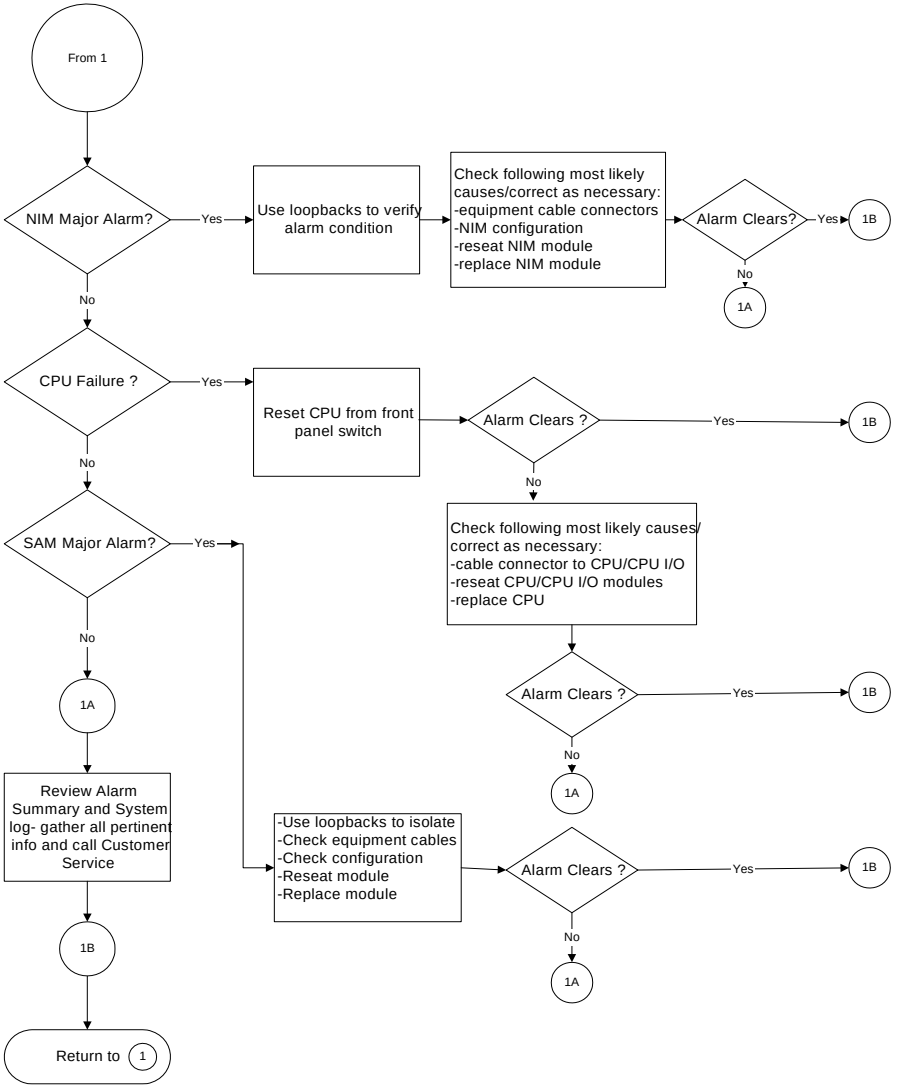


Figure 8-3: APM Major Alarm Troubleshooting Flowchart

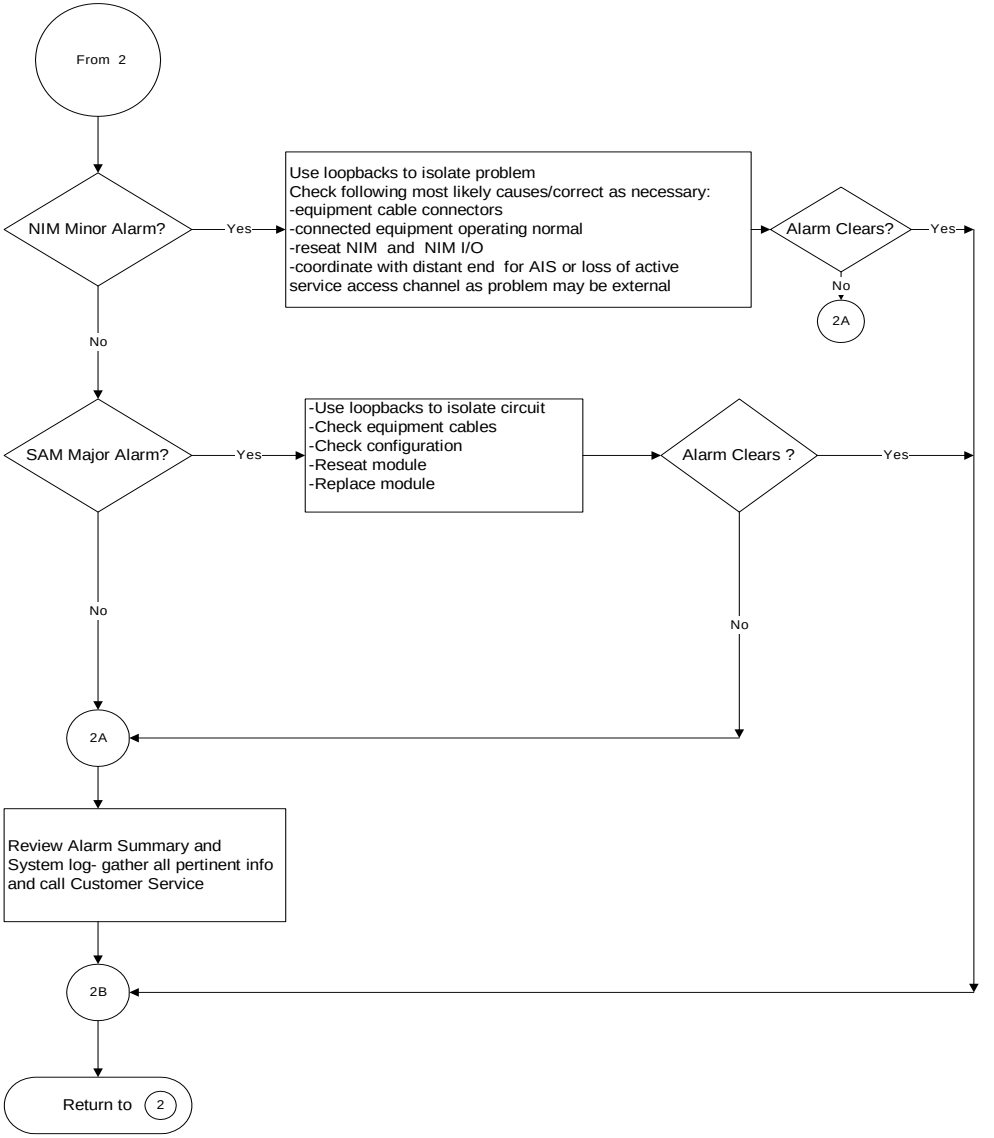


Figure 8-4: APM Minor Alarm Troubleshooting Flowchart

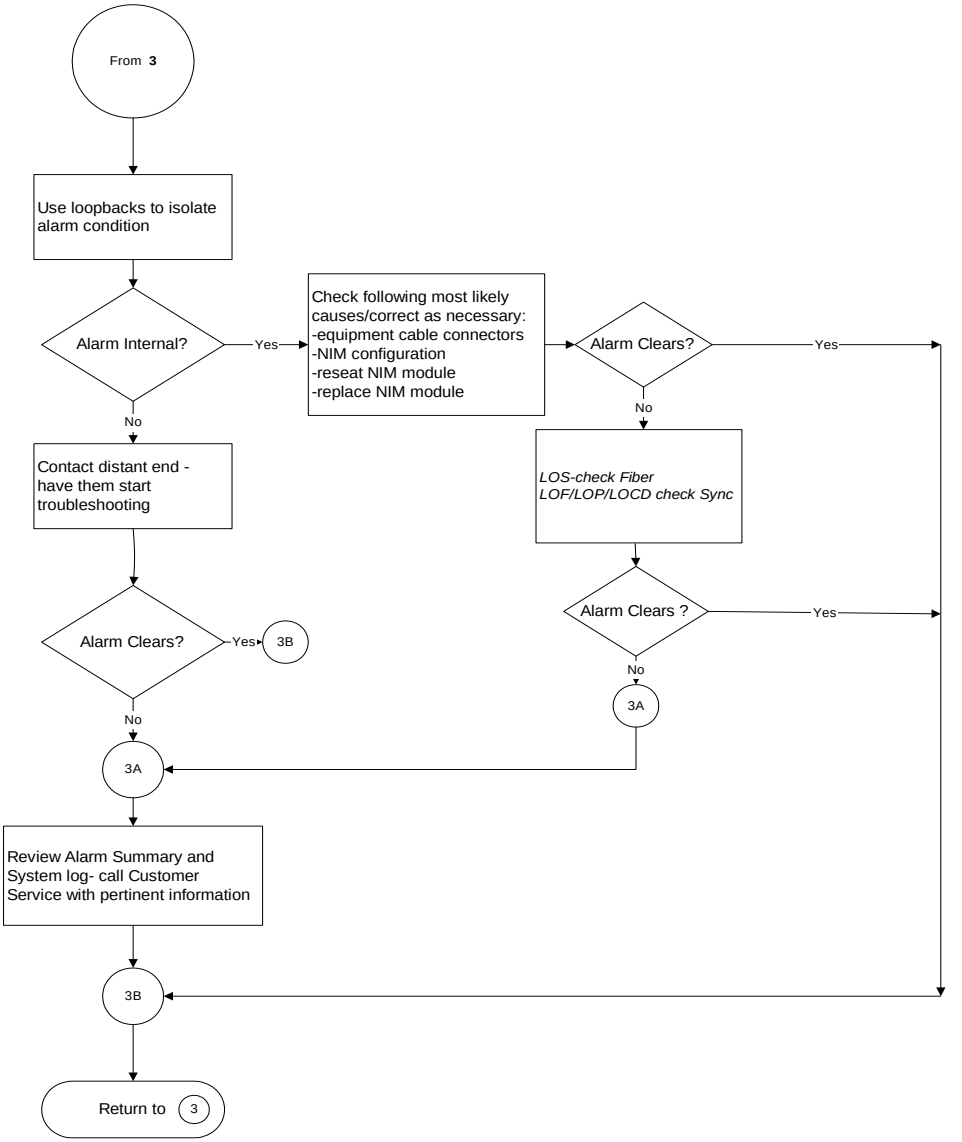


Figure 8-5: NIM Major Alarm Troubleshooting Flowchart

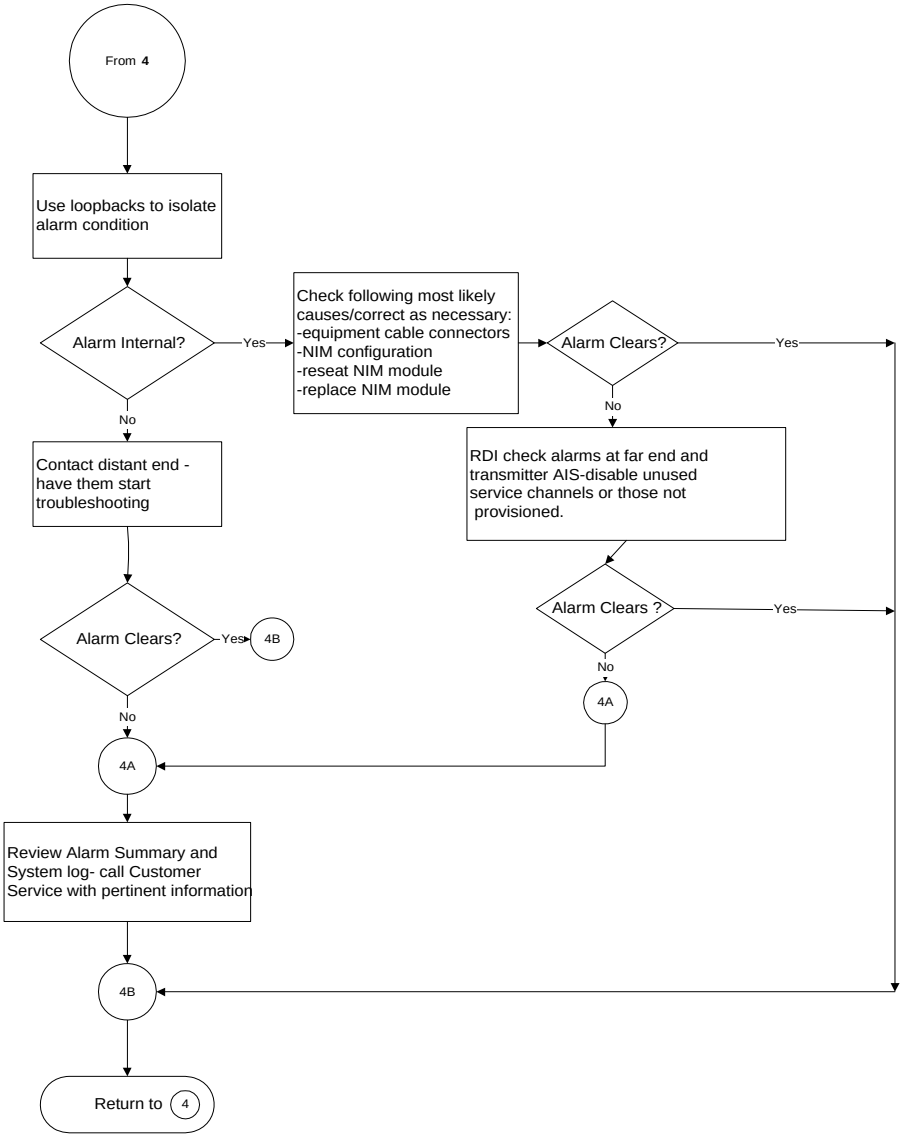


Figure 8-6: NIM Minor Alarm Troubleshooting Flowchart

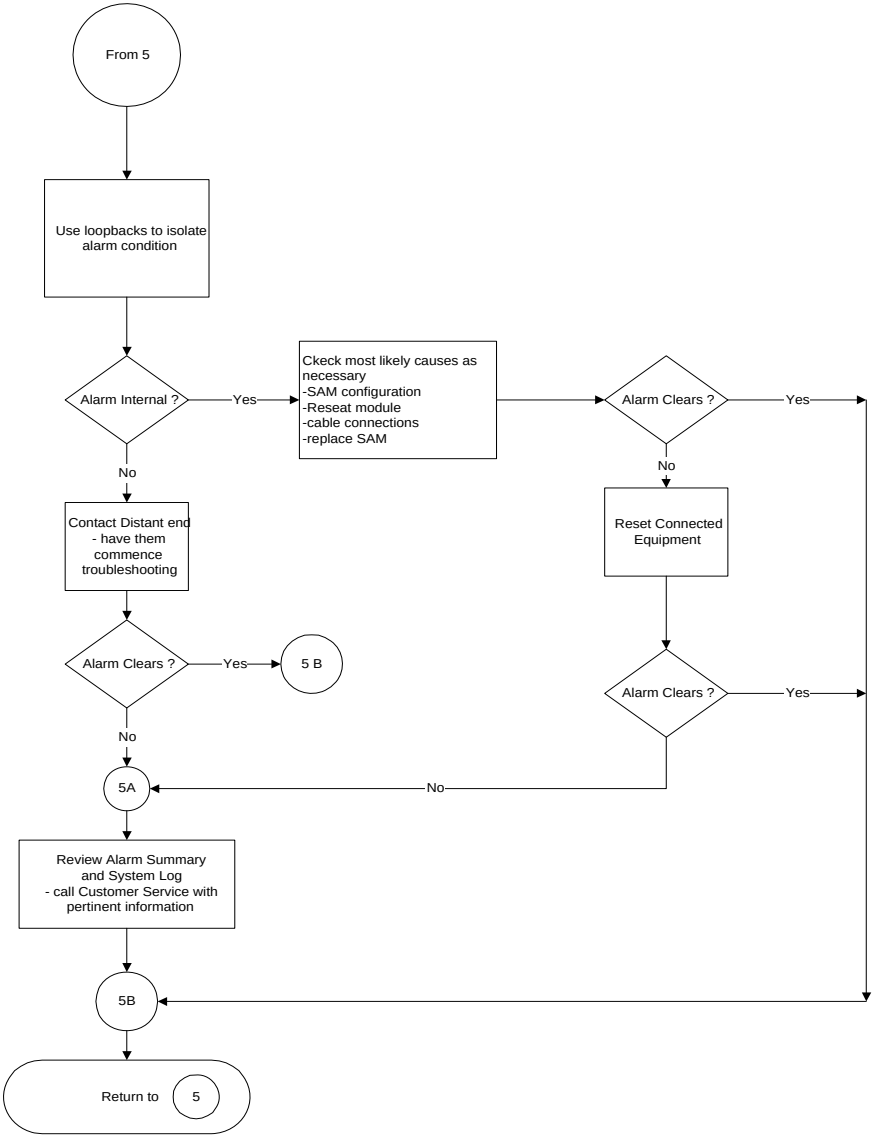


Figure 8-7: SAM Major Alarm Troubleshooting Flowchart

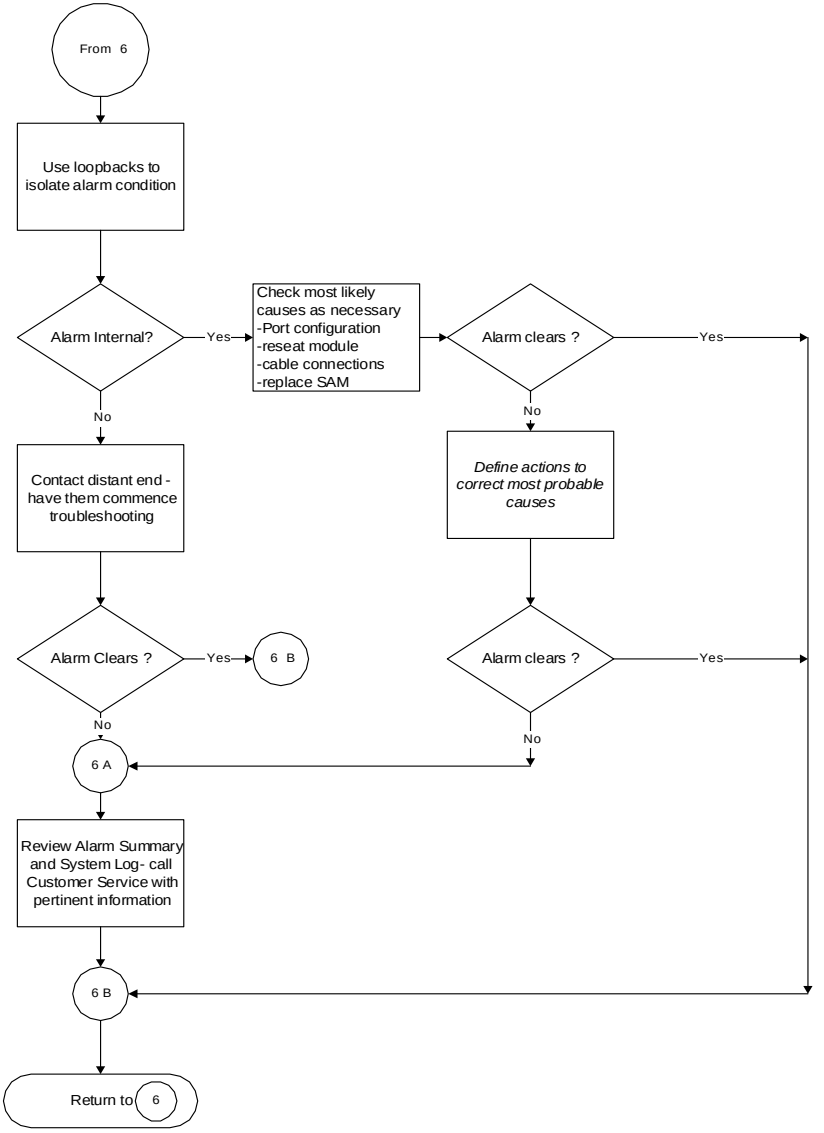


Figure 8-8: SAM Minor Alarm Troubleshooting Flowchart

Repair/Replacement

- Power Supply ... *8-37*
- NIM Replacement ... *8-38*
- SAM Replacement ... *8-39*
- IOM Replacement ... *8-40*
- CPU Replacement ... *8-41*
- CPU IOM Replacement ... *8-42*
- Fan Replacement ... *8-42*
- Integrated Fan/Alarm Module Replacement ... *8-43*
- Fan Filter Cleaning and Replacement ... *8-45*

All repair/replacement actions should be accomplished by a qualified technician familiar with the Broadmore 1700 system. The majority of repairs involve troubleshooting and the replacement of a module or card.

WARNING! **USE ESD PRECAUTIONS: WEAR AN ESD GROUNDING STRAP WHILE HANDLING ANY CARDS OR ACCESSING THE INSIDE OF THE BROADMORE 1700. FAILURE TO FOLLOW ESD PROCEDURES MAY DAMAGE SENSITIVE COMPONENTS AND VOID THE WARRANTY.**

CAUTION! **MODULE REMOVAL AND INSERTION – ON A POWERED-UP SYSTEM, WAIT AT LEAST 15 SECONDS AFTER ANY MODULE REMOVAL OR INSERTION TO ALLOW THE SYSTEM TO STABILIZE. FAILURE TO FOLLOW THIS PROCEDURE MAY RESULT IN SYSTEM ERRORS REQUIRING TOTAL SYSTEM REBOOT. WHEN INSTALLING A REPLACEMENT CPU IN A REDUNDANT CPU SYSTEM, DO NOT REBOOT OR POWER DOWN THE SYSTEM BEFORE CPU SYNCHRONIZATION IS COMPLETE AS INDICATED BY A SOLID STATUS LIGHT.**

Power Supply

The Broadmore 1700 receives –48 VDC power at the chassis rear from a user-provided source. Repair/replacement of this source is external to the scope of this manual.

Optionally, the Broadmore 1700 may be configured with a dual AC power supply. When an AC power source is used, the following steps are required to repair/replace a power supply module. The power supply is usually mounted directly below the Broadmore 1700. The power supply design facilitates module replacement without any wiring or power disruption.

1. Ensure that a replacement module is available.
2. Identify the problem module by front panel indicator that will not be illuminated and a software alarm will be received at the control station. Additionally, the integrated fan/alarm module front panel will give a no-power indication (from *green* to *off*) for the defective module.
3. Turn the power switch ***off*** for the defective power module. Loosen the front panel screws and remove the defective module by pulling it straight out the front. Fully insert the replacement module, ensuring proper alignment. Turn the power switch ***on*** and the front panel should display *green.* The power modules are hot-swappable. This replacement will not impact Broadmore 1700 operation due to the built-in redundancy feature.
4. Tighten the front panel retainer screws to hold the new module in place.
5. Contact Customer Service and return the defective module for repair.

NIM Replacement

There are no field repairable items on a NIM. In a redundant configuration, replacement of a NIM will not impact user ATM network connectivity. In a single NIM configuration, all ATM connectivity will be disrupted during card replacement. Follow the steps below to replace a NIM.

1. Ensure that a replacement module is available.
2. Remove the chassis front cover. The Broadmore 1700 retains power; individual NIMs may be removed/replaced without power disruption to other modules.
3. Remove the fiber connection and loosen retaining screws at the top and bottom of the NIM to be replaced.
4. Use the installed ejectors to unseat the NIM from the midplane. This is done by simultaneously pushing up on the top ejector and down on the bottom ejector.
5. Remove the defective NIM by pulling it straight out the front.
6. Insert the replacement NIM, ensuring correct alignment with the card guides and midplane connector.
7. Firmly press the new NIM into place so that it is fully seated with the midplane connector. Tighten both retaining screws (finger tight only) and install the fiber connection.
8. The new NIM will automatically configure to the last known configuration of the slot where installed. In a single NIM system (non-redundant), originated SVCs and PVCs will be re-connected per the current connection list. The far end must originate the re-connection of incoming SVCs. Monitor activity for the ATM connection to ensure proper operation
9. Replace the chassis front cover.
10. Return the defective NIM for repair. Contact Customer Service for a Return Material Authorization (RMA) number and detailed procedures.

SAM Replacement

There are no field repairable items on a SAM. All user equipment connected to the defective SAM will lose ATM connectivity during replacement. Equipment connected via other SAM(s) will not have an ATM service disruption. Follow the steps below to replace a SAM:

1. Ensure that you have a replacement module.
2. Remove the chassis front cover. The Broadmore 1700 retains power; individual SAM cards may be removed/replaced without service disruption to other modules.
3. Loosen retaining screws at the top and bottom of the SAM to be replaced.
4. Use the installed ejectors to unseat the SAM from the midplane. This is done by simultaneously pushing out on the ejectors.
5. Remove the defective SAM by pulling it straight out the chassis front.
6. Insert the replacement SAM, ensuring correct alignment with the card guides and midplane connector.
7. Firmly press the new SAM into place so that it is fully seated with the midplane connector. Tighten both retaining screws (finger tight only). The new card configuration will be identical to the last configuration for the installed slot.
8. Replace the chassis front cover.
9. Return the defective SAM for repair. Contact Customer Service for an RMA number and procedures.

IOM Replacement

There are no field repairable items on an IOM. All user equipment connected to the defective IOM will lose ATM connectivity during replacement. Equipment connected via other IOM(s) will not have an ATM service disruption. Follow the steps below to replace an IOM:

NOTE: If the HSAM Bridge card must be replaced, temporarily remove the HSAM IOMs in slots D and E to replace the bridge card.

1. Ensure that you have a replacement module.
2. Remove the chassis rear cover. The Broadmore 1700 retains power; individual IOM cards may be removed/replaced without service disruption to other modules.
3. Remove and tag the cables on the IOM to be replaced.
4. Loosen retaining screws at the top and bottom of the IOM to be replaced.
5. Use the installed ejectors to unseat the IOM from the midplane. This is done by simultaneously pushing out on the ejectors.
6. Remove the defective IOM by pulling it straight out the chassis.
7. Insert the replacement IOM, ensuring correct alignment with the card guides and midplane connector.
8. Firmly press the new IOM into place so that it is fully seated with the midplane connector. Tighten both retaining screws (finger tight only). The new card configuration will be identical to the last configuration for the installed slot.
9. Reconnect the cables to the IOM.
10. Replace the chassis rear cover.
11. Return the defective IOM for repair. Contact Customer Service for an RMA number and procedures.

CPU Replacement

CAUTION! **WHEN INSTALLING A REPLACEMENT CPU IN A REDUNDANT CPU SYSTEM, DO NOT REBOOT OR POWER DOWN THE SYSTEM BEFORE CPU SYNCHRONIZATION IS COMPLETE AS INDICATED BY A SOLID STATUS LIGHT.**

NOTE: Each CPU card has a battery that should be replaced periodically. See Appendix B, *Spare Parts List* for part number information.

There are no field-repairable items on the CPU card. In a single CPU system, there will be a disruption of system control and administration during card replacement. In a redundant CPU system, user PVCs through the ATM network remain intact during replacement of a CPU. In a single CPU system, the CPU will reboot and all PVC service is interrupted. Follow the steps below to replace a CPU Card:

1. Ensure that you have a replacement module.
2. Remove the chassis front cover.
3. Loosen retaining screws at the top and bottom of the CPU card to be replaced. The Broadmore 1700 retains power; the CPU card may be removed/replaced without service disruption to other modules.
4. Use the installed ejectors to unseat the CPU card from the midplane. This is done by simultaneously pushing up on the top ejector and down on the bottom ejector.
5. Remove the defective CPU card by pulling it straight out chassis front.
6. Insert the replacement CPU card, ensuring correct alignment with the card guides and midplane connector.
7. Firmly press the new CPU card into place so that it is fully seated with the midplane connector. Tighten both retaining screws (finger tight only).

NOTE: The new CPU card will come configured. Any necessary updates will be announced.

8. Replace the chassis front cover.

9. Return the defective CPU card for repair. Contact Customer Service for an RMA number and detailed procedures.

CPU IOM Replacement

There are no field-repairable items on the CPU IOM. This module does contain a unique part of the Ethernet address such that the Broadmore 1700 system must be alerted to the module change in order to restore service. Only replace the standby CPU or CPU IOM on a redundant system. This will ensure correct programming of the IP, CIP, LANE and ATM address information.

1. Ensure that you have a replacement CPU IOM with remote reboot jumpers set correctly.
2. Follow the CAMMI sequence on the next page to signal CPU IOM removal.
3. Loosen top and bottom retaining screws, use ejectors to unseat the module, and pull it straight out to remove.
4. Insert the replacement CPU IOM, ensuring alignment with the guides and midplane connector. Tighten both retaining screws (finger tight only).
5. Return the defective CPU IOM for repair. Contact Customer Service for an RMA number and detailed procedures.

Fan Replacement

There are no field-repairable items on the fan tray assembly. The air filters are removable for routine cleaning. Removal and replacement of the fan assembly will not impact normal Broadmore 1700 operations. This replacement should be completed quickly to avoid possible overheating of Broadmore 1700 components. Follow the steps below to replace the fan assembly.

1. Ensure that you have a replacement fan tray assembly.
2. Loosen retaining screws on both sides of the fan assembly to be replaced.
3. Remove the defective fan assembly from below the chassis by pulling it straight out.
4. Insert the replacement fan assembly, ensuring alignment in the side guides.

5. Firmly press the new fan assembly into place so that it is fully seated. Tighten both retaining screws (finger tight only).
6. Return the defective fan assembly for repair. Contact Customer Service for an RMA number and procedures.

Integrated Fan/Alarm Module Replacement

The integrated fan/alarm module has two replaceable fuses. Follow the steps below to replace a fuse.

1. Fuses and spares are located on the integrated fan/alarm module IOM accessed from the chassis rear. Remove the rear cover to access the fuses.
2. Blown fuses are removed by pulling directly out.
3. Remove a spare fuse by pulling directly out. Insert the replacement fuse(s) in reverse to the one(s) removed.
4. Obtain replacement spare fuse(s) locally. Use only 7.5 Amp (Bussman part # GMT7.5) or equivalent.

CAUTION! FUSES ARE ONE-TIME USAGE ITEMS. IF THE FUSE BLOWS A SECOND TIME, CIRCUIT DAMAGE MAY BE MORE EXTENSIVE, AND IT MAY BE NECESSARY TO DISCONNECT POWER TO THE ENTIRE BROADMORE 1700 CHASSIS AND REPLACE THE INTEGRATED FAN/ALARM MODULE. ALL CIRCUITS WILL EXPERIENCE A DISRUPTION DURING INTEGRATED FAN/ALARM MODULE REPLACEMENT.

Follow the steps below to replace an integrated fan/alarm module.

1. Ensure that a replacement module is available.
2. Remove the chassis front cover.
3. Loosen retaining screws at the top and bottom of the integrated fan/alarm module card to be replaced.
4. Use the installed ejectors to unseat the integrated fan/alarm module card from the midplane. This is done by simultaneously pushing up on the top ejector and down on the bottom ejector.

5. Remove the defective integrated fan/alarm module card by pulling it straight out the chassis front.
6. Insert the replacement integrated fan/alarm module, ensuring correct alignment with the card guides and midplane connector.
7. Firmly press the new integrated fan/alarm module into place so that it is fully seated with the midplane connector. Tighten the retaining screw (finger tight only).
8. Replace the chassis front cover.
9. Return the defective integrated fan/alarm module for repair. Contact Customer Service for an RMA number and procedures.

General Maintenance

- Fan Filter Cleaning and Replacement ... *8-45*
- Maintenance/Diagnostics ... *8-46*
- Engineering Analysis ... *8-48*

The Broadmore 1700 is designed to provide continuous service with minimal maintenance provided operational conditions remain within specifications. Cooling fans are included in the Broadmore 1700 to aid in heat dissipation. The fan tray has two air filters, one on each fan. Complete the fan filter cleaning procedure below on a monthly basis, or more often if the filters are excessively dirty. It is not necessary to power down the Broadmore 1700 for this routine maintenance.

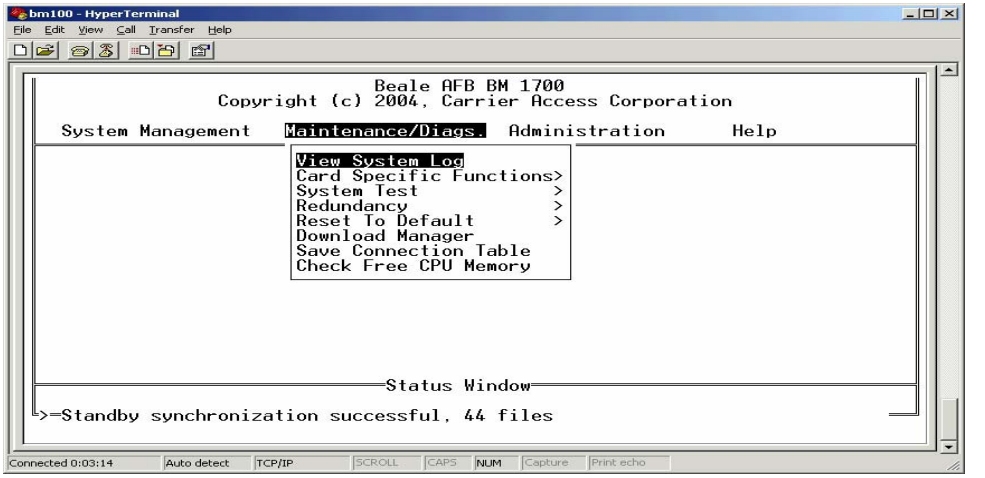
Fan Filter Cleaning and Replacement

1. Loosen screws and remove the chassis front cover.
2. Loosen screws and remove the fan tray by pulling on the Center handle.
3. Remove the fan filters from the bottom of the tray. Each filter snaps out of place.
4. The filters are re-usable. Wash them in a mild soapy solution, clean thoroughly, and dry completely. Additional filters may be obtained from the manufacturer, Globe Motors (part # FFM745) or Customer Service. Filters may be replaced with new ones.
5. Snap the filters back into place.
6. Re-install the fan tray, being careful to align it in the guides provided. Ensure the fans are operating properly, then tighten the screws to hold the fan tray in place.
7. Replace the chassis front panel and secure retaining screws.

NOTE: This is the only routine maintenance required for the Broadmore 1700.

Maintenance/Diagnostics

CAMMI provides access to several maintenance and test functions under the Maintenance/Diagnostics main menu. These items are explained below.

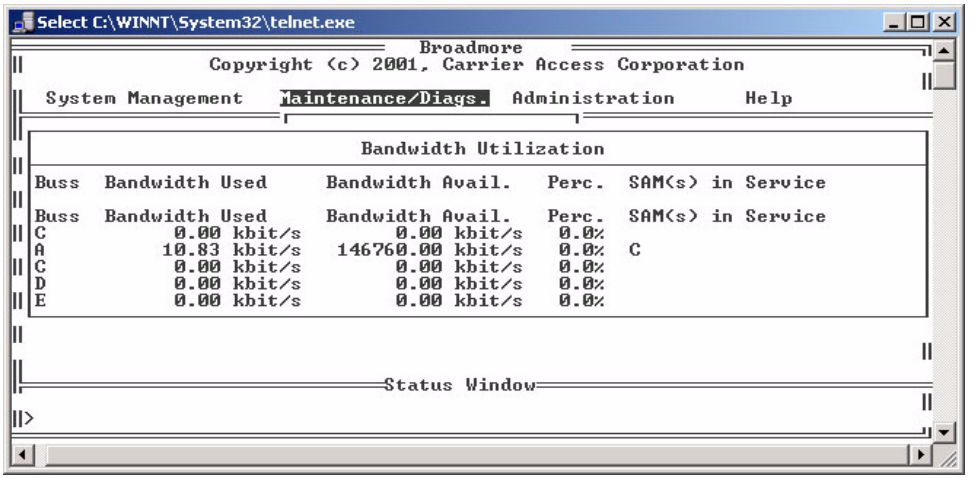


View System Log provides a historical record of events, such as configuration, establishing a PVC, or other action that affects service. Messages are filtered by privilege level. All messages are displayed at the Supervisor level, many of which can only be interpreted by Carrier Access engineers.

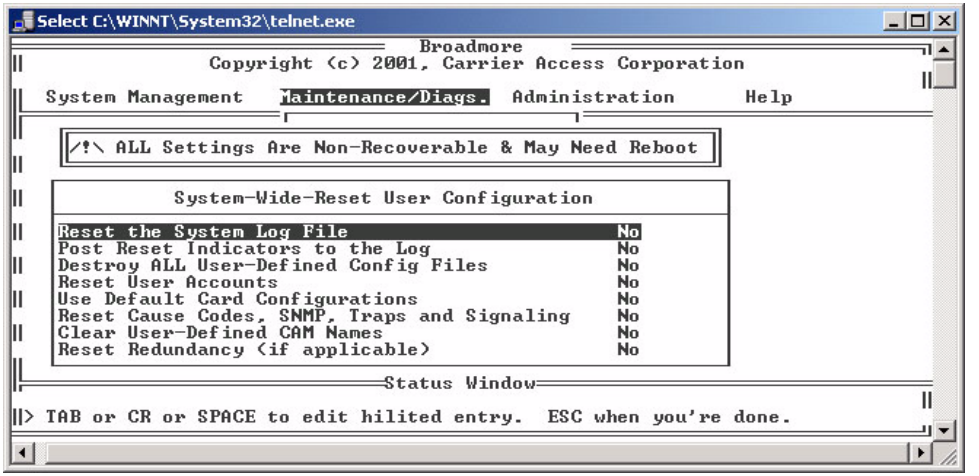
Environmental Indicators will display the current status of power, fan, and BITS clocks. This display, shown below, does not indicate fan removal.

```
System Status

Power source A missing!
Power source B present.
Chassis fan operational.
Bits On card A :Present
Bits On card B :Present
```

Bandwidth Utilization displays the amount of bandwidth being used, the remaining bandwidth available, and the percentage used. This can be recalculated by pressing 'R' when the screen displays.


Select C:\WINNT\System32\telnet.exe
Broadmore
Copyright (c) 2001, Carrier Access Corporation
System Management
Maintenance/Diags.
Administration
Help
Bandwidth Utilization
Buss Bandwidth Used Bandwidth Avail. Perc. SAM(s) in Service
Buss Bandwidth Used Bandwidth Avail. Perc. SAM(s) in Service
C 0.00 kbit/s 0.00 kbit/s 0.0%
A 10.83 kbit/s 146760.00 kbit/s 0.0% C
C 0.00 kbit/s 0.00 kbit/s 0.0%
D 0.00 kbit/s 0.00 kbit/s 0.0%
E 0.00 kbit/s 0.00 kbit/s 0.0%
Status Window
>

Reset to Defaults allows you to reset the Broadmore 1700 to the factory defaults settings.


Select C:\WINNT\System32\telnet.exe
Broadmore
Copyright (c) 2001, Carrier Access Corporation
System Management
Maintenance/Diags.
Administration
Help
/!\ ALL Settings Are Non-Recoverable & May Need Reboot
System-Wide-Reset User Configuration
Reset the System Log File No
Post Reset Indicators to the Log No
Destroy ALL User-Defined Config Files No
Reset User Accounts No
Use Default Card Configurations No
Reset Cause Codes, SNMP, Traps and Signaling No
Clear User-Defined CAM Names No
Reset Redundancy (if applicable) No
Status Window
> TAB or CR or SPACE to edit hilited entry. ESC when you're done.

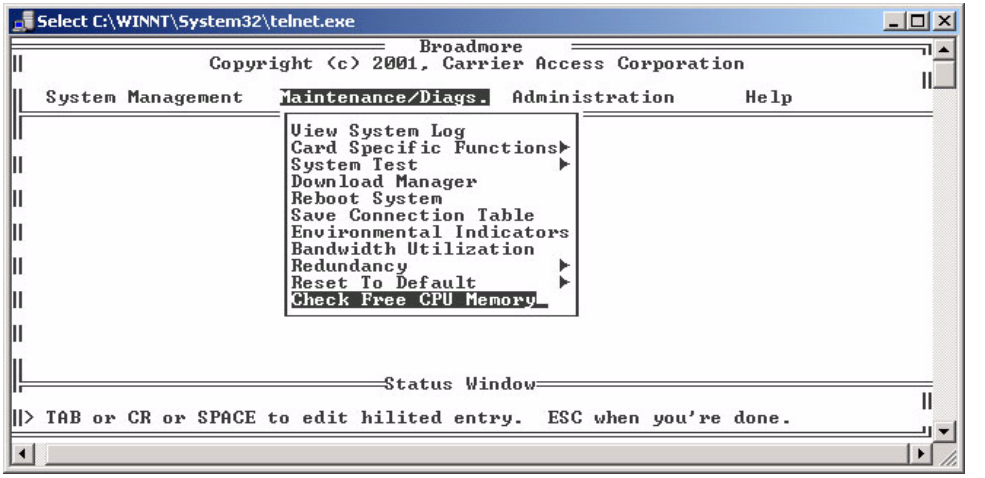
Engineering Analysis

There are several options under the **Maintenance/Diags**. menu reserved for use by Carrier Access engineers. A brief description is provided to aid in understanding the system. These commands should only be used in close coordination with Carrier Access engineers.

Card Specific Functions leads to individual card diagnostics where you **Peek and Poke** various information fields.

System Test runs a preconfigured test and displays statistical results. The **Asserts** option will activate additional software traps to provide more detailed information.

Check Free CPU Memory will display the free memory available as shown below. This is valuable information for coordination with Carrier Access engineers.



Download Manager is used to download new software. Instructions are provided with any new software distributed.

Reboot System reboots the CPU and uses the system setting in place prior to the reboot. This option is necessary when downloading new software revisions or changing your system's IP address. The changes you make to system settings will not take effect until you reboot.

Save Connection Table will preserve the current connection data in a text file format so that it can be viewed using any standard text editor.

Summary of Front Panel LEDs

The following table provides descriptions of the front panel LEDs for the Broadmore 1700.

Module	LED Display	LED Color	Definition
APM	Major Alarm	Red	Major fault for 2.5 seconds
		None	No major alarm
	Minor Alarm	Amber	Minor alarm for 2.5 seconds
		Not Lit	No minor alarm
	*Over Temp	Red	* Not supported at this time.
		Not Lit	
	Power	Green	-48 volts power is present
		Not Lit	No power present
	Power Fail A/B		
NIM	Fault	Red	Major alarm detected for 2.5 seconds
		Amber	Minor alarm detected for 2.5 seconds
		Green	Normal, no fault condition
	On-Line	Red-flashing	NIM failed POST, not ready
		Amber	NIM passed POST and in standby
		Green	NIM is online, normal
	LOS	Red	Loss of Signal
		Green	Acceptable optical receive power for 10 seconds
SAM	Fault	Red	Major alarm detected for 2.5 seconds
		Amber	Minor alarm detected for 2.5 seconds
		Green	Normal, no fault condition
		Not Lit	Out of service
	On-line	Red	SAM failed POST, not ready
		Amber	offline or standby
		Green	Normal, no fault condition
	Alarm	Red	Major

Module	LED Display	LED Color	Definition
		Amber	Minor
		Green	Normal, enabled
		Not Lit	Out of service
CPU	Master	Green	online as master
		Amber	Standby
	On-line Activity	Amber	Normal activity

CHAPTER 9

Command Line Interface

In this Chapter:

- CLI Access ... *9-2*
- Creating and Running Scripts ... *9-4*
- Port Configuration ... *9-9*
- Monitor ... *9-11*
- About Command ... *9-12*

CLI Access

The Command Line Interface (CLI) provides much the same functional control of the Broadmore 1700 as the CAMMI program. CLI commands are entered as text. The command prompt displays the current location from the cascading menu structure that parallels CAMMI. Therefore, a working knowledge of CAMMI makes navigation through the CLI easier.

NOTE: Use CAMMI for administrative features such as changing password or identifying new users.

Use the following steps for CLI access.

1. Log into the Broadmore 1700 operating system, pSoS, using a valid user name and password.

NOTE: At this point, you can type 'help' to view a list of commands. You can also type 'help *command*' to obtain help on any of the listed commands.

2. At the 1700> prompt, type 'cli' and press enter. The prompt changes to cli > for successful access to the CLI program.

NOTE: The security mode and user privilege level determines the CLI commands available to each operator.

3. Type **?** and press enter at any prompt to display the available commands, both general and for the current command level.
   - General commands apply at all levels.
   - quit: exits the CLI program
   - up: moves up one level in the command structure
   - clear: removes data from the screen except the last line, which is displayed at the top of the screen

Enter commands that are not case sensitive as they appear when viewed using the **?** query. The spelling must be correct.

Example:

At the cli > prompt, enter **?** to display a list of available commands. At this point, the level commands are:

- sys
- maintain
- about

General commands are:

- up
- clear
- quit
- ? (help)

The level commands correspond with the CAMMI main menu except for the administrative function, which is not implemented in CLI.

Creating and Running Scripts

To reduce the time required to configure one or more Broadmores, you can script a series of CLI commands that can be saved to a file, uploaded to the Broadmore, and executed as needed.

You can use a text editor or a terminal emulation program to create a script file. For example, using a terminal emulator such as Symantec Procomm®, you can perform the following steps to create a script and save it to a file.

Select **Tools** ↵
Select **Scripts** ↵
Select **Start Recorder** ↵
Enter the appppropriate sequence of **CLI** commands
Select **Stop Recorder** ↵
Save the script as <filename> of choice

A script file must meet the following requirements:

- Each command must begin on a new line.
- Comments or unused configuration settings must begin with a semicolon (**;**). When the Broadmore's script interpreter encounters a semicolon, all remaining text on the line is ignored.
- The file must be named as follows:

  filename.scp

To upload and run a script file:

1. Log in to the Broadmore using FTP or SFTP. See *"FTP Login" on page 10-21* or *"SFTP Login" on page 11-43* for instructions.
2. Using your FTP or SFTP software, upload the script file to the **script** directory on the Broadmore's online CPU.
3. Log into the Broadmore's operating system using a valid user name and password. At the 1700> prompt, enter the following command:

   `runscript` *`filename`*

NOTE: On a redundant Broadmore, you must also load and run the script file on the standby CPU.

After you issue the `runscript` command, the script interpreter switches to the CLI> prompt and executes all commands from the script file in sequence. Each command is displayed as if you were manually entering it during a normal session.

If the script interpreter encounters an error, it continues to attempt to interpret each successive line until it can execute a valid command. If no valid commands can be executed before the end of the file, review the error displayed to resolve the problem.

It is recommended that all logs be reviewed after at least the first time a script is executed to ensure that the script performed as intended.

NOTE: Depending upon the load on the Broadmore's CPU, you may need to insert delays between some commands in a script. For example, when using a 'deleteall' command, you may need to insert a delay before attempting to reconfigure the same resources. You may also need delays when configuring a large number of PVCs in a row. Depending upon system load, delays of up to 30 seconds may be necessary between some script commands. To insert a delay between script commands, use the following command:

`sleep(`*`n`*`)`

where *n* = 1 to 30 seconds.

Sample scripts are shown below and on the following pages. See Appendix H for a list of Broadmore commands.

NOTE: Do not use the following command in a script:

`showi`

This command is "show interactive" for the system log and requires user input. If necessary, use the `show` command instead.

Example 1 - Script for Configuring Port 1 on a DS1 Card

```
; This script configures Port 1 services on a DS1 card in Slot F.
;-------------------------------------------------------------------

sys config slot f port1   ;(sys/config for slot f port 1)

Port Name
t1 port1                  ;Port name

Port Mode
online
;offline

Service Clocking Mode
;SRTS Timing
;Adaptive Timing
;Synchronous Network
;Synchronous Loop
Synchronous NIM
;Synchronous Port         ;(only used with clock recovery type setting)
Clock Recovery Type       ;(only used with synchronous Port service
                          ; clocking mode otherwise left undefined)
Undefined
;Master
;Slave

Clock Recovery Interface  ;(only used with synchronous Port service
                          ;clocking mode)
Undefined
```

```
;Transmit Line
;Receive Line

Line Buildout
0 to 110 feet
;110 to 220 feet
;220 to 330 feet
;330 to 440 feet
;440 to 550 feet
;550 to 660 feet
Loopback
Normal
;Local
;Remote

CBR Service Type
Unstructured
;Structured No CAS
;Structured with CAS

Frame Type
Direct Unstructured
;Extended Superframe
;Superframe

CBR Signal Format
B8ZS
;AMI
save
up
up
up
up
```

Example 2- Script for Configuring a PVC on a DS1 Card

```
; This script configures a PVC connection named test on Port 1 of a
; DS1 card in Slot F.
;---------------------------------------------------------------------

sys config pvc
set
connectionname
test
localcard f
localport 1
portchannelmap 111111111111111111111111
txvpi 0
txvci 32
rxvpi 0
rxvci 32
cdv 80
save
```

Example 3- Script for Deleting a PVC on a DS1 Card

```
; This script deletes a PVC connection on Port 1 of a DS1 card in
; Slot F.
;---------------------------------------------------------------------

sys config pvc
delete test
save
```

Port Configuration

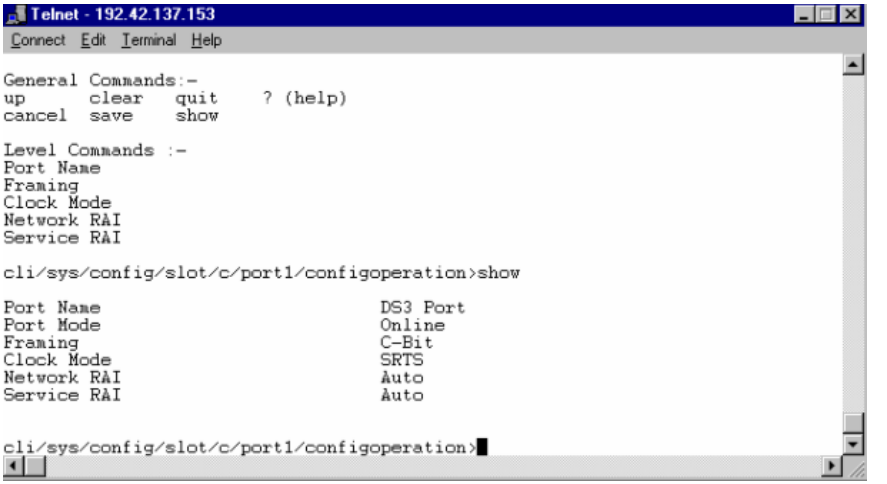
This example shows how to use CLI to configure a DS3 port. Follow the steps below from the initial CLI screen to access the configuration items. The steps shown below must be entered in sequence one-at-a-time. CLI does not support going directly to the last screen via a single entry (e.g., a single entry of sys/config/slot/c/port1/ configoperation/show will give an error for unrecognized entry).

Enter **sys** ↵	response is	cli/sys>
Enter **config** ↵	response is	cli/sys/config>
Enter **slot** ↵	response is	cli/sys/config/slot>
Enter **c** ↵	response is	cli/sys/config/slot/c>
Enter **port1** ↵	response is	cli/sys/config/slot/c/port1>
Enter **configoperation** ↵	response is	cli/sys/config/slot/c/port1/configoperation>
Enter **show** ↵	response is the screen shown on the next page	

Each parameter can be revised. For example, to change the port name to “DS3Port”, you would enter:

```
Port name  DS3 port
```

Then type **show** and press Enter to see that the change has taken effect.



Note that the above general commands have two additional items: cancel and save. The normal way to leave any screen is to enter up, which takes you to the previous command level. However, this is not allowed on this screen because configuration data has been altered. This data must be either discarded (cancel) or retained (save) before proceeding.

The command **cardstatus** will display a list of all Broadmore 1700 slots with the module installed. Cardstatus is available when you are in either of the following command levels:

cli/sys/config/slot

cli/sys/monitor/slot

Monitor

Use Monitor to display system operation statistics. The CLI command string to monitor and show port counters is displayed below. Note that an additional command, reset, is available. Reset will zero all counters. This was done as the first command in the display below.

```
cli/sys/monitor/slotstats/c/port2/counters>reset

cli/sys/monitor/slotstats/c/port2/counters>show

DS3 SAM (Online) Slot C Port 2 Counters
Tx Cells 311228                  Rx Cells 311227

Network Errors                   Service Errors                 Major Alarm YES
SEQ Errors     0                 BPV Errors     8670            Minor Alarm NO
SNP Errors     0                 EXZ Errors     8670
F-BIT Errors   0                 F-BIT Errors   8670
PARITY Errors  0                 PARITY Errors  7420
C-BIT Errors   0                 C-BIT Errors   7439
FEBE Errors    8670              FEBE Errors    510
BERT Errors    0                 BERT Errors    0

cli/sys/monitor/slotstats/c/port2/counters>
```

About Command

The About command, accessed from the initial CLI> prompt will provide information about each slot, including:

- Type module
- Module serial number
- Hardware revision
- Software revision

The information displayed is tailored for the type of module. This information is particularly valuable when contacting Customer Service.

CHAPTER 10

Security Management

In this Chapter:

- Security Features ... *10-2*
- Security Guidance ... *10-3*
- Logging In ... *10-5*
- Log-in Banner ... *10-6*
- System Clock ... *10-7*
- Network Time Protocol ... *10-8*
- Managing Users and Audit Trails ... *10-10*
- IP ICMP Messages ... *10-17*
- SNMP Messages ... *10-18*
- Shell Commands (Non-FIPS Mode) ... *10-19*
- FTP Login ... *10-21*

Security Features

The Broadmore provides the following security features:

- User ID and password authentication
- Four levels of user privileges for accessing command functions
- Configuration activity audit trails
- Enable/disable SNMP and ICMP messages
- SNMPv3 USM/VACM
- Log-in Banner for special user instructions

Only the Network Administrator (SuperUser) can create and modify user accounts, set access privileges, and monitor user activity audit trails. The Broadmore requires that users log into the Broadmore through Telnet and FTP.

NOTE: Be sure to use the appropriate fonts and screen settings to maintain the proper screen appearance.

Security Guidance

- **Receipt and Inspection** – Broadmore components containing operating system software are packaged and sealed at the factory with tamper-proof security tape. Upon receipt, carefully examine the security sealing tapes on the shipping containers for any signs of tampering. (See *"Receipt" on page 3-2*.)
- **Security** – Broadmore components containing operating system software (CPU modules, memory modules, and storage media) should be handled in accordance with applicable security procedures.
- **Initial Login** – The Broadmore is shipped with a default username and password for logging in the first time. A SuperUser should log in the first time to configure the Broadmore for secure operation.
  For maximum security, perform the following steps:
  (1) configure IP access (via ethernet, LANE, or CIP)
  (2) create a temporary SuperUser account
  (4) delete the public SYSADMIN account and log out
  (5) after logging in securely, you can safely create user accounts and configure the Broadmore for secure operation.
- **Security Modes** – The Broadmore is shipped with security turned off. Only a SuperUser can change the FIPS and SecurID modes. If these security modes are required, see next chapter.
- **Potential Security Vulnerabilities**
  (1) The Broadmore accepts loose source routed IP packets, so it is recommended that source routed packets be dropped on routers and firewalls. (See manufacturer's instructions.)
  (2) The Broadmore RS-232 COM 1 serial port used for "Craft Access" does not immediately terminate a management session if a user disconnects without typing "exit". During the following timeout period, another user can connect without logging into the RS-232 port and other users are denied access through the ethernet port. It is recommended that all accounts be created with "Remote Access" only, except for one failsafe SuperUser account with "Craft Access." The craft password should be stored safely in the NOC. When needed, the SuperUser can log into the craft port, fix things, change the password, log out, and store the new password back in the NOC.

- **System Clock** – The system clock is used to time stamp all events recorded in the system log and user audit log. To set the system clock, see *"System Clock" on page 10-7*.

- **User Administration** – The Broadmore authenticates users by identification and role-based access privilege levels and maintains an audit trail activity log. Only a SuperUser can assign users and access levels, set the minimum number of characters required for user names and passwords (user ID rules), and clear the system log. The security officer must ensure that all users change their passwords periodically in accordance with local security practice.
  (1) It is recommended that passwords be changed at least once every 6 months. Users must be instructed to use a random combination of all the usable characters for passwords.
  (2) It is recommended that all users, access privileges, and role assignments be reviewed periodically or whenever a personnel termination, transfer, or role change occurs.

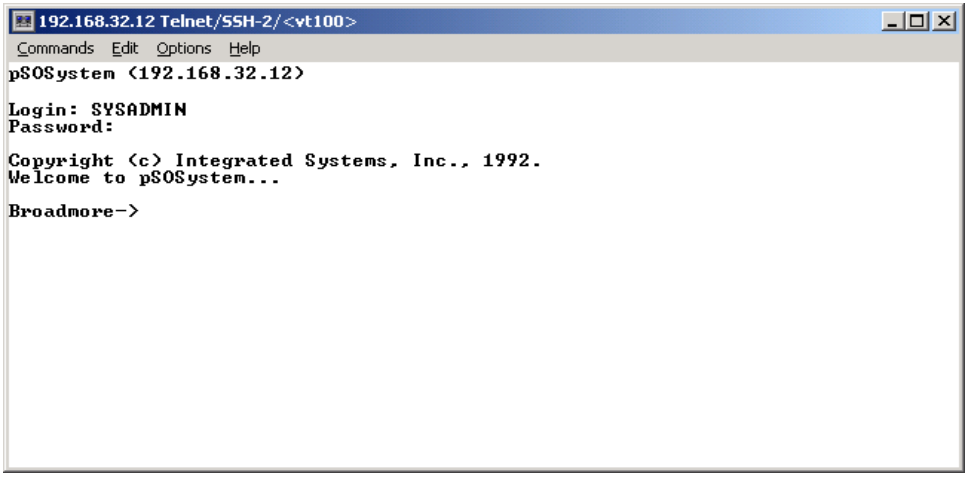
- **Audit Trails** – Audit trails must be enabled for FIPS mode.
  The cryptographic module provides a system log and user audit log. The audit log (audit.txt) records user actions while the system log (sys.log) records system events and configuration changes.
  A SuperUser has access to pSOS shell commands that can overwrite the system and audit log files. This misuse of shell commands to corrupt the audit trail is strictly prohibited and removes the Broadmore from the evaluated configuration. It is recommended that user audit trails be examined periodically in accordance with local security practice to determine if the Broadmore is being accessed by unauthorized users or during nonstandard hours, or if the configuration is being accessed or altered in an inappropriate manner. For example, every third consecutive attempted login failure produces an entry in the system log.

Logging In

The following example uses the Windows telnet client software.

To log into the Broadmore:

1. Open a telnet window.
2. Type in the Hostname and Username. The Hostname is the IP address of the Broadmore, and the Username is **admin**.
3. Select Keyboard Interactive from the Primary pull-down menu in the Authentication panel.
4. Click Connect.



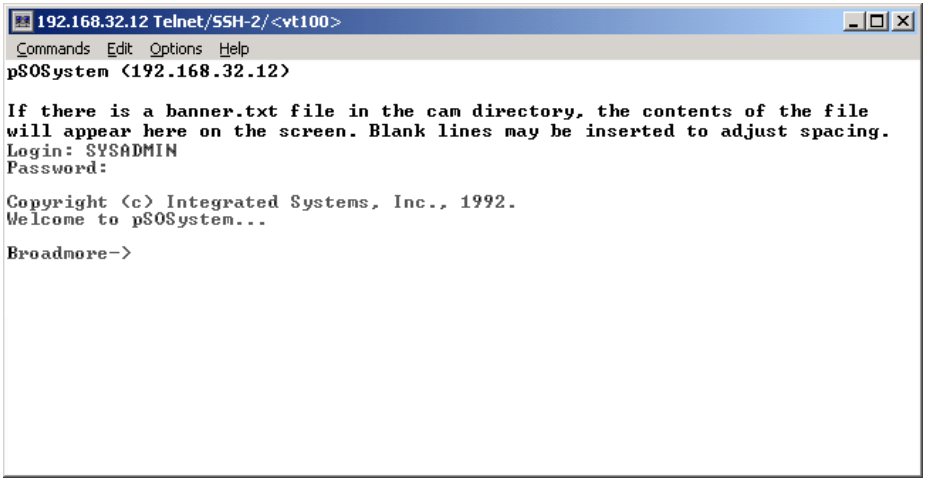
NOTE: For initial system installation, the factory default user name is **SYSADMIN** and the password is **INITIAL**. To ensure network security, a network administrator (SuperUser) must create new user names and passwords. See *"Managing Users and Audit Trails" on page 10-10*.

5. When Broadmore user login message displays, type the Login and Password. You will need to press Enter after each.

   After successfully logging into the Broadmore user's list, the Broadmore command prompt displays.

Log-in Banner

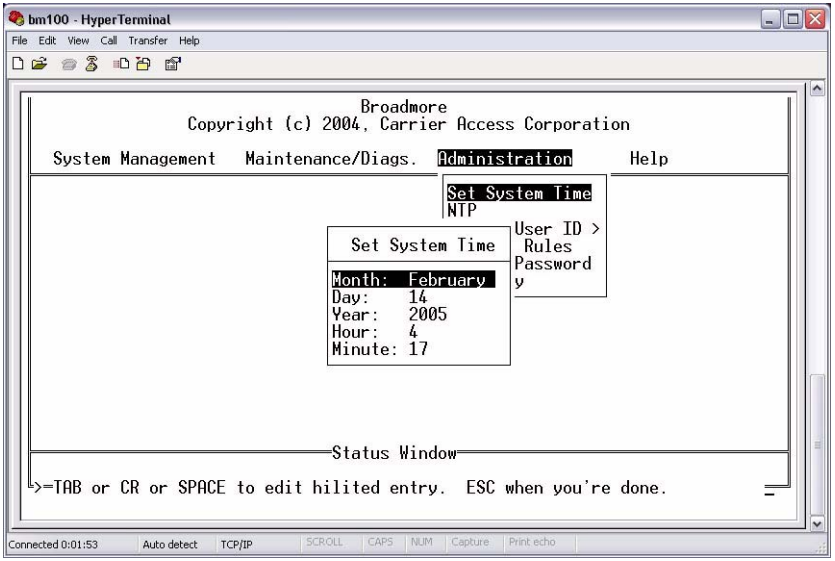
The Broadmore provides the ability to insert a customizable banner that will appear when a user logs in. The banner is a simple way to provide special instructions to the user. A SuperUser can implement this feature by using ftp or SFTP to download a banner text file, named **banner.txt**, to the Broadmore **cam** directory. When a shell login is requested, the contents of the banner file (if any) will be dumped to the screen just ahead of the login prompt, as in the following example.


192.168.32.12 Telnet/SSH-2/<vt100>
Commands Edit Options Help
pSOSystem (192.168.32.12)
If there is a banner.txt file in the cam directory, the contents of the file
will appear here on the screen. Blank lines may be inserted to adjust spacing.
Login: SYSADMIN
Password:
Copyright (c) Integrated Systems, Inc., 1992.
Welcome to pSOSystem...
Broadmore->>

System Clock

The Broadmore CPU system clock provides the time and date stamp used for system logs, events, and audit trails. A SuperUser must set the system clock either manually after powering up the Broadmore or configure the Broadmore to use a network timing source (see *"Network Time Protocol" on page 10-8*).

Select Set System Time from the Administration menu. Then set the Month, Day, Year, Hour, and Minute to the correct values. When finished, press Escape and select Yes to change the system clock.



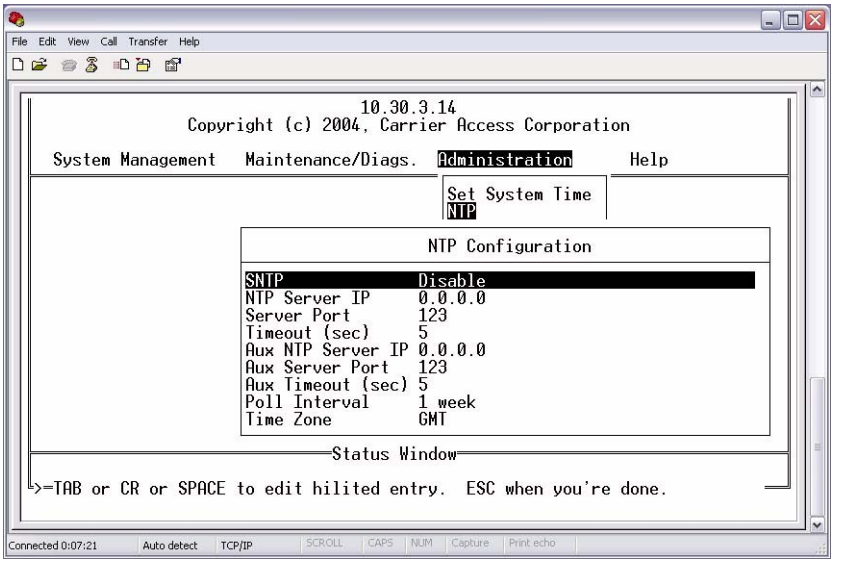
NOTE: Changing the system clock is an event recorded in the system log (see *"System Log" on page 10-15*).

Network Time Protocol

The Broadmore CPU system clock provides the time and date stamp used for system logs, events, and audit trails. A SuperUser must set the system clock either manually after powering up the Broadmore (see *"System Clock" on page 10-7*) or configure the Broadmore to use a network time source as described below. The Broadmore uses Simple Network Time Protocol (SNTP), which is an Internet standard for periodically synchronizing the system clocks connected to an IP network.

If the clock uses the NTP source, the system clock will be automatically synchronized to the NTP source when power is reapplied to the Broadmore.

Select NTP from the Administration menu. Then set the following parameters to the desired values. When finished, press Escape and select Yes to accept the changes.


File Edit View Call Transfer Help
10.30.3.14
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help
Set System Time
NTP
NTP Configuration
SNTP Disable
NTP Server IP 0.0.0.0
Server Port 123
Timeout (sec) 5
Aux NTP Server IP 0.0.0.0
Aux Server Port 123
Aux Timeout (sec) 5
Poll Interval 1 week
Time Zone GMT
Status Window
>=TAB or CR or SPACE to edit hilited entry. ESC when you're done.
Connected 0:07:21 Auto detect TCP/IP SCROLL CAPS NUM Capture Print echo

Item	Options	Comments
SNTP	Enable, Disable	When enabled, the Broadmore system clock will be synchronized to the network time source.
NTP Server IP		The IP address of the primary network time source.
Server Port	0 to 32767	
Timeout (sec)	1 to 100	The time to wait for a response from the primary network time source.
Aux NTP Server IP		The IP address of the auxiliary network time source, to be used if a request to the primary network time source exceeds the timeout period.
Aux Server Port	0 to 32767	
Aux Timeout (sec)	1 to 100	The time to wait for a response from the auxiliary network time source.
Poll Interval	1 hr, 8 hr, 1 day, 1 week	Determines how often the Broadmore will request an update from the NTP source. The default is 1 week.
Time Zone	GMT or specific zone	The default is Greenwich Mean Time (GMT) but you can choose among 24 international time zones.

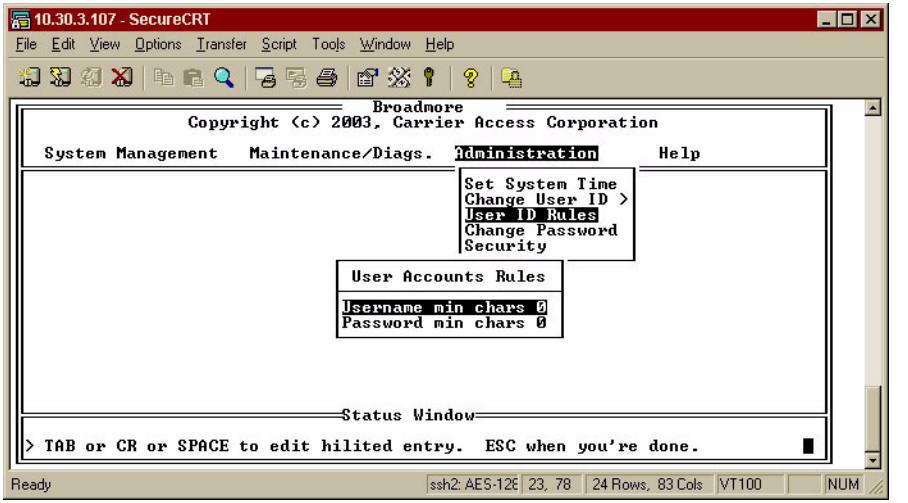
Managing Users and Audit Trails

- User ID Rules ... *10-10*
- Change User ID ... *10-11*
- User Audit Trails ... *10-13*

User ID Rules

A SuperUser can set the minimum allowable number of characters in user names and passwords by selecting User ID Rules from the Administration menu.

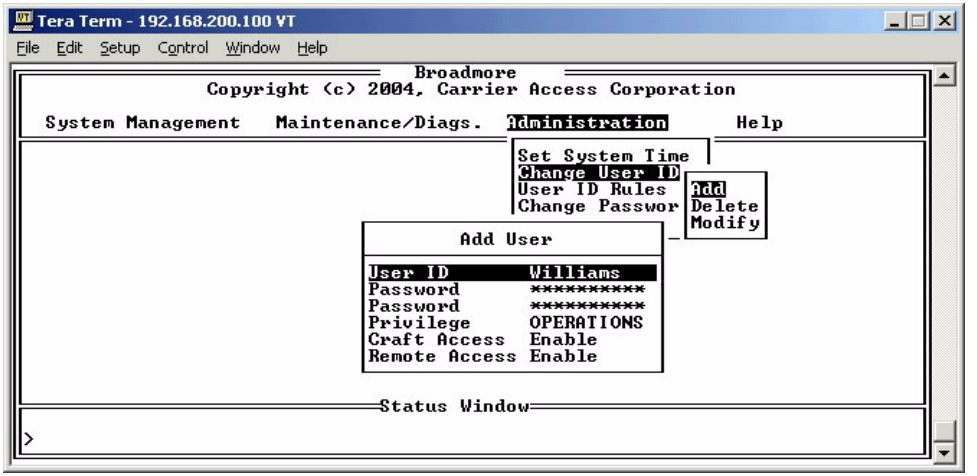
From this menu, select the Username or Password and enter the required minimum number of characters.



Change User ID

The Change User ID menu allows a SuperUser to add, delete, and modify user IDs. (Any user can change their own password using the Change Password menu.)

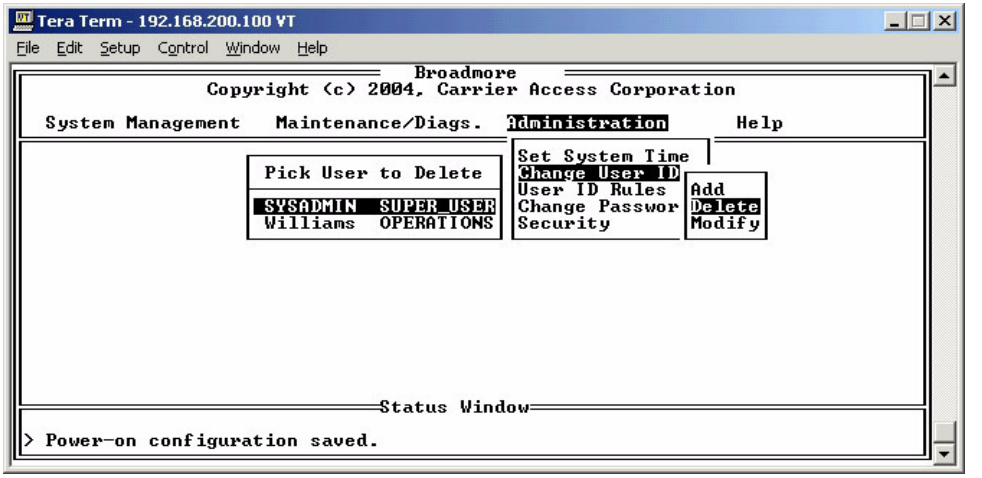
Adding a User



Enter the following information for each user that is added.

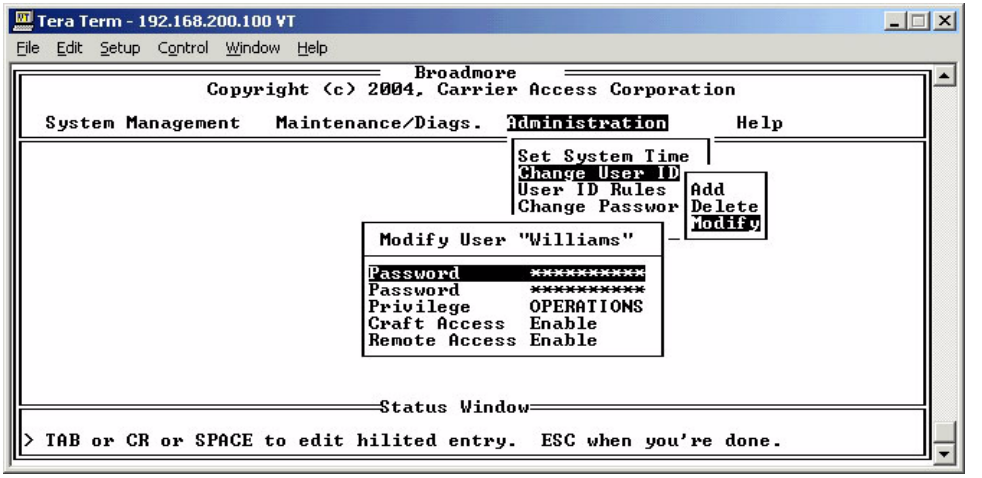
Item	Options	Comments
User ID		A unique user identifier
Password/ Password		A unique password for the user and a second password field to confirm
Privilege	BROWSER OPERATIONS SYS_ADMIN SUPER_USER	The level of user access. See *"User Security Configuration" on page 7-26.*
Craft Access	Enable, Disable	Access through the serial port on the front of the CPU.
Remote Access	Enable, Disable	Access through telnet, secure shell login, ftp or secure ftp.

Deleting a User



After you select the user to be deleted, a confirmation message appears. Select Yes to delete the user, or No to exit without making any changes.

Modifying a User



After you select the user to be modified, enter the appropriate information in the

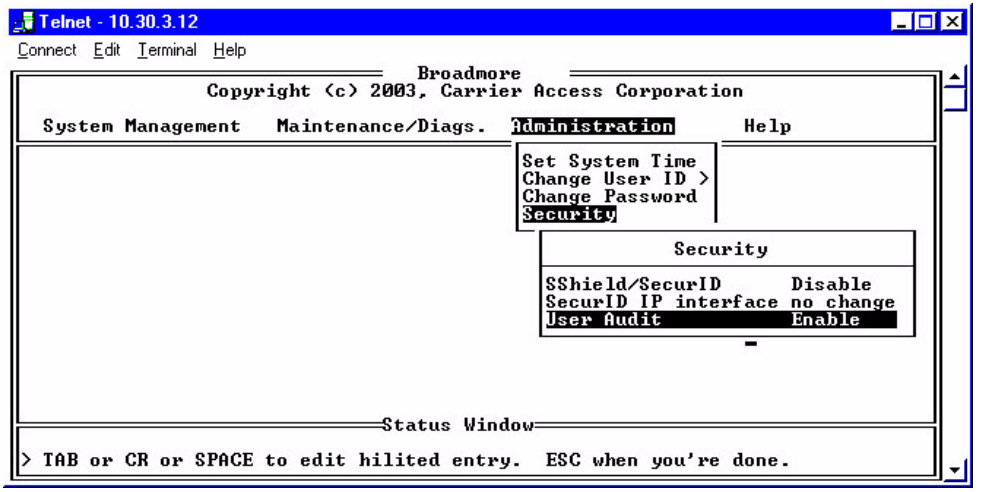
Modify User fields. See *"Adding a User" on page 10-11* for more information on the individual fields.

User Audit Trails

NOTE: It is recommended that audit trails remain disabled until deployment of the Broadmore with the anticipated "power up" configuration is complete. This prevents the audit logging of numerous initial installation configuration changes. Only a SuperUser can enable or disable this function or use shell commands to access the audit file.

Only a SuperUser can enable or disable this function or use shell commands to access the audit files.

Audit Trails protect a Broadmore by providing traceability of who performed procedures on the unit, what procedures were performed, and when they took place. The Broadmore local audit trail logs changes to configuration parameters and user logins.



NOTE: The audit file is located in the **cam** directory. The craft port will allow **cat**, **head**, or **tail** commands in the non-FIPS mode.

NOTE: Audit files can be up to 200k bytes, with the most recent data being located at the end of the file. Use the **tail** command to display the end of the file. For detailed examination, it is best to upload the file via FTP.

To display the audit file, at the Broadmore > prompt, enter the following commands:

cd cam ↵

tail audit.txt ↵

An example of the output is provided below.

```
Copyright (c) Integrated Systems, Inc., 1992.
Welcome to pSOSystem...

Broadmore->cd cam
Broadmore->tail audit.txt
08/08/2002 10:36:59 Q SYSADMIN:Security:User Audit=Enable
08/08/2002 10:37:53 Q SYSADMIN:Reboot System
08/09/2002 08:42:10 Q SYSADMIN:Create VP Reservation:"Test", slot G, vp 0, vcSt2
08/09/2002 08:45:22 Q SYSADMIN:Create VP Reservation:"Test2", slot G, vp 1, vcS2
08/09/2002 09:07:25 Q SYSADMIN:General Properties:Bandwidth Meter=Enable
Broadmore->
```

User audit files record the following information for each user action:

- date
- time
- online CPU (Q or R) or standby CPU (q or r)
- user name
- event type
- short description of the event

Once the **audit.txt** file is full, the file is automatically closed, the name is changed to **audit_o.txt**, in case a SuperUser wants to access the old file via FTP. A new file is then opened named **audit.txt** and new data is written to that file.

Deleting Audit Trails

A user can delete the contents of the system log by using the CAMMI (Maintenance/Diags, View System Log, Delete command) or the corresponding CLI command. However, this only deletes the events that can be viewed by their access level.

Archiving Audit Trails

A SuperUser can archive the **audit.txt** and **audit_o.txt** files using an FTP client to copy the files to another computer or storage device. After logging in with FTP, navigate to the **cam** directory and locate the audit.txt and audit_o.txt files.

System Log

The system log file **sys.log** is a circular file that contains a recent history of system users, events, and alarms. Old records are overwritten by new records. The log file identifies the currently active CPU and any user currently logged into the Broadmore. All users can use the CAMMI interface to view those system events permitted by their access role (see *"Maintenance/Diagnostics" on page 8-46*). Only a SuperUser can copy or delete the sys.log file. After logging in using an FTP client, navigate to the **cam** directory and locate the sys.log file. The system log can be deleted and archived in much the same way as the audit log files.

For example, the SuperUser can delete the **sys.log** file by using the **del** shell command, as in the following example.

cd cam ↵

del sys.log ↵

Rather than using FTP, a SuperUser can also display or delete the system log through the Command Line Interface.

To display the system log, log into the Broadmore and enter the following commands at the Broadmore prompt:

cli ↵

maintain ↵

systemlog ↵

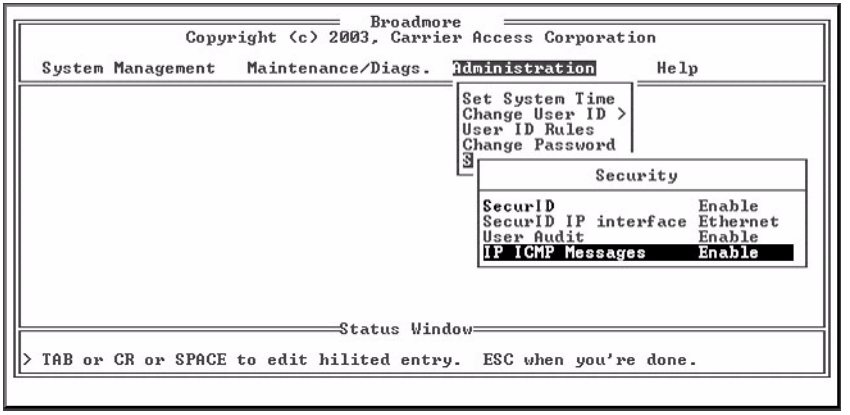
showi ↵

You can then navigate through the system log by following the instructions appearing at the bottom of the window.

To delete the system log, enter **clearlog** instead of **showi**.

IP ICMP Messages

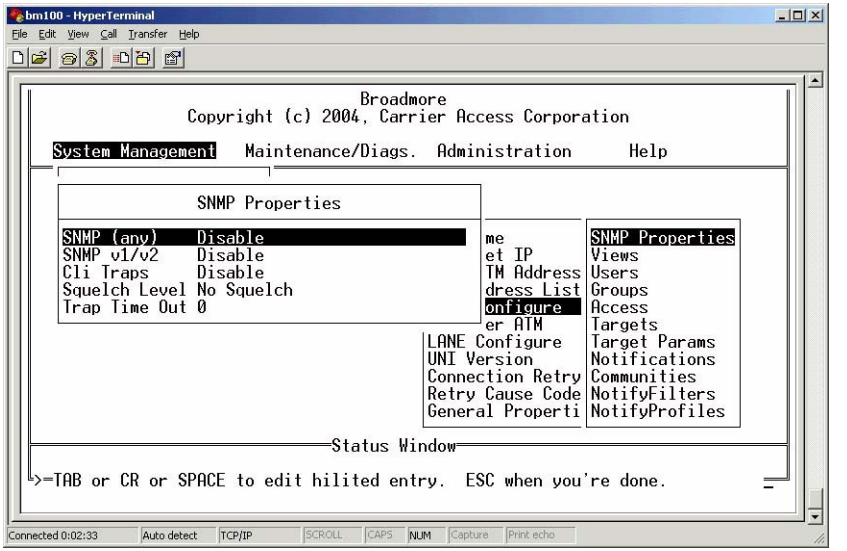
You can use the Internet Control Message Protocol selection to enable or disable all ICMP messages for Internet Protocols such as ping or echo. Disabling ICMP is a common defense against denial-of-service attacks using ping floods.



SNMP Messages

You can use the SNMP Properties selection to enable or disable SNMP messages. The Broadmore supports older SNMP v1 and v2, as well as newer SNMPv3 protocols. You have the option to disable only SNMPv1 and v2 messages, or all SNMP messages.

Only a SuperUser or SysAdmin can access or change these properties (see *"SNMP Properties" on page 12-3*).



Select **System Management** ↵
Select **Configure** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **SNMP Properties** ↵

Shell Commands (Non-FIPS Mode)

Shell commands are UNIX-like commands provided by the embedded pSOS operating system. Command syntax is available using the “help” command. Authorized Access to each command is based on the user privilege level.

FIPS Mode

The Broadmore is shipped with FIPS mode security turned off. A SuperUser can use the **fipsmode** shell command to enable FIPS mode operation (see *“Security Management (FIPS Mode)” on page 11-1*).

Authorized Access to Shell Commands

The following table lists the authorized commands available to each access privilege level when operating in non-FIPS mode.

User ID → **↓ Authorized Services**	**Super_User**	**Sys_Admin**	**Operations**	**Browser**
arp	•	•		
cammi (start GUI interface)	•	•	•	•
cat	•	•	•	
cd	•	•	•	
cli	•	•	•	•
cmp	•	•	•	
comp	•	•	•	
copy	•	•	•	
cp	•	•	•	
del	•	•	•	
dir	•	•	•	
du	•	•		
echo	•	•	•	•
fipsmode	•			

User ID → ↓ Authorized Services	Super_User	Sys_Admin	Operations	Browser
head	●	●	●	
help	●	●	●	●
ifconf	●	●		
ls	●	●	●	
md	●	●		
mem	●			
mkdir	●	●		
move	●	●	●	
mv	●	●	●	
netstat	●	●		
ping	●	●	●	●
pwd	●	●	●	●
rd	●	●	●	
resetSecurID	●	●		
resetSecurIDIp	●	●		
rm	●	●	●	
rmdir	●	●	●	
route	●	●		
savert	●	●		
scp	●	●		
selftest	●			
setbaud	●	●	●	
setenv	●	●		
settimeout	●	●		
setwrite	●	●	●	
sigmem	●			
snmpinit	●	●		
sshdSessionShow	●	●		
sshdShow	●	●		

User ID → ↓ Authorized Services	Super_User	Sys_Admin	Operations	Browser
tail	•	•	•	
touch	•	•		
zeroize	•			

FTP Login

Users can login using ftp to access Broadmore administrative functions over IP.

To log in to the Broadmore:

1. Open your ftp program. The Connect window opens.
2. Enter the IP address of the Broadmore.
3. Enter the Username: **SYSADMIN** (the username is case sensitive).
4. Enter the Password: (example) **jsmith** – the default password for new installations is **INITIAL** (passwords are case sensitive).

CHAPTER 11

Security Management (FIPS Mode)

In this Chapter:

- Security Features ... *11-2*
- Security Guidance ... *11-3*
- Authentication and Identification ... *11-6*
- Authorized Services ... *11-7*
- Key Management ... *11-8*
- Logging In ... *11-9*
- Log-in Banner ... *11-13*
- System Clock ... *11-14*
- Network Time Protocol ... *11-15*
- Changing Security Modes ... *11-17*
- User Administration and Audit Trails ... *11-26*
- Shell Commands (FIPS Mode) ... *11-34*
- SFTP Login ... *11-43*
- SecurID Features ... *11-49*
- Residual Data and Memory Volatility ... *11-50*

Security Features

This release of the Broadmore includes the Broadmore/SSHield Management Module, which is a FIPS 140-2 validated software-only module that meets the security requirements of Federal Information Processing Standard PUB 140-2. The Broadmore/SSHield Management Module enables the secure operation and control of the Broadmore's ATM configuration parameters via a command line interface (CLI) or menu based interface (CAMMI). TeamF1's SSHield provides security by means of the SSH (IETF SECSH) protocol to ensure that network connections are secure.

A detailed description of the Broadmore security features are provided in the "Broadmore/SSHield Management Module Version 4.0 Security Policy" available at the following web sites:

- *http://www.carrieraccess.com/support/* under the Broadmore documents
- *http://csrc.nist.gov/cryptval/* under the Validation Lists

When the FIPS Security option is enabled on the Broadmore, the following security features are available:

- RSA SecurID® authentication (optional, see *"SecurID Features" on page 11-49*)
- Private management data paths using SSHield for CLI/CAMMI sessions and Secure File Transfer Protocol (SFTP)
- Configuration activity audit trails
- Zeroize command for decommissioning one or both CPUs

Enabling FIPS mode security disables FTP and Telnet access. Users must log in using secure client replacements such as SecureCRT® and SecureFX®. A secure terminal emulator is required to enter a secure Broadmore system. Although many secure terminal emulators are available, SecureCRT is recommended.

NOTE: Be sure to use the appropriate fonts and screen settings to maintain the proper screen appearance.

Additional security-relevant features include:

- Enable/disable SNMP and ICMP messages
- SNMPv3 USM/VACM
- Log-in Banner for special user instructions

Security Guidance

- **Receipt and Inspection** – Broadmore components containing FIPS 140-2 validated software are packaged and sealed at the factory with tamper-proof security tape. Upon receipt, carefully examine the security sealing tapes on the shipping containers for any signs of tampering. (See *"Receipt" on page 3-2*.)
- **Security** – Broadmore components containing FIPS 140-2 validated software (CPU modules, memory modules, and storage media) should be handled in accordance with applicable security procedures.
- **Initial Login** – The Broadmore is shipped with a default username and password for logging in the first time. A SuperUser (Crypto Officer) should log in the first time to configure the Broadmore for secure operation.
  For maximum security, perform the following steps:
  (1) configure IP access (via ethernet, LANE, or CIP)
  (2) install security keys
  (3) create a temporary SuperUser account
  (4) delete the public SYSADMIN account
  (5) enable FIPS mode and reboot the system
  (6) after logging in securely, you can safely create user accounts and configure the Broadmore for secure operation.
- **Security Modes** – The Broadmore is shipped with security turned off. Only a SuperUser can change the FIPS and SecurID modes (see *"Changing Security Modes" on page 11-17*).

- **Potential Security Vulnerabilities**
  (1) Disabling fipsmode deletes existing user access accounts and cryptographic keys and reverts the Broadmore to the factory default SuperUser ID and password, which can deny management access and compromise security. No one can log in till the Broadmore is rebooted. It is recommended that the fipsmode be changed only during initial setup and decommissioning.
  (2) The Broadmore accepts loose source routed IP packets, so it is recommended that source routed packets be dropped on routers and firewalls. (See manufacturer's instructions.)
  (3) The Broadmore RS-232 COM 1 serial port used for "Craft Access" does not immediately terminate a management session if a user disconnects without typing "exit". During the following timeout period, another user can connect without logging into the RS-232 port and other users are denied access through the ethernet port. It is recommended that all accounts be created with "Remote Access" only, except for one failsafe SuperUser account with "Craft Access." The craft password should be stored safely in the NOC. When needed, the SuperUser can log into the craft port, fix things, change the password, log out, and store the new password back in the NOC.

- **Initialization and Verification** – When the Broadmore is powered up in the FIPS mode, the FIPS 140-2 validated software will perform a self-test to verify software integrity and cryptographic functions. To verify that the Broadmore is operating in FIPS mode, see *"Help About Security" on page 11-17*.

- **Key Management** – A DSA private hosts key is required for SSH2 connection to the Broadmore. A default key is provided for use in initializing the Broadmore after installation at the customer site. The SuperUser should change this key before making the Broadmore operational and change it periodically in accordance with local security practice.

- **System Clock** – The system clock is used to time stamp all events recorded in the system log and user audit log. To set the system clock, see *"System Clock" on page 11-14*.

- **User Administration** – The Broadmore authenticates users by identification and role-based access privilege levels and maintains an audit trail activity log. Only a SuperUser can assign users and access levels, set the minimum number of characters required for user names and passwords (user ID rules), and clear the system log. The security officer must ensure that all users change their passwords periodically in accordance with local security practice.
  (1) It is recommended that passwords be changed at least once every 6 months. Users must be instructed to use a random combination of all the usable characters for passwords.
  (2) It is recommended that all users, access privileges, and role assignments be reviewed periodically or whenever a personnel termination, transfer, or role change occurs.

- **Audit Trails** – Audit trails must be enabled for FIPS mode.
  The cryptographic module provides a system log and user audit log. The audit log (audit.txt) records user actions while the system log (sys.log) records system events and configuration changes.
  A SuperUser has access to pSOS shell commands that can overwrite the system and audit log files. This misuse of shell commands to corrupt the audit trail is strictly prohibited and removes the Broadmore from the evaluated configuration. It is recommended that user audit trails be examined periodically in accordance with local security practice to determine if the Broadmore is being accessed by unauthorized users or during nonstandard hours, or if the configuration is being accessed or altered in an inappropriate manner. For example, every third consecutive attempted login failure produces an entry in the system log.

- **Decomissioning and Sanitizing** – The **zeroize** command is not intended for normal operational use. It is intended as a security measure (per FIPS 140-2 requirements) to allow a SuperUser to completely remove all security-sensitive data that may be required before decommissioning a CPU. Turning off FIPS mode will erase Critical Security Parameters (CSPs) but does not erase the FIPS validated operating software. For additional information on sanitizing the equipment, see *"Residual Data and Memory Volatility" on page 11-50*.

Authentication and Identification

The cryptographic module supports distinct operator roles and enforces the separation of these roles using identity-based operator authentication that requires a Username and Password, and optional SecurID.

The SecurID option has no effect on FIPS 140-2 compliance. When SecurID is enabled, operators must also enter a SecurID token before they can gain access to the Broadmore. The SecurID token is a number that may be constant or change every minute, and it is verified by an RSA Authentication Manager deployed at the customer site.

A username and password are always required to log in, whether or not SecurID is enabled. The mandatory username is an alphanumeric string of characters whose minimum length can be set by the Security Officer. The password is a string of characters from the 94 printable and human-readable characters whose length can be set by the Crypto Officer.

Passwords be changed at least once every 6 months and that users be instructed to use a random combination of all the usable characters for passwords.

Upon successful authentication, the role and privilege level are selected based on the identity (username) of the operator. At the end of a session, the operator should log off, though the user is automatically logged off after a configurable period of inactivity.

Role	Privilege Level	Authorized Functions
User	Browser	User is able to look at most all data plane information but is not able to affect anything. To protect security data, no file access is permitted. This role cannot access the security settings.
	Operations	User is able to perform data plane configurations, such as defining PVCs, SVCs, configuring service card parameters. To protect security data, no file access is permitted under this privilege level. This role cannot access the security settings.
	SysAdmin	User is able to perform global configuration operations such as redundancy. To protect security data, no file access is permitted. This role cannot access the security settings.
Crypto Officer	SuperUser	This role is required to manage system accounts, use SFTP, and alter security settings. Only users at this privilege level may turn FIPS mode on or off.

Authorized Services

The following table lists the authorized services available to each privilege level.

User ID → ↓ Authorized Services	Super_User	Sys_Admin	Operations	Browser
SecureCRT (SSH2 terminal client)	•	•	•	•
SecureFX (SSH2 SFTP client)	•			
Change User ID	•			
Change own password	•	•	•	•
System Services	•	•		
Connection Retry	•	•	•	
Establish connections	•	•	•	
Delete connections	•	•	•	
Configure modules	•	•	•	
Maintenance/Diagnostics	•	•		
View System Log	•	•	•	•
Environmental Indicators	•	•	•	•
Boot/Reboot system or card	•	•		
Card diagnostics	•			
System test	•			
Check free CPU memory	•			
View configuration statistics	•	•	•	•
Security management including user accounts, audit trail, and zeroizing	•			
Change Files using shell commands	•			

NOTE: For a complete list, see *"Authorized Access to Shell Commands" on page 11-41*.

Key Management

A DSA private hosts key is required for SSH2 connection to the Broadmore.

Default DSA Key

During manufacture, a default **host_dsa** key file is placed in the **/SSHD** directory of the Broadmore CPU. This default key is intended only for use in initializing the Broadmore after installation at the customer site and should be changed by the SuperUser (Crypto Officer) before making the Broadmore operational.

NOTE: The DSA hosts key can only be replaced by the SuperUser while the Broadmore is in the FIPS mode.

Generating DSA Key Pairs

DSA keys can be generated on a UNIX or Windows host, using key generation utilities provided as a part of the ssh clients/server software of various vendors.

OpenSSH provides ssh-keygen to generate DSA keys on a UNIX or Windows host. The ssh-keygen program can be downloaded from the URL http://www.openssh.org.

The following example shows how to generate the **host_dsa** key on a UNIX host or on a Windows PC running Cygwin.

```
$ ssh-keygen -t dsa -f host_dsa -N "" -C <comments>
```

Installing the DSA Key

With the Broadmore in FIPS mode, the SuperUser can use an SSH2 client (such as SecureFX) to log into the Broadmore/SSHield module and install the **host_dsa** key in the **/SSHD** directory on the Broadmore CPU.

NOTE: After installing the DSA key, the Broadmore must be rebooted in order for the change to take effect.

Logging In

NOTE: If FIPS mode is currently turned off, you must follow the non-FIPS mode instructions for *"Logging In" on page 10-5*.

Broadmore units are shipped from the factory with SecurID turned off and FIPS mode turned off. After logging into the Broadmore, the Crypto Officer can configure the Broadmore to use SecurID, if desired.

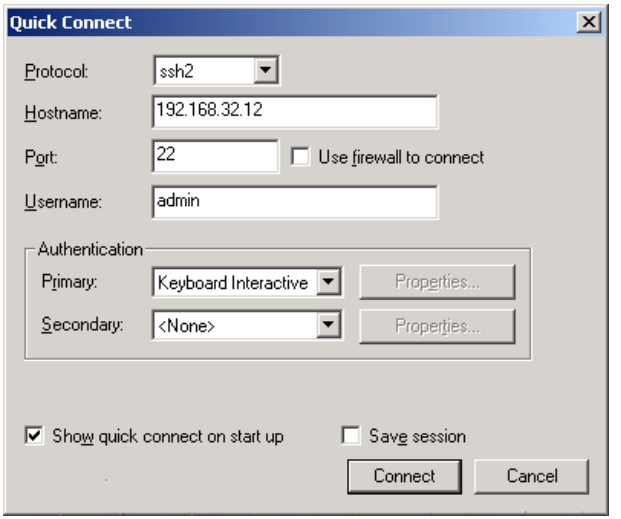
Using both SecurID and FIPS mode with the Broadmore provides a two-stage login. First, users log in using RSA SecurID. Then they can log into the Broadmore/SSHield Management Module.

Logging in with SecurID Disabled

The following example uses SecureCRT as the secure client software.

To log into the Broadmore:

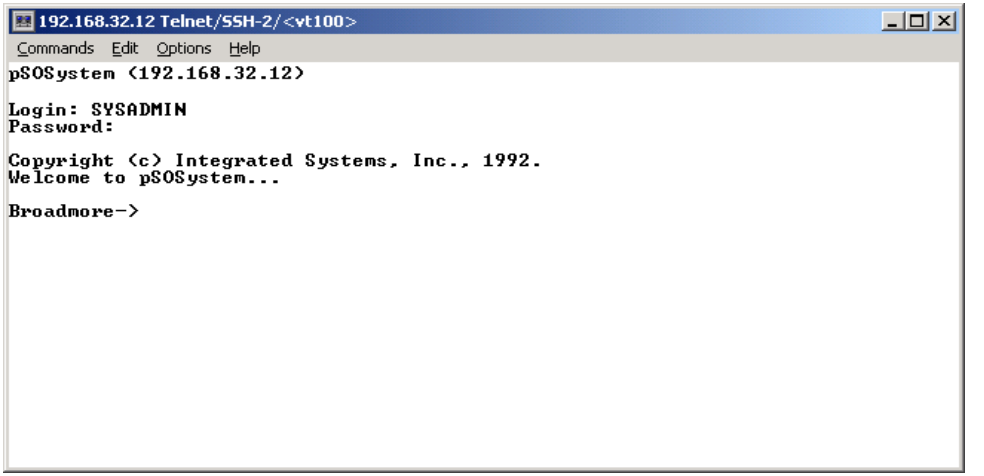
1. Open SecureCRT. The Quick Connect window opens.



2. Select ssh2 from the Protocol pull-down menu.

3. Type in the Hostname and Username. The Hostname is the IP address of the Broadmore, and the Username is the Broadmore user name.
4. Click Connect.
5. When the Broadmore Login message appears, type in the Login and Password. You will need to press Enter after each. (The factory defaults for the initial installation are **SYSADMIN** and **INITIAL**.)

   After successfully logging in, the Broadmore command prompt displays.

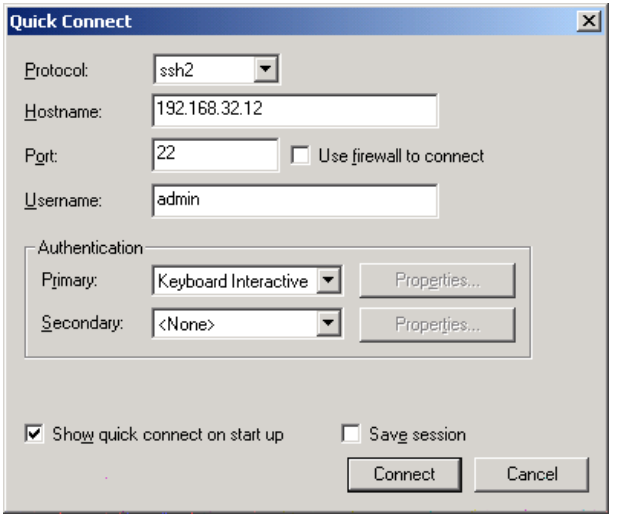


Logging in with SecurID Enabled

The following example uses SecureCRT as the secure client software.

To log into the RSA SecurID server:

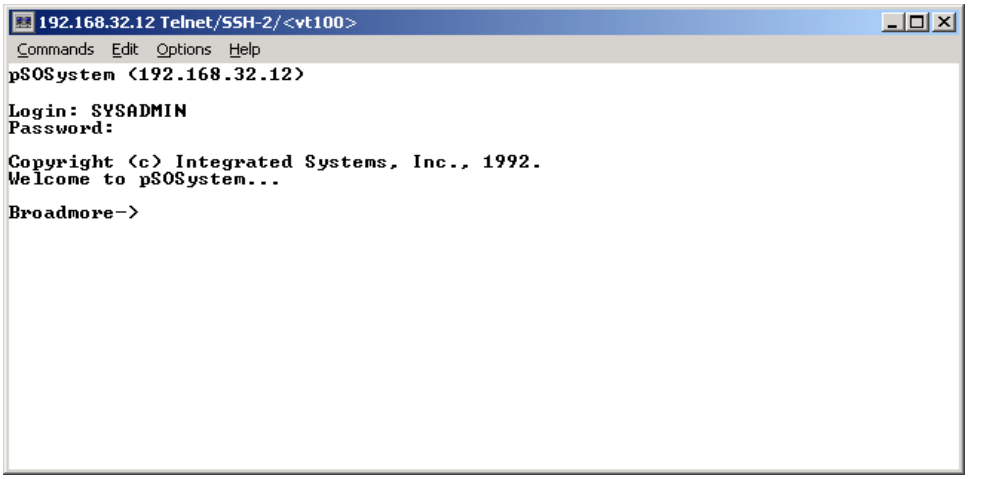
1. Open SecureCRT. The Quick Connect window opens.



2. Select ssh2 from the Protocol pull-down menu.
3. Type in the Hostname and Username. The Hostname is the IP address of the Broadmore, and the Username is the SecurID user name that is configured on the RSA SecurID server with a token assigned to it.
4. Select Keyboard Interactive from the Primary pull-down menu in the Authentication panel.
5. Click Connect.

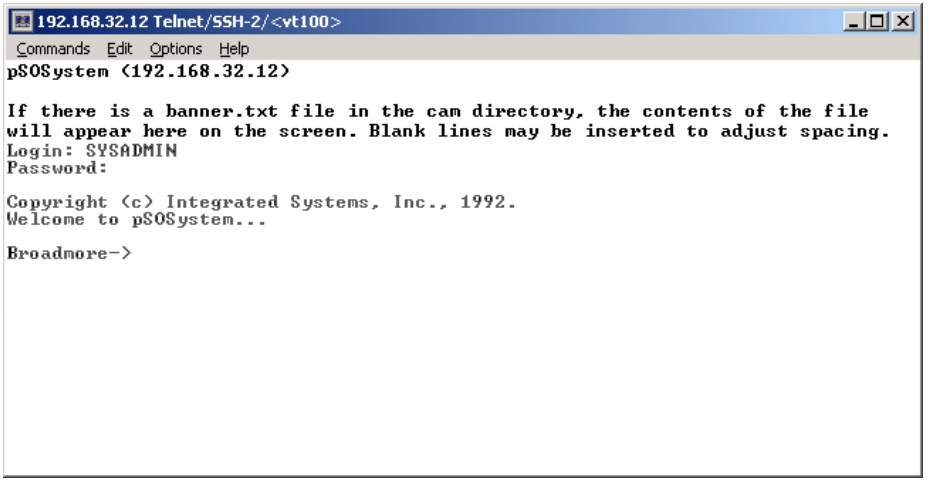
The Keyboard Interactive Authentication opens.

6. Type in the SecurID passcode, and click OK.
7. After successfully logging into SecurID, the Broadmore login displays.
8. Type the Login and Password. You will need to press Enter after each. (The factory defaults for the initial installation are **SYSADMIN** and **INITIAL**.) After successful login, the Broadmore command prompt appears.



Log-in Banner

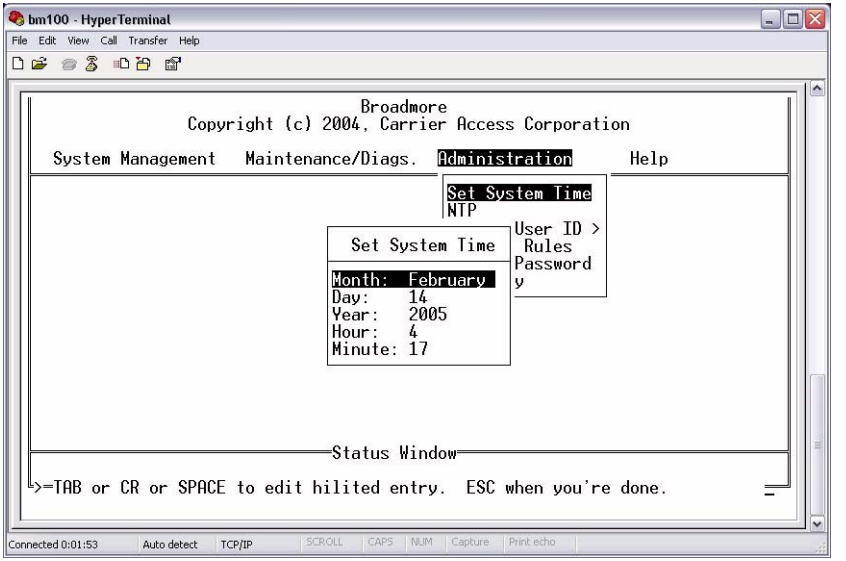
The Broadmore provides the ability to insert a customizable banner that will appear when a user logs in. The banner is a simple way to provide special instructions to the user. A SuperUser can implement this feature by using ftp or SFTP to download a banner text file, named **banner.txt**, to the Broadmore **cam** directory. There is no limit to the size of this file. When a shell login is requested, the contents of the banner file (if any) will be dumped to the screen just ahead of the login prompt, as in the following example.


192.168.32.12 Telnet/SSH-2/<vt100>
Commands Edit Options Help
pSOSystem (192.168.32.12)
If there is a banner.txt file in the cam directory, the contents of the file
will appear here on the screen. Blank lines may be inserted to adjust spacing.
Login: SYSADMIN
Password:
Copyright (c) Integrated Systems, Inc., 1992.
Welcome to pSOSystem...
Broadmore->>

System Clock

The Broadmore CPU system clock provides the time and date stamp used for system logs, events, and audit trails. A SuperUser must set the system clock either manually after powering up the Broadmore or configure the Broadmore to use a network timing source (see *"Network Time Protocol" on page 11-15*).

Select Set System Time from the Administration menu. Then set the Month, Day, Year, Hour, and Minute to the correct values. When finished, press Escape and select Yes to change the system clock.



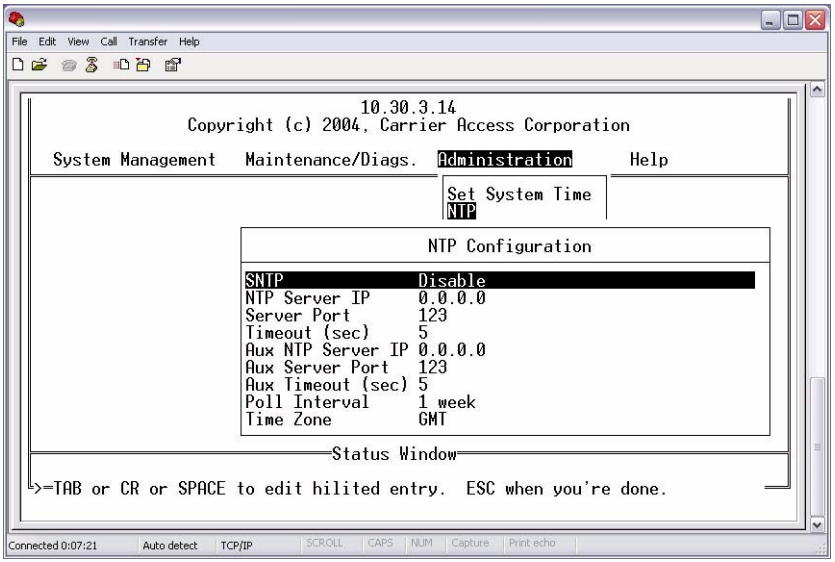
NOTE: Changing the system clock is an event recorded in the system log (see *"System Log" on page 11-32*).

Network Time Protocol

The Broadmore CPU system clock provides the time and date stamp used for system logs, events, and audit trails. A SuperUser must set the system clock either manually after powering up the Broadmore (see *"System Clock" on page 11-14*) or configure the Broadmore to use a network time source as described below. The Broadmore uses Simple Network Time Protocol (SNTP), which is an Internet standard for periodically synchronizing the system clocks connected to an IP network.

If the clock uses the NTP source, the system clock will be automatically synchronized to the NTP source when power is reapplied to the Broadmore.

Select NTP from the Administration menu. Then set the following parameters to the desired values. When finished, press Escape and select Yes to accept the changes.



Item	Options	Comments
SNTP	Enable, Disable	When enabled, the Broadmore system clock will be synchronized to the network time source.
NTP Server IP		The IP address of the primary network time source.
Server Port	0 to 32767	
Timeout (sec)	1 to 100	The time to wait for a response from the primary network time source.
Aux NTP Server IP		The IP address of the auxiliary network time source, to be used if a request to the primary network time source exceeds the timeout period.
Aux Server Port	0 to 32767	
Aux Timeout (sec)	1 to 100	The time to wait for a response from the auxiliary network time source.
Poll Interval	1 hr, 8 hr, 1 day, 1 week	Determines how often the Broadmore will request an update from the NTP source. The default is 1 week.
Time Zone	GMT or specific zone	The default is Greenwich Mean Time (GMT) but you can choose among 24 international time zones.

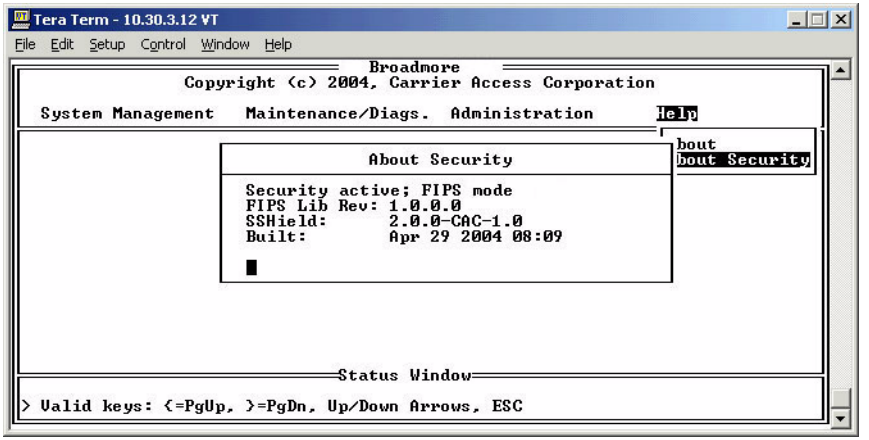
Changing Security Modes

- Help About Security ... *11-17*
- Enabling FIPS Mode ... *11-18*
- Disabling FIPS Mode ... *11-20*
- Enabling SecurID ... *11-21*
- Disabling SecurID ... *11-24*
- IP ICMP Messages ... *11-24*
- SNMP Messages ... *11-25*

Only a Crypto Officer (SuperUser) can change the security modes. The Broadmore is shipped from the factory with FIPS mode and SecurID turned off. The security modes can only be changed after successfully logging into the Broadmore while operating in the current mode configuration (see *"Logging In" on page 11-9*).

Help About Security

Any user, regardless of security level, can use this command. Selecting Help About Security from the main menu will display the current FIPS mode setting and the version numbers of the security software included in the Broadmore.



Item	Comments
FIPS Mode Active	Broadmore is in FIPS 140-2 validated operating mode
Security inactive; non-FIPS mode	Broadmore is not in FIPS approved operating mode
FIPS Lib Rev	Version of FIPS Library.
SSHield	Version of SSHield software
Built	Build date of SSHield software

Enabling FIPS Mode

Only a Superuser (Crypto Officer) can change the security modes. The Broadmore is shipped from the factory with FIPS mode turned off. The security mode can only be changed after successfully logging into the Broadmore for the first time, by performing the following steps.

1. Log into the online CPU (Broadmore primary IP address) with a conventional terminal emulator such as Telnet (see *"Logging In" on page 10-5*).
2. Enable FIPS mode by entering the following command at the Broadmore prompt:
   fipsmode on ↵
3. Set the session timeout for the Broadmore craft port by entering the following command:
   settimeout <hh:mm:ss> ↵
   Example: **settimeout 00:05:00** sets the timeout to 5 minutes.
   The current value can be displayed by entering **settimeout** by itself.

NOTE: The SSH session timeout is fixed at 5 minutes.

4. Reboot the Broadmore for the change to take effect by entering the following commands at the Broadmore prompt:
   cli ↵
   maintain ↵
   redundancy ↵
   cpu ↵
   rebootstandby ↵
   releasecpu ↵

NOTE: The above command sequence reboots the standby CPU (if any) and then the online CPU. In a redundant system, both CPUs must be rebooted into the FIPS mode. Rebooting the online CPU will terminate the current management session. After reboot, the previous standby CPU will normally become the online CPU. It may take several minutes for the ARP tables in the network to refresh before you can log into the online CPU.

5. Verify that the Broadmore is in FIPS mode by logging in with an SSH terminal emulator such as SecureCRT (see *"Logging In" on page 11-9*). If you must use Telnet, the Broadmore is not in FIPS mode.
6. Start up the CAMMI interface by entering the following command at the Broadmore prompt:
   cammi ↵
7. After logging in, also verify that the Broadmore is in FIPS mode by observing that the CAMMI *Help / About Security* screen shows that FIPS mode is active (see *"Help About Security" on page 11-17*).
8. Select *Administration / User ID Rules* and set the username and password minimum length values (see *"User ID Rules" on page 11-26*).

NOTE: The Broadmore will only enforce the minimum length values when creating new user accounts. Old accounts are not affected. The Superuser (Crypto Officer) must ensure that all user accounts meet FIPS 140-2 requirements.

Disabling FIPS Mode

Only a Superuser (Crypto Officer) can change the security modes. The security mode can only be changed after successfully logging into the Broadmore, then performing the following steps.

CAUTION! DISABLING FIPSMODE WILL DELETE EXISTING USER ACCESS ACCOUNTS AND CRYPTOGRAPHIC KEYS AND REVERT THE BROADMORE TO THE FACTORY DEFAULT SUPERUSER ID AND PASSWORD, WHICH CAN DENY MANAGEMENT ACCESS AND COMPROMISE SECURITY. NO ONE CAN LOG IN REMOTELY TILL THE BROADMORE IS REBOOTED.

1. Log into the online CPU (Broadmore primary IP address) with a secure SSH terminal emulator such as SecureCRT (see *"Logging In" on page 11-9*).
2. Disable FIPS mode by entering the following shell command at the Broadmore prompt:
   fipsmode off ↵
3. Reboot the Broadmore for the change to take effect by entering the following commands at the Broadmore prompt:
   cli ↵
   maintain ↵
   redundancy ↵
   cpu ↵
   rebootstandby ↵
   releasecpu ↵

NOTE: The above command sequence reboots the standby CPU (if any) and then the online CPU. In a redundant system, both CPUs must be rebooted into the non-FIPS mode. Rebooting the online CPU will terminate the current management session. After reboot, the previous standby CPU will normally become the online CPU. It may take several minutes for the ARP tables in the network to refresh before you can log into the online CPU.

4. Log into the Broadmore using a conventional terminal emulator such as Telnet (see *"Logging In" on page 10-5*).
5. Start up the CAMMI interface by entering the following command at the Broadmore prompt:
   cammi ↵
6. Verify that the Broadmore is not in FIPS mode by observing that the CAMMI *Help / About Security* screen shows that FIPS mode is inactive.

Enabling SecurID

NOTE: SecurID is only available when FIPS mode is turned on (see *"Enabling FIPS Mode" on page 11-18*). Only the online CPU can be accessed when SecurID is enabled.

Only a Superuser (Crypto Officer) can change the security modes. The Broadmore is shipped from the factory with SecurID turned off. The security mode can only be changed after successfully logging into the Broadmore while in its current security mode.

SecurID requires that the Broadmore CPUs be set up for use with a SecurID server. Each CPU must be set up one at a time. In a redundant system, one CPU must be temporarily removed while the other is being set up.

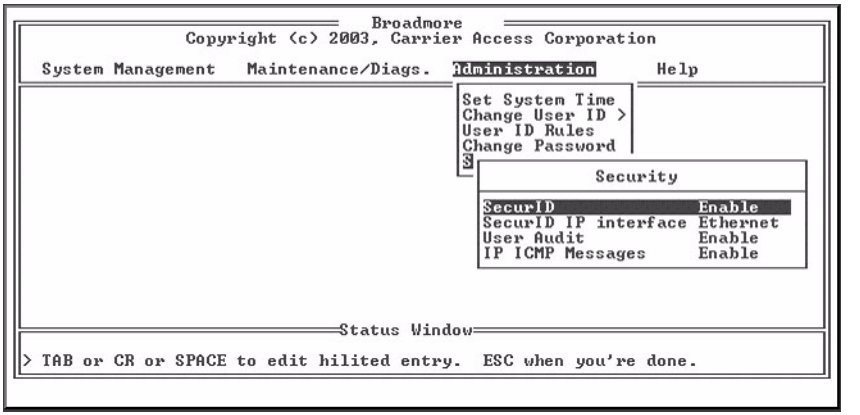
SecurID requires the following:

- RSA SecurID server version 5.0 or higher
- SSH terminal emulator, such as SecureCRT
- SFTP software, such as SecureFX
- Ethernet connection to both CPUs

1. If the Broadmore has two CPUs, remove the CPU that is not on line.

Setting up the first CPU

2. Using an SSH terminal emulator, log into the online CPU and ensure that the Broadmore is operating in FIPS mode (see *"Help About Security" on page 11-17*). If FIPS mode is not enabled, follow the procedure in *"Enabling FIPS Mode" on page 11-18* to turn on FIPS mode and then reboot the Broadmore.
3. On the SecurID server, create an Agent Host for the Broadmore and a **sdconf.rec** file.
4. Using SFTP software, put the **sdconf.rec** file into the **securid** directory of the Broadmore's online CPU.
5. Using the CAMMI *Administration / Security* menu, select SecurID and press the space bar choose Enable. Select SecurID Interface and press the space bar to choose Ethernet. (See figure below.)
6. If the step 5 was successful, the first CPU is set up correctly and you are ready to use SecureID with that CPU. If step 5 was not successful, do not proceed until the problem is fixed.



Item	Options	Comments
SecurID	Enable, Disable	This feature is described in the Carrier Access *RSA SecurID Ready Implementation Guide* for the Broadmore 500, 1700, and 1750. This guide also describes how to manipulate the security options in the "sdopts.rec" file.
SecurID IP Interface	No change, IP, LANE, CIP	If you choose "no change," any pre-existing sdopts.rec file will not be affected. If no sdopts.rec file exists, the system will default to ethernet IP. If you choose another option, the system will create an sdopts.rec file with your selection.

Setting up the second CPU

7. Using SFTP software, get the following three files from the **secureid** directory for use in setting up the second CPU: **sdconf.rec**, **secret**, and **sdopts.rec**.
8. Remove the first CPU and insert the second CPU into the chassis.
9. Using an SSH terminal emulator, log into the second CPU and ensure that the Broadmore is operating in FIPS mode (see *"Help About Security" on page 11-17*). If FIPS mode is not enabled, follow the procedure in *"Enabling FIPS Mode" on page 11-18* to turn on FIPS mode and then reboot the Broadmore.
10. Using SFTP software, put the three files (copied in step 7) into the **secureid** directory of the second CPU: **sdconf.rec**, **secret**, and **sdopts.rec**.
11. Using the CAMMI Administration/Security menu, select SecurID and press the space bar choose Enable. Select SecurID Interface and press the space bar to choose Ethernet.
12. Log out and log back in using your SecurID credentials.
13. If the proceeding step was successful, you can now insert both CPUs.

Disabling SecurID

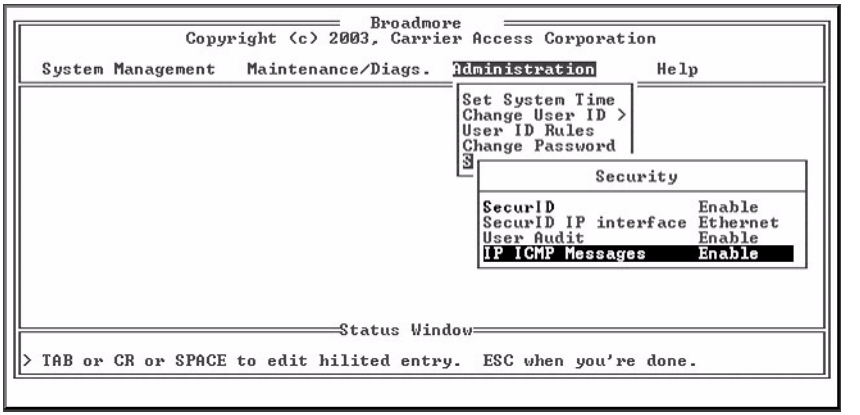
NOTE: SecurID is only available when FIPS mode is turned on (see *"Enabling FIPS Mode" on page 11-18*). Only the online CPU can be accessed when SecurID is enabled.

Only a Superuser (Crypto Officer) can change the security modes. The Broadmore is shipped from the factory with SecurID turned off. The security mode can only be changed after successfully logging into the Broadmore while in its current security mode.

1. Using the CAMMI *Administration / Security* menu, select SecurID and press the space bar choose Disable.
2. The next time you log into the Broadmore, you will not have to provide any SecurID credentials.

IP ICMP Messages

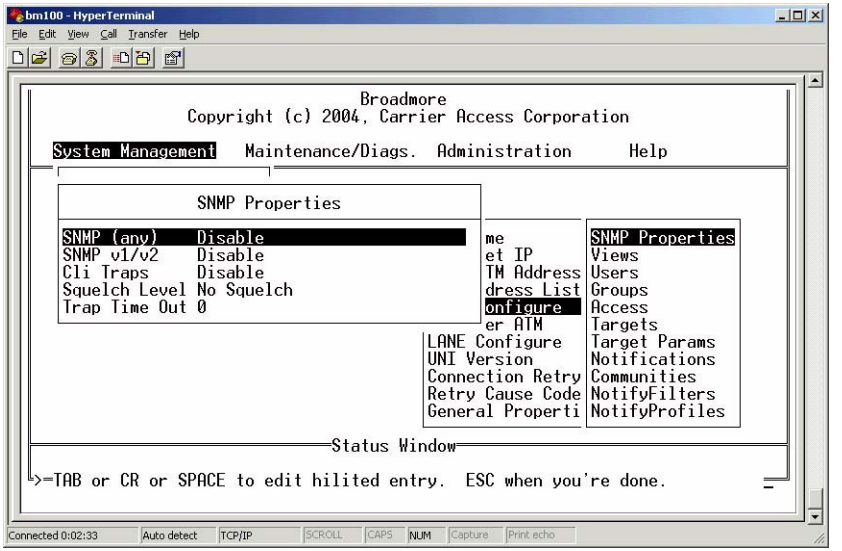
You can use the Internet Control Message Protocol selection to enable or disable all ICMP messages for Internet Protocols such as ping or echo. Disabling ICMP is a common defense against denial-of-service attacks using ping floods.



SNMP Messages

You can use the SNMP Properties selection to enable or disable SNMP messages. The Broadmore supports older SNMP v1 and v2, as well as newer SNMPv3 protocols. You have the option to disable only SNMPv1 and v2 messages, or all SNMP messages.

Only a SuperUser or SysAdmin can access or change these properties (see *"SNMP Properties" on page 12-3*).



Select **System Management** ↵
Select **Configure** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **SNMP Properties** ↵

User Administration and Audit Trails

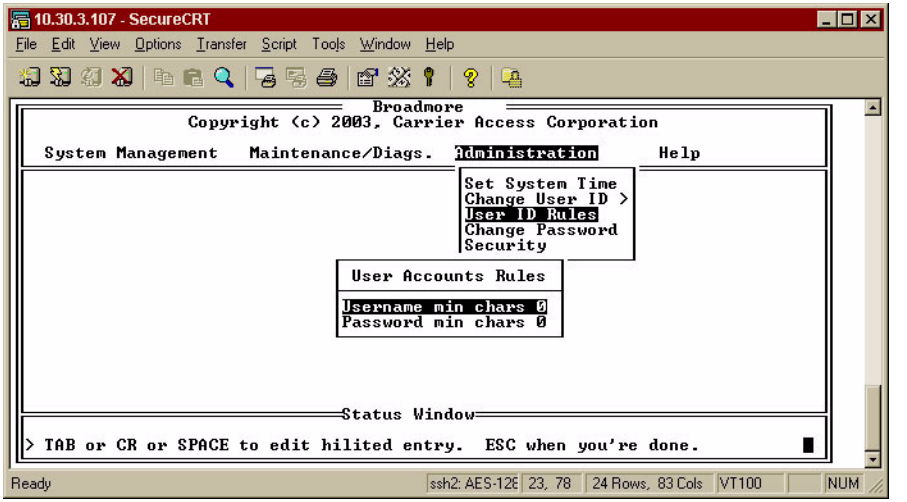
- User ID Rules ... *11-26*
- Change User ID ... *11-27*
- User Audit Trails ... *11-30*

User ID Rules

A SuperUser can set the minimum allowable number of characters in user names and passwords by selecting User ID Rules from the Administration menu.

From this menu, select the Username or Password and enter the required minimum number of characters. These values must be set to at least 6 characters to satisfy FIPS 140-2 security requirements.

NOTE: The Broadmore will only enforce the minimum length values when creating new user accounts. Old accounts are not affected. It is up to the Superuser (Crypto Officer) to ensure that all user accounts meet FIPS 140-2 security requirements.

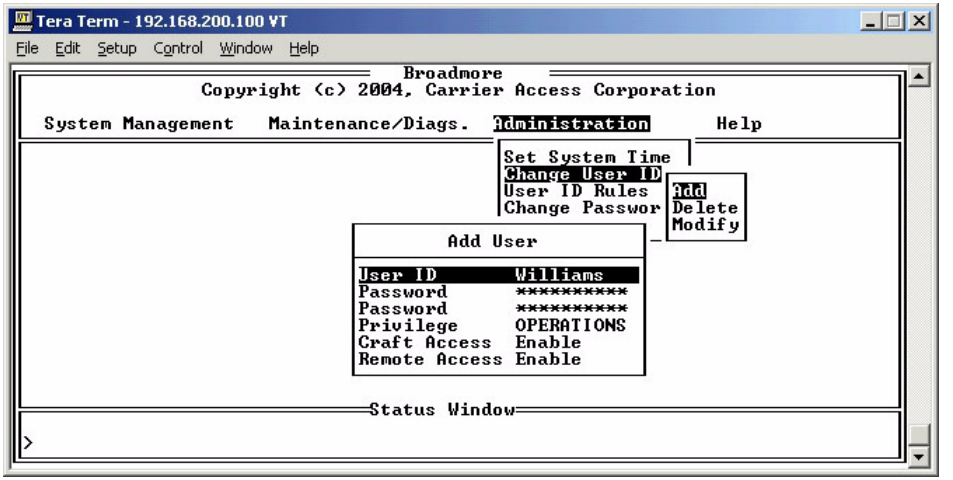


Change User ID

The Change User ID menu allows a SuperUser to add, delete, and modify user IDs.

NOTE: After logging in, any user can change their own password using the Change Password menu.

Adding a User

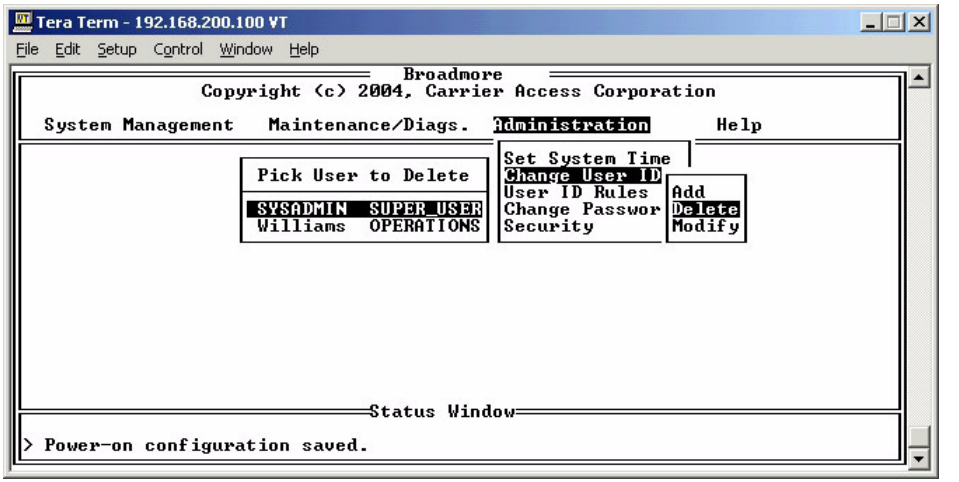


Enter the following information for each user that is added.

Item	Options	Comments
User ID		A unique user identifier
Password/ Password		A unique password for the user and a second password field to confirm
Privilege	BROWSER OPERATIONS SYS_ADMIN SUPER_USER	The level of user access. See *"User Security Configuration" on page 7-26.*

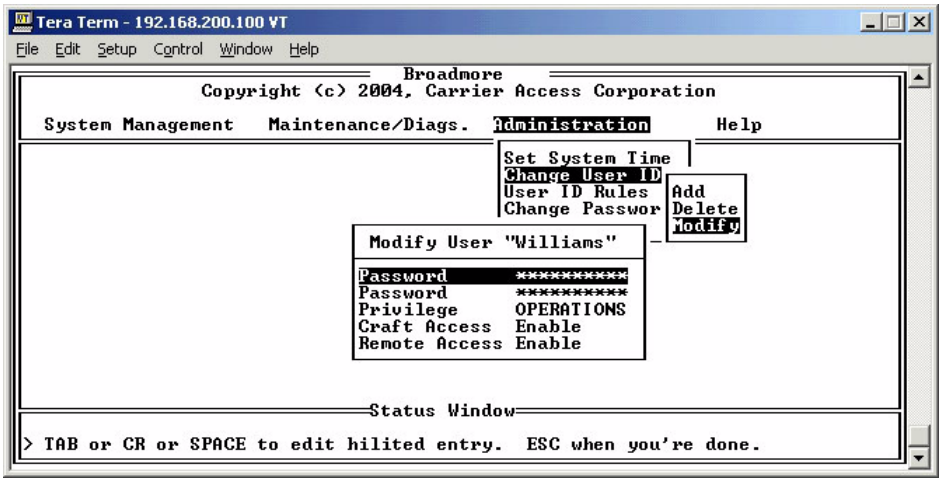
Item	Options	Comments
Craft Access	Enable, Disable	Access through the serial port on the front of the CPU.
Remote Access	Enable, Disable	Access through the Ethernet port on the CPU IOM.

Deleting a User



After you select the user to be deleted, a confirmation message appears. Select Yes to delete the user, or No to exit without making any changes.

Modifying a User



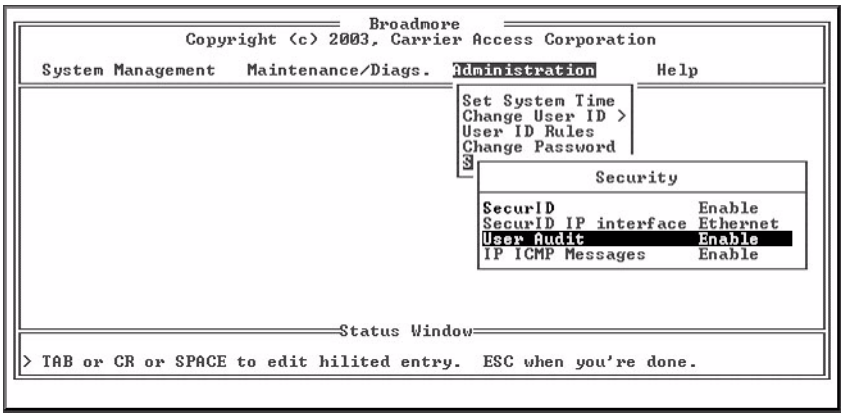
After you select the user to be modified, enter the appropriate information in the Modify User fields.

Item	Options	Comments
User ID		A unique user identifier
Password/ Password		A unique password for the user and a second password field to confirm
Privilege	BROWSER OPERATIONS SYS_ADMIN SUPER_USER	The level of user access. See *"User Security Configuration" on page 7-26.*
Craft Access	Enable, Disable	Access through the serial port on the front of the CPU.
Remote Access	Enable, Disable	Access through telnet, secure shell login, ftp or secure ftp.

User Audit Trails

NOTE: It is recommended that audit trails remain disabled until deployment of the Broadmore with the anticipated "power up" configuration is complete. This prevents the audit logging of numerous initial installation configuration changes. Only a SuperUser can enable or disable this function or use shell commands to access the audit file.

Audit Trails protect a Broadmore by providing traceability of who performed procedures on the unit, what procedures were performed, and when they took place. The Broadmore local audit trail logs changes to configuration parameters and user logins.



NOTE: The audit file is located in the **cam** directory and can only be accessed via SSH2. The craft port does not allow **cat**, **head**, or **tail** commands in the FIPS mode.

NOTE: Audit files can be up to 200k bytes, with the most recent data being located at the end of the file. Use the **tail** command to display the end of the file. For detailed examination, it is best to upload the file via FTP or SFTP.

To display the audit file, at the Broadmore > prompt, enter the following commands:

cd cam ↵

tail audit.txt ↵

An example of the output is provided below.

```
Copyright (c) Integrated Systems, Inc., 1992.
Welcome to pSOSystem...

Broadmore->cd cam
Broadmore->tail audit.txt
08/08/2002 10:36:59 Q SYSADMIN:Security:User Audit=Enable
08/08/2002 10:37:53 Q SYSADMIN:Reboot System
08/09/2002 08:42:10 Q SYSADMIN:Create VP Reservation:"Test", slot G, vp 0, vcSt2
08/09/2002 08:45:22 Q SYSADMIN:Create VP Reservation:"Test2", slot G, vp 1, vcS2
08/09/2002 09:07:25 Q SYSADMIN:General Properties:Bandwidth Meter=Enable
Broadmore->
```

User audit files record the following information for each user action:

- date
- time
- online CPU (Q or R) or standby CPU (q or r)
- user name
- event type
- short description of the event

Once the **audit.txt** file is full, the file is automatically closed, the name is changed

to **audit_o.txt**, in case a SuperUser wants to access the old file via SecureFX in SFTP mode. A new file is then opened named **audit.txt** and new data is written to that file.

Deleting Audit Trails

A user can delete the contents of the system log by using the CAMMI (Maintenance/Diags, View System Log, Delete command) or the corresponding CLI command. However, this only deletes the events that can be viewed by their access level.

Archiving Audit Trails

A SuperUser can archive the **audit.txt** and **audit_o.txt** files using an SSH2 FTP client such as SecureFX to copy the files to another computer or storage device. After logging in with SecureFX, navigate to the **cam** directory and locate the audit.txt and audit_o.txt files.

System Log

The system log file **sys.log** is a circular file that contains a recent history of system users, events, and alarms. Old records are overwritten by new records. The log file identifies the currently active CPU and any user currently logged into the Broadmore. All users can use the CAMMI interface to view those system events permitted by their access role (see *"Maintenance/Diagnostics" on page 8-46*). Only a SuperUser can copy or delete the sys.log file. After logging in using an SSH2 FTP client such as SecureFX, navigate to the **cam** directory and locate the sys.log file. The system log can be deleted and archived in much the same way as the audit log files.

For example, the SuperUser can delete the **sys.log** file by using the **del** shell command, as in the following example.

cd cam ↵

del sys.log ↵

Rather than using FTP, a SuperUser can also display or delete the system log through the Command Line Interface.

To display the system log, log into the Broadmore and enter the following commands at the Broadmore prompt:

cli ↵

maintain ↵

systemlog ↵

showi ↵

You can then navigate through the system log by following the instructions appearing at the bottom of the window.

To delete the system log, enter **clearlog** instead of **showi**.

Shell Commands (FIPS Mode)

- fipsmode ... *11-34*
- selftest ... *11-34*
- settimeout ... *11-35*
- sshdShow ... *11-35*
- sshdSessionShow ... *11-37*
- scp ... *11-38*
- resetSecurID ... *11-39*
- zeroize ... *11-40*
- Authorized Access to Shell Commands ... *11-41*

Shell commands are UNIX-like commands provided by the embedded operating system. Command syntax is available using the "help" command. Authorized Access to each command is based on the user privilege level.

fipsmode

NOTE: The procedure for using this command is given in *"Enabling FIPS Mode" on page 11-18*.

The FIPS mode can only be changed by a SuperUser (Crypto Officer). At the Broadmore prompt, type **fipsmode** (to see current value) or **fipsmode on** (to enable) or **fipsmode off** (to disable). After executing this command, the Broadmore must be rebooted for the change to take effect.

selftest

Self-tests of the FIPS algorithms are performed automatically during power-up. A SuperUser (Crypto Officer) can perform a manual self-test at any time. At the Broadmore prompt, type **selftest**. The following message will be displayed if all tests pass.

```
AES Passed
DES Passed
TDES Passed
```

```
DSA Passed
FIPS 186-2 RAND Passed
RSA Passed
SHA1 Passed
IMAGE SIG VERIFY Passed
HMAC-SHA1 Passed
```

NOTE: If a manually initiated self-test results in a self-test failure, the management module will reboot.

NOTE: A FIPS algorithm self-test failure will immediately disable all management connections, as required by FIPS-2. The Broadmore will continue to carry existing ATM communications traffic across the data plane but the operating configuration can not be changed until the unit is repaired.

settimeout

A SuperUser (Crypto Officer) can set the session timeout for the Broadmore craft port for user inactivity. The command syntax is:

settimeout <hh:mm:ss>

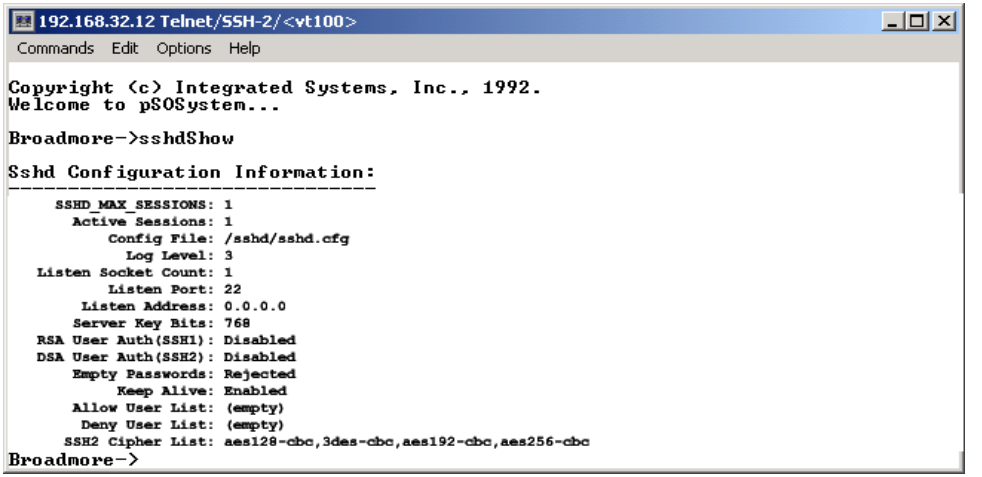
Example: **settimeout 00:05:00** will set the timeout to 5 minutes.

Entering **settimeout** by itself will display the current value.

NOTE: The SSH session timeout is fixed at 5 minutes.

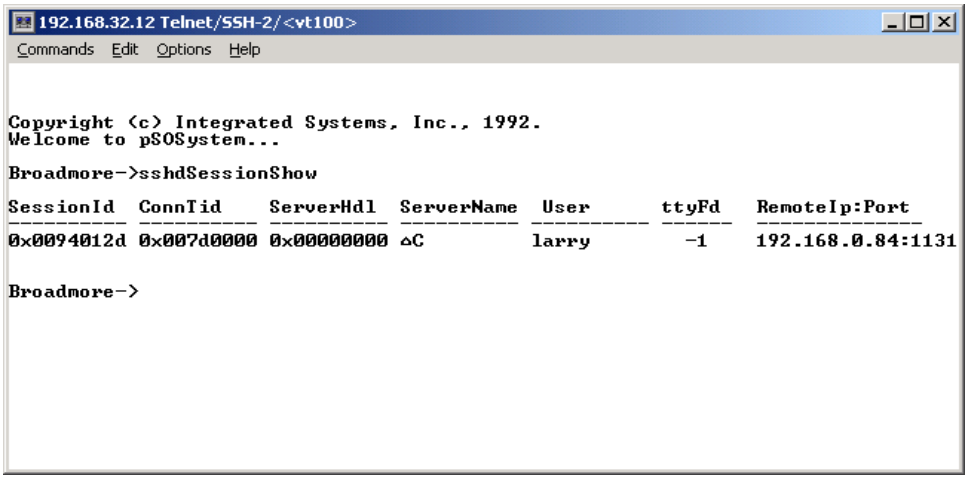
sshdShow

A SuperUser (Crypto Officer) can access the SSHD configuration information, at the Broadmore prompt, by typing **sshdShow**. An example of the output is provided in the following graphic.


192.168.32.12 Telnet/SSH-2/<vt100>
Commands Edit Options Help
Copyright (c) Integrated Systems, Inc., 1992.
Welcome to pSOSystem...
Broadmore->sshdShow
Sshd Configuration Information:
SSHD_MAX_SESSIONS: 1
Active Sessions: 1
Config File: /sshd/sshd.cfg
Log Level: 3
Listen Socket Count: 1
Listen Port: 22
Listen Address: 0.0.0.0
Server Key Bits: 768
RSA User Auth(SSH1): Disabled
DSA User Auth(SSH2): Disabled
Empty Passwords: Rejected
Keep Alive: Enabled
Allow User List: (empty)
Deny User List: (empty)
SSH2 Cipher List: aes128-cbc,3des-cbc,aes192-cbc,aes256-cbc
Broadmore->
Broadmore->sshdShow

sshdSessionShow

A SuperUser (Crypto Officer) can access the information about current active ssh server sessions, at the Broadmore prompt, by typing **sshdSessionShow**. An example of the output is provided in the following graphic.



Item	Comments
Session ID	Session ID
ConnTid	Task ID of the sshd server handling the connection
ServerTid	Session server task spawned for the connection
ServerName	Session server name
User	Connecting user's name
ttydFd	fd available to the server task for IO with the sshd connection task
RemoteIp:Port	IP and port of the remote system

scp

A SuperUser (Crypto Officer) can copy files to a specific directory, using the secure copy (scp) command.

Using SCP

The scp client can be invoked from a target-OS shell by running scp and passing all options as parameter strings. Examples are provided below.

To copy a file, **local_file**, on the target to a remote server, **my_server**, use the following command:

```
-> scp <local_file> my_self@my_server:<local_file_new>
```

To to copy files from the remote server, **my_server**, to the target, **local_file**, use this command:

```
-> scp my_self@my_server:/<dir>/<file> /<dir>/<new_file>
```

To display a list of options and usage information use the "**-h**" command.

```
-> scp -h
```

Enabling Debug Messages

A SuperUser (Crypto Officer) can enable Debug by using the "-v" option. An example is provided below.

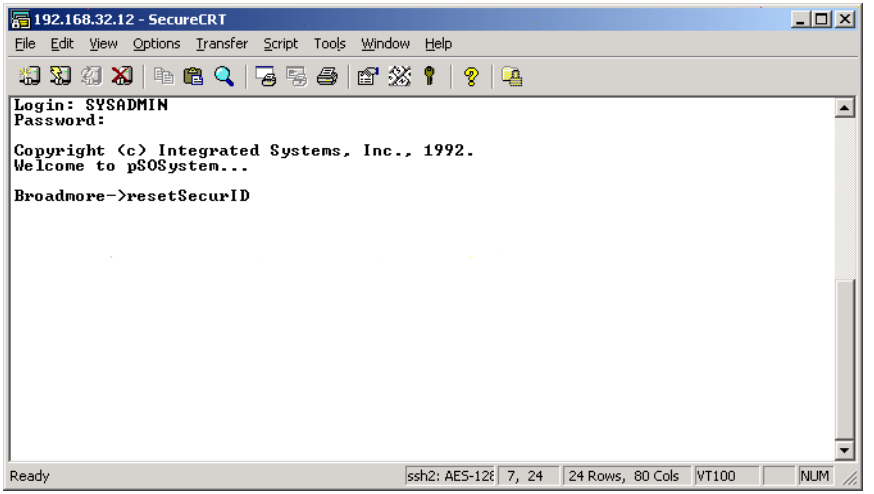
```
-> scp -v [local_file] user_name@remote:[remote_file]
```

NOTE: For more information about shell commands and general information about session privacy, go to www.openssh.org.

resetSecurID

A SuperUser (Crypto Officer) can use the resetSecurID command to reset the node secret file. This command is necessary if the system administrator has, for example, changed the server and needs to get a fresh node secret file.

At the Broadmore> prompt, type resetSecurID as shown in the following figure, and then press Enter.



The node secret file resets, and the screen returns to the prompt. There is no output with this command.

zeroize

WARNING! **THE ZEROIZE COMMAND WILL DECOMMISSION THE CPU MODULE AND MAKE IT INOPERABLE (THE CPU WILL NOT REBOOT). THIS COMMAND WILL PERMANENTLY ERASE ALL CRITICAL SECURITY PARAMETERS AND CPU FLASH MEMORY. A ZEROIZED CPU CONTAINS NO SECURITY DATA OR OPERATING SYSTEM SOFTWARE. A ZEROIZED CPU CAN BE RETURNED TO THE FACTORY FOR REPAIR.**

This command is not intended for normal operational use. It is intended as a security measure (per FIPS 140-2 requirements) to allow a SuperUser (Crypto Officer) to completely remove all security-sensitive data that may be required before decommissioning a CPU. This command has two options:

- To zeroize only the standby CPU, type **zeroize standby**
  This option is intended primarily for decommissioning a defective CPU module.
- To zeroize both CPUs, type **zeroize global** which will first zeroize the standby CPU and then the online CPU. This option is intended for decommissioning the entire Broadmore system.

Authorized Access to Shell Commands

The following table lists the authorized commands available to each access privilege level when operating in FIPS mode.

User ID → ↓ Authorized Services	Super_User	Sys_Admin	Operations	Browser
arp	•4	•		
cammi (start GUI interface)	•	•	•	•
cat	•			
cd	•	•	•	
cli	•	•	•	•
cmp	•			
comp	•			
copy	•			
cp	•			
del	•			
dir	•	•	•	
du	•	•		
echo	•	•	•	•
fipsmode	•			
head	•			
help	•	•	•	•
ifconf	•	•		
ls	•	•	•	
md	•	•		
mem	•			
mkdir	•			
move	•			
mv	•	•		
netstat	•	•		
ping	•	•	•	•

User ID → ↓ Authorized Services	Super_User	Sys_Admin	Operations	Browser
pwd	●	●	●	●
rd	●			
resetSecurID	●			
resetSecurIDIp	●			
rm	●			
rmdir	●			
route	●	●		
savert	●	●		
scp	●			
selftest	●			
setbaud	●	●	●	
setenv	●	●		
settimeout	●			
setwrite	●			
sigmem	●			
snmpinit	●	●		
sshdSessionShow	●			
sshdShow	●			
tail	●			
touch	●	●		
zeroize	●			

SFTP Login

Users can login using SFTP to access Broadmore administrative functions the same way as ftp. With SFTP, the data is encrypted as it flows to and from the Broadmore over IP.

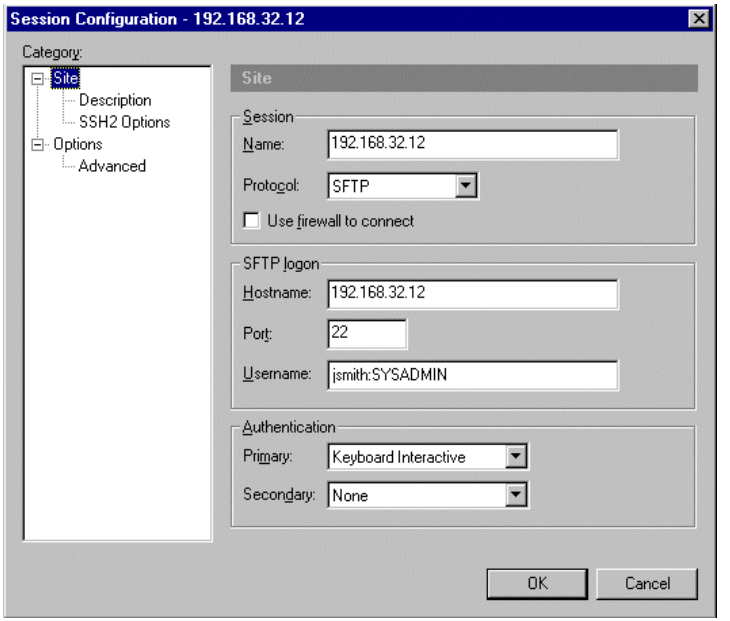
The following procedure provides an example of how to login using SFTP using SecureFX.

Logging in with SecurID Disabled

To log in to RSA SecurID and the Broadmore:

1. Open SecureFX. The Connect window opens.
2. Right-click on the connection you want to use, and select Properties.

   The Session Configuration window opens.



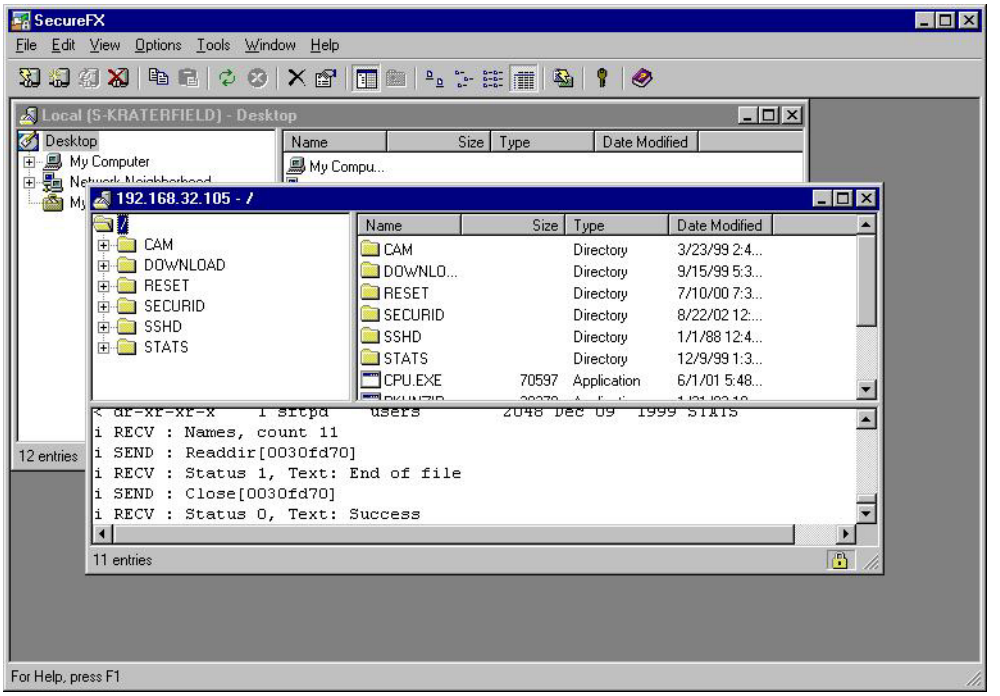
3. In the Session panel, type the IP address of the Broadmore, and select SFTP from the Protocol pull-down menu.

NOTE: Some secure ftp clients do not allow a colon as the first character. The following step works with SecureFX.

4. In the SFTP logon panel, type in the Hostname and Username. The Hostname is the IP address of the Broadmore; the Username is Broadmore local user name prefixed by a colon (for example, **:SYSADMIN**).
5. Select Keyboard Interactive from the Primary pull-down menu in the Authentication panel.
6. Click OK. The following prompt appears:

7. Type in the local Broadmore password, and click OK.

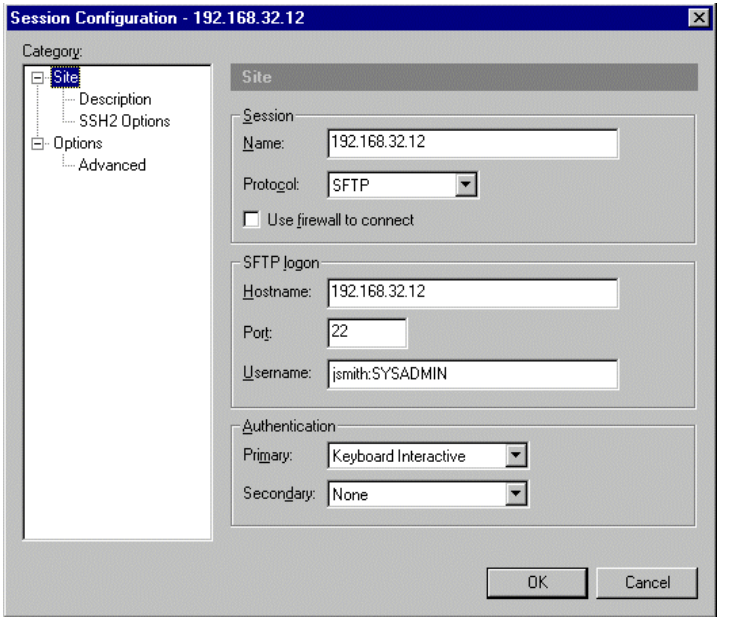
   After successfully logging in, the SecureFX window appears as shown in the following graphic.


SecureFX
File Edit View Options Tools Window Help
Local (S-KRATERFIELD) - Desktop
Desktop
My Computer
Name Size Type Date Modified
192.168.32.105 - /
CAM
DOWNLOAD
RESET
SECURID
SSHD
STATS
Name Size Type Date Modified
CAM Directory 3/23/99 2:4...
DOWNLO... Directory 9/15/99 5:3...
RESET Directory 7/10/00 7:3...
SECURID Directory 8/22/02 12:...
SSHD Directory 1/1/88 12:4...
STATS Directory 12/9/99 1:3...
CPU.EXE 70597 Application 6/1/01 5:48...
i RECV : Names, count 11
i SEND : Readdir[0030fd70]
i RECV : Status 1, Text: End of file
i SEND : Close[0030fd70]
i RECV : Status 0, Text: Success
12 entries
11 entries
For Help, press F1

Logging in with SecurID Enabled

To log in to RSA SecurID and the Broadmore:

1. Open SecureFX. The Connect window opens.
2. Right-click on the connection you want to use, and select Properties. The Session Configuration window opens.



3. In the Session panel, type the IP address of the Broadmore, and select SFTP from the Protocol pull-down menu.
4. In the SFTP logon panel, type in the Hostname and Username. The Hostname is the IP address of the Broadmore; the Username is The SecurID user name and Broadmore local user name joined together with a colon between and no spaces (for example, **jsmith:SYSADMIN**).
5. Select Keyboard Interactive from the Primary pull-down menu in the Authentication panel.

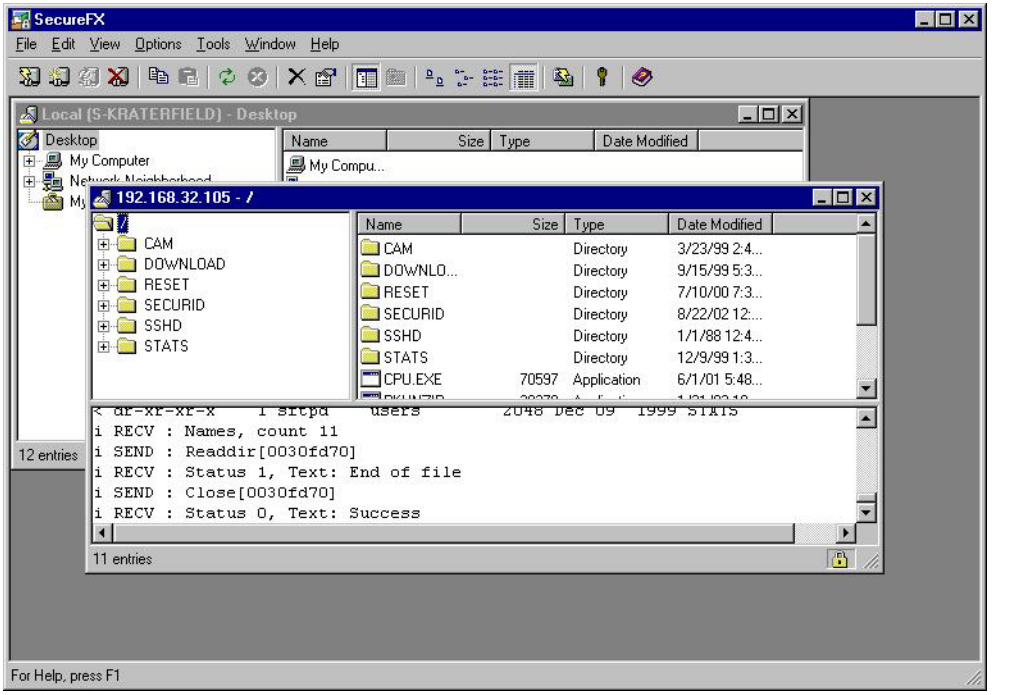
6. Click OK. The following prompt appears:

7. Click OK. The Keyboard Interactive Authentication window opens.

8. Type in the SecurID passcode, and click OK. A second Keyboard Interactive Authentication window opens.

9. Type in the local Broadmore password, and click OK.

   After successfully logging into SecurID, the SecureFX window appears as shown in the following graphic.


SecureFX
File Edit View Options Tools Window Help
Local (S-KRATERFIELD) - Desktop
Desktop
My Computer
Name Size Type Date Modified
My Compu...
192.168.32.105 - /
CAM
DOWNLOAD
RESET
SECURID
SSHD
STATS
Name Size Type Date Modified
CAM Directory 3/23/99 2:4...
DOWNLO... Directory 9/15/99 5:3...
RESET Directory 7/10/00 7:3...
SECURID Directory 8/22/02 12:...
SSHD Directory 1/1/88 12:4...
STATS Directory 12/9/99 1:3...
CPU.EXE 70597 Application 6/1/01 5:48...
< dr-xr-xr-x 1 sftpd users 2048 Dec 09 1999 STATS
i RECV : Names, count 11
i SEND : Readdir[0030fd70]
i RECV : Status 1, Text: End of file
i SEND : Close[0030fd70]
i RECV : Status 0, Text: Success
11 entries
12 entries
For Help, press F1

SecurID Features

SecurID is an option that may be used to help authenticate a user prior to logging into the Broadmore/SSHield Management Module. SecurID does not use FIPS approved algorithms but using SecurID does not in any way affect the security provided by the FIPS-2 validated Broadmore/SSHield Management Module.

Feature	Details
RSA Authentication Methods Supported	Native SecurID
RSA Authentication Manager/Agent Library Version	5.0.2
RSA Authentication Manager 5 Locking	Yes
Replica RSA Authentication Manager Support	Full Replica Support
Secondary RADIUS/TACACS+Server Support	N/A
Location of Node Secret on Client	\securid\securid
RSA Authentication Manager Agent Host Type	Net OS Agent
SecurID User Specification	All remote users
SecurID Protection of Administrators	No

Residual Data and Memory Volatility

- Non-Volatile Memory ... *11-50*
- Network Interfaces ... *11-51*
- Sanitation Procedures ... *11-51*

This notice summarizes relevant security concerns associated with the movement of sensitive data through any Broadmore ATM Multiplexer and subsequent re-deployment of these products into open environments. Should there be any questions or concerns regarding this notice, please contact Carrier Access Corporation customer support at 800-786-9929.

Non-Volatile Memory

The modules used in the Broadmore each contain one or more of the following types of non-volatile memory: removable and non-removable flash memory. There is no internal data path or mechanism provided in a Broadmore to permit network data streams to be recorded onto non-volatile media. Such unintended or hostile actions on the part of the Broadmore could only be enabled by the surreptitious alteration of the device's embedded firmware and hardware. Thus, adequate physical security and access controls are required to prevent hostile implementation of "other" (non-Carrier Access provided) firmware and hardware.

With Release 4.0, Broadmore received FIPS 140-2 validation (see certificate #478 posted under the Validation Lists at *http://csrc.nist.gov/cryptval/*). When operated properly, this version of software contains "zeroize" commands that reformats the flash memory and destroys all stored configuration and sensitive data. It also contains a start-up routine that verifies that no surreptitious software has been loaded. See the *Broadmore/SSHield Management Module Security Policy* for more information.

The Broadmore also has a limited amount of cell buffering implemented via random access memory (RAM). This memory implementation is entirely volatile and will be immediately lost upon power-down. Data that has been buffered in the Broadmore RAM cannot be recovered under any circumstances after power-down.

Network Interfaces

Network Interface Modules (NIMs) are installed in the Broadmore ATM Multiplexer and provide an interface to the ATM network. Each NIM contains non-volatile flash memory for storing run-time code. These chips are not physically accessible from the ATM data path and thus cannot store data that passes through the Broadmore.

Sanitation Procedures

The following table summarizes procedures for all Broadmore modules when removing them from authorized areas to open areas.

Product Release	Product Type	Sanitation Procedure
Release 3.8 and earlier	Broadmore Unit	Power Off for 24 hours Remove flash memory from CPU
	Network Interface Modules	Power Off for 24 hours
	CPU	Power Off for 24 hours Remove flash memory from CPU
Release 4.0 and later	Broadmore Unit	FIPS Zeroize Global/Standby Power Off for 24 hours
	Network Interface Modules	Power Off for 24 hours
	CPU	FIPS Zeroize Standby Power Off for 24 hours

CHAPTER 12

SNMP Configuration

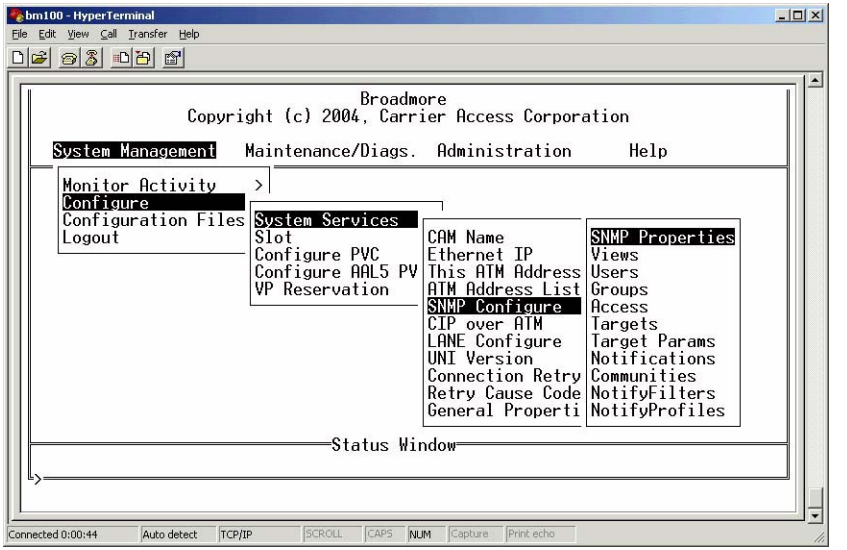
In this Chapter:

- SNMP Overview ... *12-2*
- SNMP Properties ... *12-3*
- USM/VACM Configuration ... *12-7*
- Trap Configuration ... *12-29*

SNMP Overview

Simple Network Management Protocol (SNMP) is a plain-text service with no access to any critical security parameters (CSPs). The Broadmore supports SNMP v1, v2, and v3. Follow the sequence below to configure the SNMP parameters.

NOTE: SNMPv3 configuration must be performed through CAMMI. Do not use the CLI to configure SNMPv3 parameters.



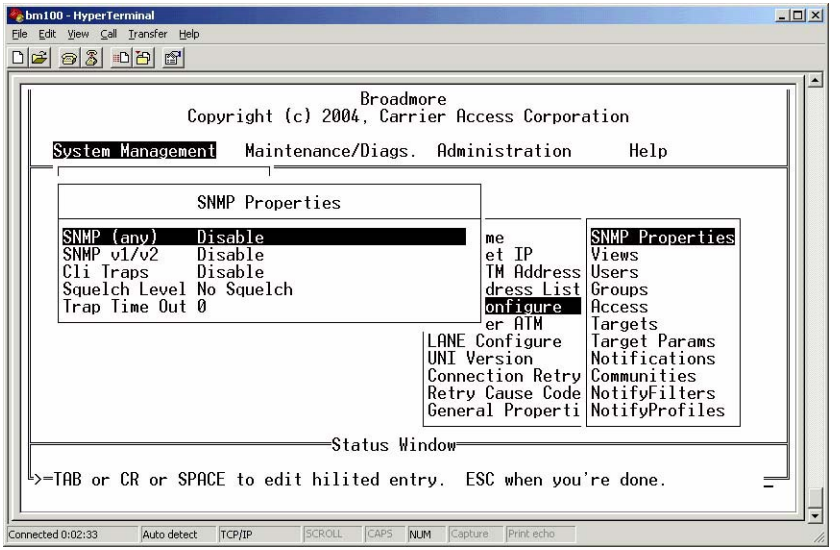
Select **System Management** ↵
Select **Configure** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select the item you want to configure.
With each selection, confirm your changes and press *Esc* to exit.

SNMP Properties

These settings can only be accessed and changed by a SuperUser or SysAdmin. SNMP properties allow the user to control SNMP operation.

NOTE: The shell command **snmpinit** can be used to reinitialize SNMP properties (including traps) as though from a cold boot. SNMP properties will also be mirrored to the standby CPU.

.



Select **System Management** ↵
Select **Configure** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **SNMP Properties** ↵
Select the item you want to configure.
With each selection, confirm your changes and press *Esc* to exit.

The following table lists the SNMP property selections.

Item	Options	Comments
SNMP (any)	Enable, Disable	Enables or disables all SNMP messages.
SNMP v1/v2	Enable, Disable	Enables or disables only SNMP v1 and v2 messages. SNMPv3 messages are enabled.
CLI Traps	Enable, Disable	This selection is a switch (enable/disable) that allows you to view trap messages when logged into CLI. When enabled, trap messages will echo to the screen when they occur.
Squelch Level	Below Current Level, Equal or Below Current Level, No Squelch	The Broadmore is shipped with the squelch level set at “below current level.” The Squelch level allows you to control the trap volume with a single “level” setting. The severity of the last “state” trap is remembered, and future traps will be sent only if the setting for the squelch level permits their severity. Only traps with matching “set” and “clear” instances become the “outstanding” trap for squelching. These are limited to the traps for card/port major and minor alarms. Each port will attempt to “set” a trap when an alarm first trips, in the absence of squelching, and a matching “clear” will be sent when that port's alarms dissipate.

Item	Options	Comments
Trap Time Out	0-3600	Enter Time Out as a value in minutes between 0 and 3600. When a trap triggers, it may be squelched if there is an outstanding trap of serious priority. The Timeout value guards against an old trap that is no longer relevant preventing any future traps. The timeout value guards against an old, no longer relevant trap preventing any future traps. When the time expires, the outstanding trap is discarded. Enter Timeout as a value in seconds between 0 and 3600 (60 minutes) with 1200 (20 minutes) as a recommended initial value.

Example: Squelching Traps

The system will be delivered from the factory with the squelch level at “below current level”. All individual traps will be enabled, with the following severities:

Critical

- Failure reboot
- NIM major alarm

Major

- SAM major alarm
- Slot failure
- Uni up/down

Minor

- SAM minor alarm
- NIM minor alarm

Inform

- Card insert/removed
- NIM switchover
- CPU switchover
- Restore
- User reboot
- Cold Start

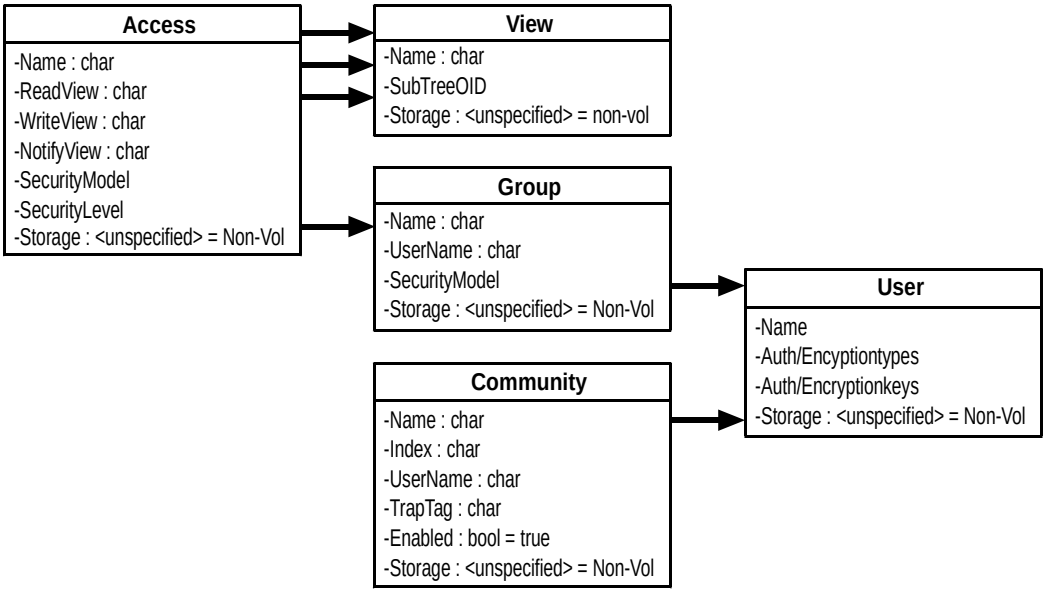
This squelch configuration will send a single “critical” trap if a NIM fiber breaks, and will prevent cascading traps like “NIM switchover” (in a redundant NIM system) from sending dozens of SAM port notifications caused by the switchover. Normally, if you get a trap on a single port of a SAM, you would get traps for all the other ports and all the other SAMs. But with the squelch at “equal or below current level”), the notifications are throttled back to 1 trap for all SAMs.

USM/VACM Configuration

- Users ... *12-9*
- Groups ... *12-14*
- Views ... *12-17*
- Access ... *12-20*
- Communities ... *12-25*

SNMPv3 supports the User-based Security Model (USM) and View-based Access Control Model (VACM). These settings can only be accessed and changed by a SuperUser (Crypto Officer).

Broadmore Implementation of USM/VACM



USM provides authentication and privacy services for SNMPv3. USM provides improved security over SNMPv1 and SNMPv2 by adding encryption and synchronized time indicators. Although USM uses cryptography to support the underlying protocol, it is a plain-text service and does not provide the level of data confidentiality or protection required by FIPS-2. Consequently, it should be treated like any other plain-text service port.

USM uses loosely synchronized monotonically increasing time indicators to defend against certain message stream modification attacks. Automatic clock synchronization mechanisms based on the protocol are specified without dependence on third-party time sources and concomitant security considerations.

VACM is an architecture for viewing and controlling users. VACM defines the access control policy that determines which users can access which subset of MIB objects in the Broadmore. VACM also defines the type of access (Read/Write) over a view.

The Broadmore organizes the USM/VACM into four tables or *entities*: Views, Users, Groups, and Access. With each entity, the following *actions* are associated:

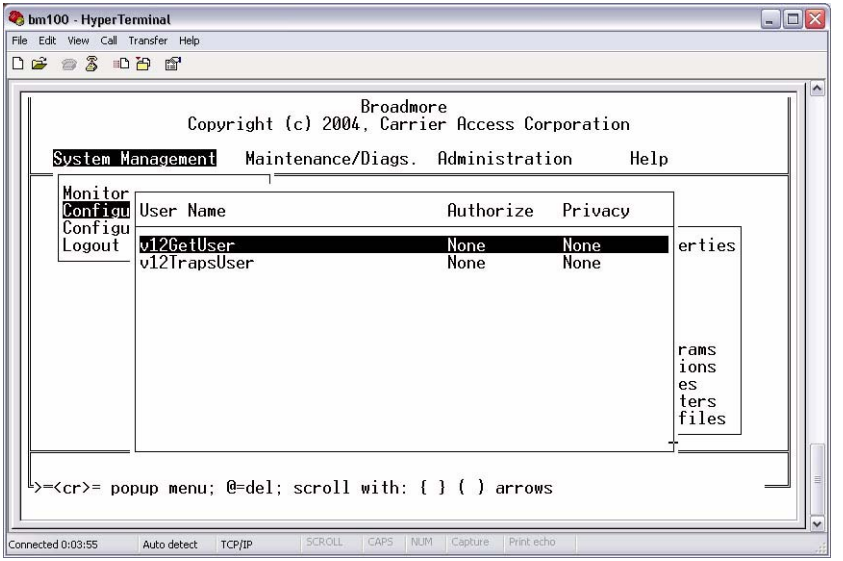
- Edit – used to modify an existing User, View, Group or an Access entry
- Copy – used to copy the information for an existing User, View, Group or an Access entry as a basis for a new one
- Delete – used to delete an existing User, View, Group or an Access entry
- New – used to add a new User, View, Group or an Access entry
- Validate Table – used to check table entries for consistency with other tables.

The Communities table supports the coexistence of SNMP v1, v2, and v3 access described in RFC 2576. The Communities table supports v1/v2 get, set, and trap requests within USM/VACM.

NOTE: When configuring USM/VACM, please note the consequences of selecting certain “Storage Type” parameters in the tables. “Permanent” entries cannot be deleted except by deleting the entire SNMP configuration and rebooting. “Read Only” entries can only be edited or removed by deleting the entire SNMP configuration and rebooting.

Users

This selection allows you to control users and their access privileges. Once this menu option is chosen, a list of existing users will be displayed. The screen below shows the two predefined users.

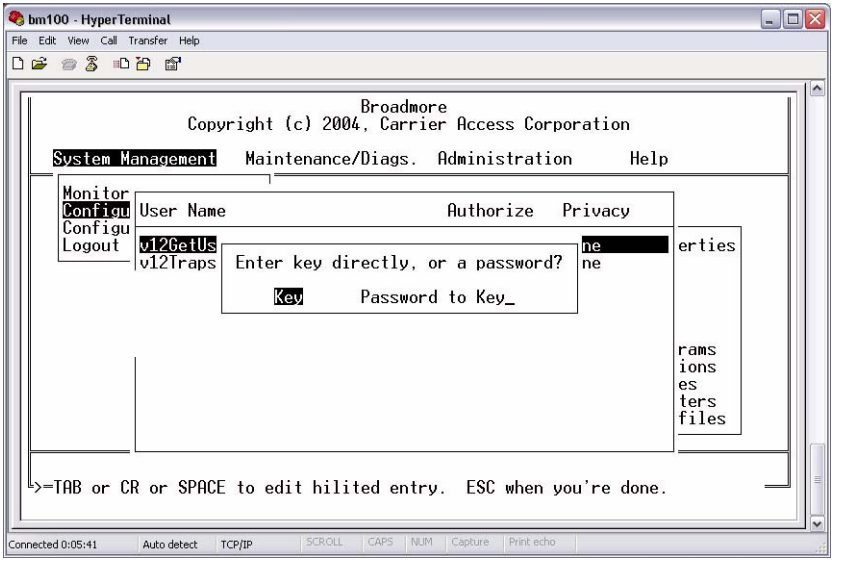


Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Users** ↵
Select one of the following:

* **Edit** ↵, edit the User information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing User
* **New** ↵, enter a new User
* **Validate Table** ↵, check table entries for consistency

With each selection, confirm your changes and press *Esc* to exit.

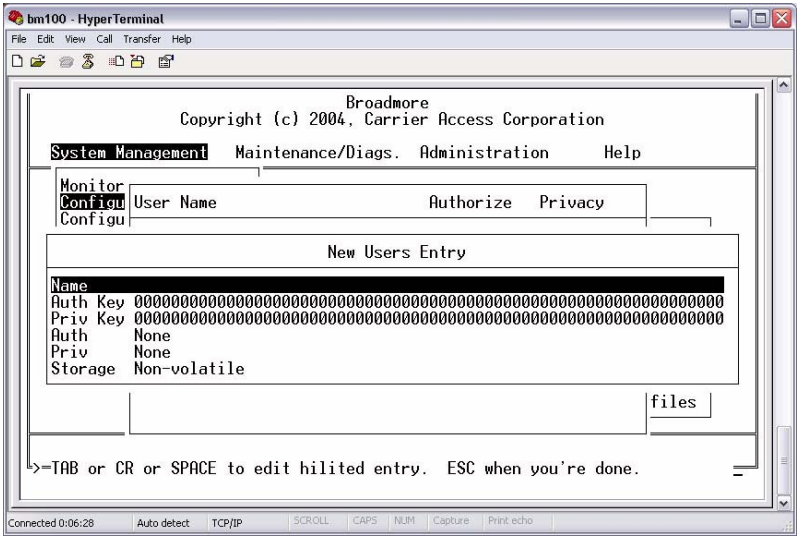
When adding a new user (either through *New* or *Copy* action), the system will present an option to either enter the Authentication and Privacy (Encryption) Key either directly (Key) or as a Password (Password to Key), as shown below.



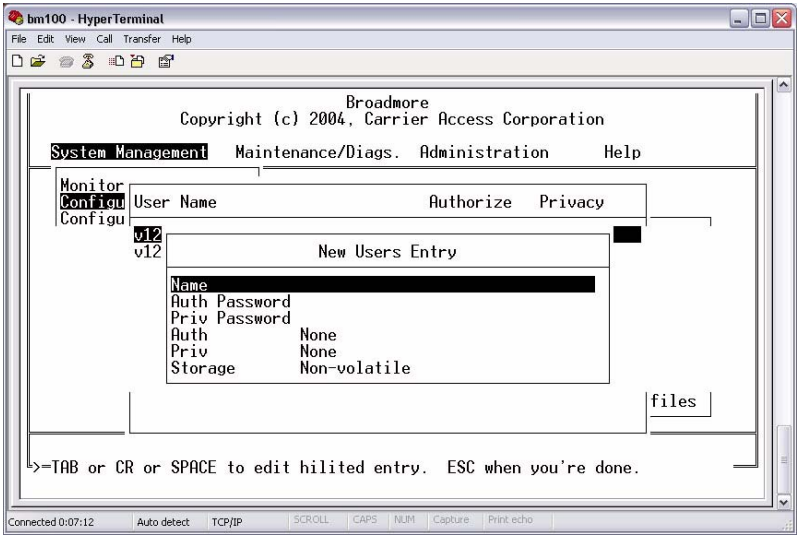
In case you choose to enter the key information as a password, the system will internally generate a key value corresponding to the password entered and maintain that information for the user. This is done because some SNMP clients use keys for authentication and encryption, while others use passwords. For clients that use passwords, a user needs to be created with passwords for the Authentication and Encryption parameters. After choosing either “Key” or “Password to key”, press “OK” and fill in the parameters described in the table above to define the new user, as shown in the following New Users Entry screen.

NOTE: When editing a user who has been created with a password for Authentication and Privacy, you can only edit the resulting key. This is because Broadmore converts the password to a key while saving the user information and does not maintain any record of the original password that had been entered.

Screen for entering User Key



Screen for entering User Password



Use the Esc key to exit this menu. If you have made any changes to the Users information the system will ask you for confirmation on whether you want to accept the changes or not.

NOTE: The entries in the User table are not actual users of the system. These usernames cannot be used for authentication in order to access the Broadmore administration functionality.

The following table describes the selections.

Item	Options	Comments
User Name	string	A unique value for User Name, 1 to 30 characters.
Auth Key	string	Key to be used for authorizing a SNMP user to the Broadmore system.
Priv Key	string	Key to be used to encrypt SNMP traffic.
Auth Password	string	Password to be used for authorizing SNMP user.
Priv Password	string	Password to be used to encrypt SNMP traffic.
Auth	None SHA MD5	Authentication protocol used.
Priv	None AES DES 3DES	Protocol used to encrypt SNMP data between a client and Broadmore SNMP agent.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings remembered after reboot. Settings can not be deleted. Settings can not be changed.

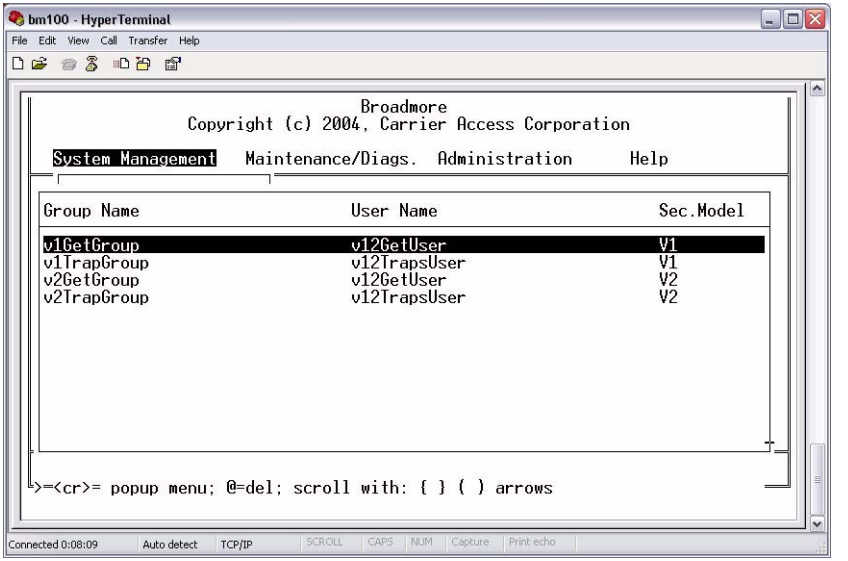
User Edit Rules

The following table describes which parameters can be modified in a Users table entry.

Item	Edit Allowed
User Name	No
Auth Password	No
Priv Password	No
Auth	Yes
Priv	Yes
Storage	Yes

Groups

The VACM model supports the concept of categorizing *users* into *groups*. A group is a unique pair defined by the parameters “User Name” and “Security Model” (see table below). The screen below shows the four predefined groups.



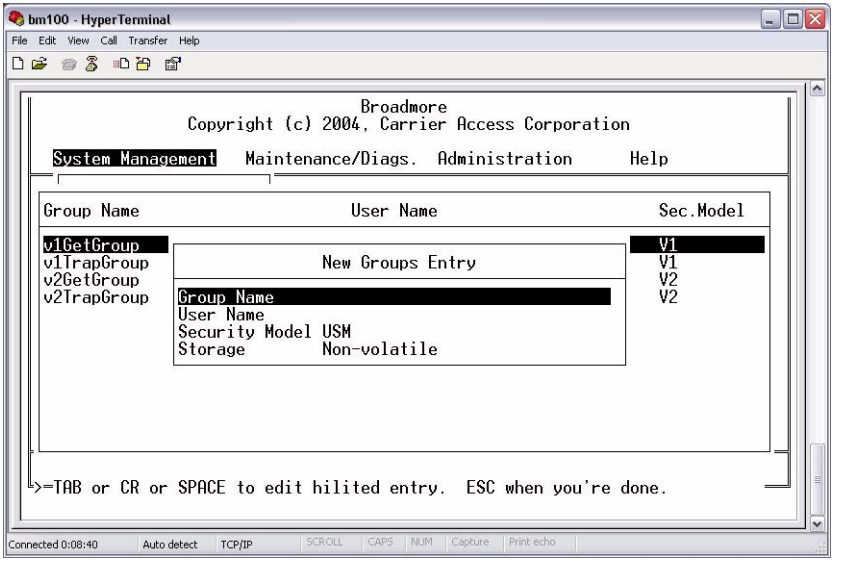
Follow the sequence below to modify the list.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Groups** ↵
Select one of the following:

* **Edit** ↵, edit the Group information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing Group
* **New** ↵, enter a new Group
* **Validate Table** ↵, check table entries for consistency

With each selection, confirm your changes and press *Esc* to exit.

Once this menu option is chosen, a list of existing groups will be displayed. To choose the required action on groups, highlight any of the existing entries and press the Enter key. The screen below shows the New Groups Entry.



The following table describes the selections.

Item	Options	Comments
Group Name	string	value for Group Name, 1 to 30 characters.
User Name	string	value for User Name, 1 to 30 characters.
Security Model	V1 V2 USM	The Security Model used in processing an SNMP query from a client. This parameter can be used to restrict access to the managed objects based on the security model set for a group.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings can be changed. Settings can not be deleted. Settings can not be changed.

Use the Esc key to exit this menu. If you have made any changes to the user information, the system will prompt you for confirmation on whether you want to accept the changes or not.

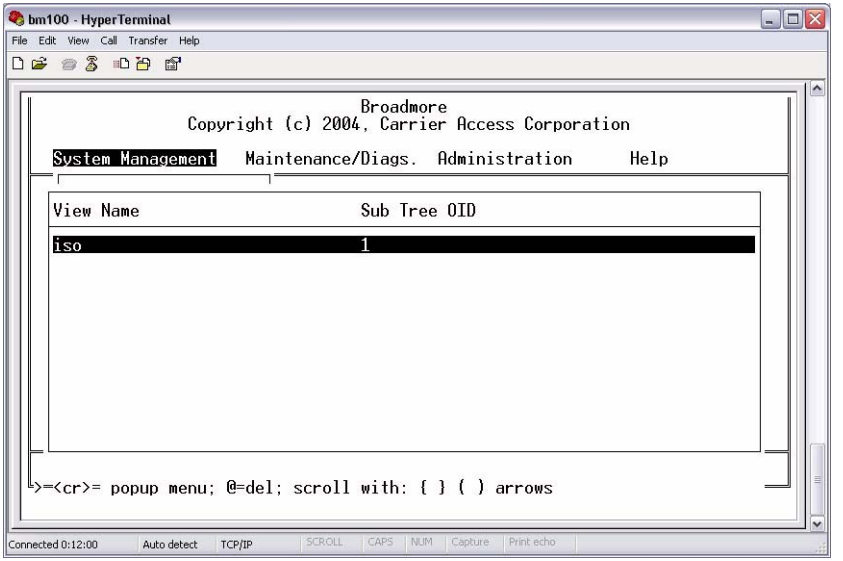
Group Edit Rules

The following table describes which parameters can be modified in a Groups table entry.

Item	Edit Allowed
Group Name	Yes
User Name	No
Security Model	No
Storage	Yes

Views

This selection allows you to create a views and assign object identifiers. The screen below shows the predefined “iso” view.

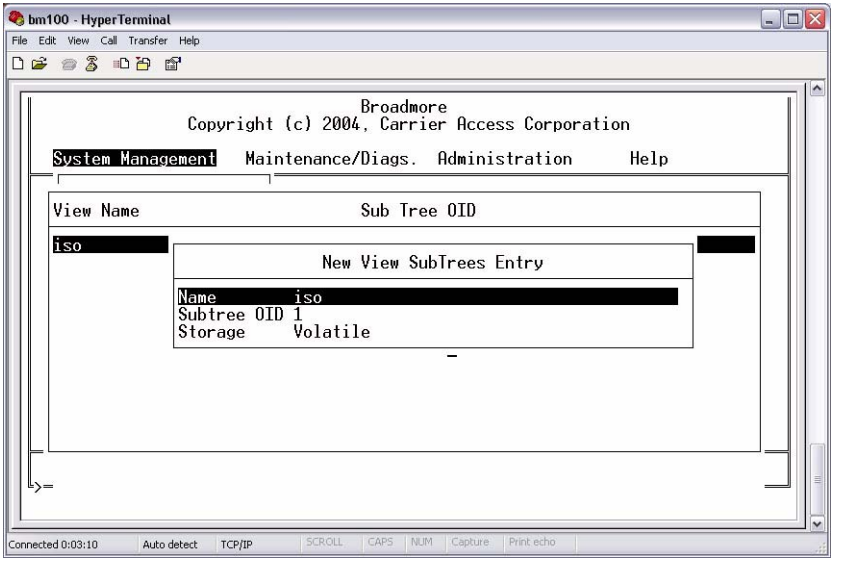

bm100 - HyperTerminal
File Edit View Call Transfer Help
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help
View Name Sub Tree OID
iso 1
>=<cr>= popup menu; @=del; scroll with: { } ( ) arrows
Connected 0:12:00 Auto detect TCP/IP SCROLL CAPS NUM Capture Print echo

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Views** ↵
Select one of the following:

* **Edit** ↵, edit the View information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing View
* **New** ↵, enter a new View
* **Validate Table** ↵, check table entries for consistency

With each selection, confirm your changes and press *Esc* to exit.

Once this menu option is chosen, a list of existing groups will be displayed. To choose the required action on groups, highlight any of the existing entries and press the Enter key. The following shows the New View SubTrees Entry screen.



The managed objects in Broadmore are organized in a tree structure, known as a MIB tree, based on the OID (Object Identifier) of each object. A view defines a particular subtree in this MIB tree. For example, one view could be defined to be over the MIB subtree represented by the OID 1.3.6 while another could be over the subtree represented by OID 1.3.6.1. Of these two views, the latter is more restrictive as it has fewer managed objects under it. A view could also be defined to be one specific OID in the entire MIB tree of managed objects. The following shows the New View SubTrees view.

The following table describes the selections

Item	Options	Comments
Name	string	Unique value for View Name, 1 to 30 characters. Default is “iso” standard.
Subtree OID	string	Unique value for Object Identifier, such as “1.3.6”.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings can be changed. Settings can not be deleted. Settings can not be changed.

Use the Esc key to exit this menu. If you have made any changes to the Users information, the system will prompt you for confirmation on whether you want to accept the changes or not.

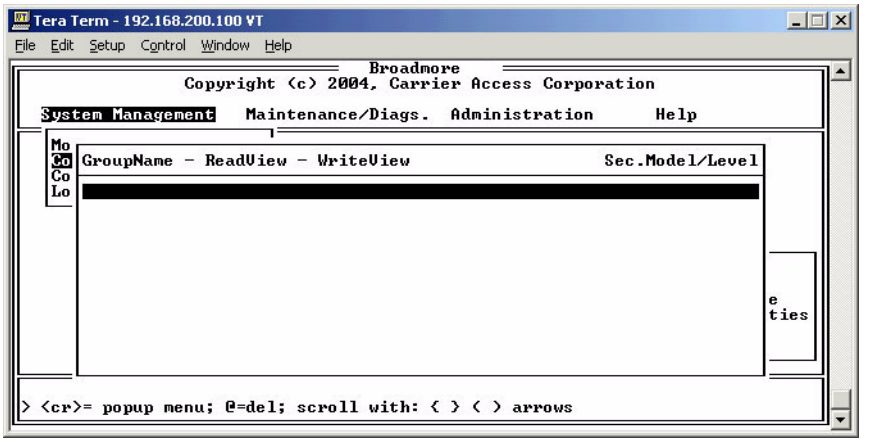
View Edit Rules

The following table describes which parameters can be modified in a Views table entry.

Item	Edit Allowed
Name	No
Subtree OID	Yes
Storage	Yes

Access

This selection allows you to control access to each Group.

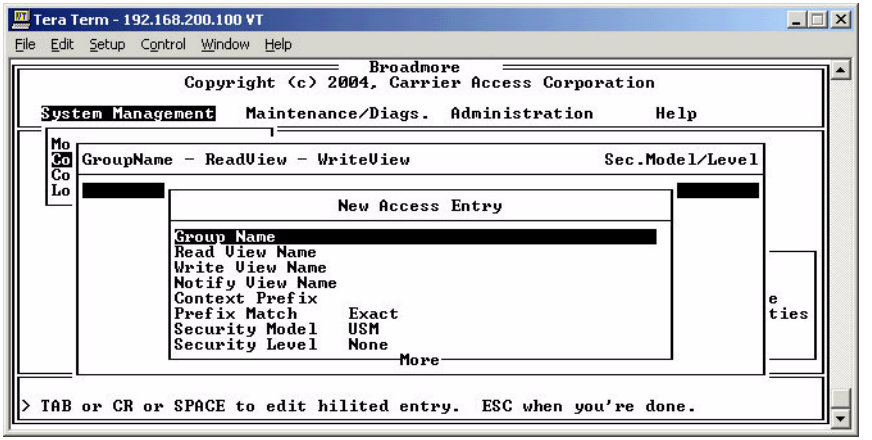

Tera Term - 192.168.200.100 VT
File Edit Setup Control Window Help
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help
GroupName - ReadView - WriteView Sec.Model/Level
> <cr>= popup menu; @=del; scroll with: < > < > arrows

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Access** ↵
Select one of the following:

* **Edit** ↵, edit the Group information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing Group
* **New** ↵, enter a new Group
* **Validate Table** ↵, check table entries for consistency

With each selection, confirm your changes and press *Esc* to exit.

Once this menu option is chosen, a list of existing access entries will be displayed. To choose the required action on an entry, highlight any of the existing entries and press the Enter key. The following shows the New Access Entry screen.


Tera Term - 192.168.200.100 VT
File Edit Setup Control Window Help
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help
GroupName - ReadView - WriteView Sec.Model/Level
New Access Entry
Group Name
Read View Name
Write View Name
Notify View Name
Context Prefix
Prefix Match Exact
Security Model USM
Security Level None
More
> TAB or CR or SPACE to edit hilited entry. ESC when you're done.

The Access entries implement the SNMP access policy for the Broadmore. For more details, see *Access Policy* below. The access table is used to enforce fine-grained access rights that form an access policy. The access list is used to define the parts of the MIB tree that are available for either read or write, for specific combinations of group and security models. It also defines whether an incoming SNMP request needs authentication and whether SNMP messages need to be encrypted.

The following table describes the selections.

Item	Options	Comments
Group Name	string	A group name from the Groups in the system, 1 to 30 characters.
Read View Name	string	A view name from the Views in the system, 1 to 30 characters. The Read View Name is for “get” access.
Write View Name	string	A view name from the Views in the system, 1 to 30 characters. The Write View Name is for “set” access.

Item	Options	Comments
Notify View Name	string	A view name from the Views in the system, 1 to 30 characters. The Notify View Name is for traps and notifications.
Context Prefix	string	A string, 1 to 30 characters. The interpretation depends on the value of the Prefix Match. If not specified, the default is an empty string, "".
Prefix Match	Exact Prefix	Exact – the contextName must match the Context Prefix. Prefix – only the initial substring of the contextName must match the Context Prefix.
Security Model	V1 V2 USM	The Security Model used in processing an SNMP query from a client. This parameter can be used to restrict access to the managed objects based on the security model set for a group.
Security Level	None AuthnoPriv AuthPriv	None – the incoming request requires no authentication or encryption. AuthnoPriv – authentication is required but SNMP messages will not be encrypted. AuthPriv – authentication is required and SNMP messages are encrypted.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings can be changed. Settings can not be deleted. Settings can not be changed.

Use the Esc key to exit this menu. If you have made any changes to the Users information the system will prompt you for confirmation on whether you want to accept the changes or not.

Access Edit Rules

The following table describes which parameters can be modified in a Access table entry.

Item	Edit Allowed
Group Name	No
Read View Name	Yes
Write View Name	Yes
Notify View Name	Yes
Security Model	No
Security Level	No
Storage	Yes

Access Policy

The USM/VACM configuration defines the complete access policy in effect for incoming SNMP requests in the system. SNMP users and a Security model define a SNMP group. Each Group along with a View defines one element of the Access Policy as defined in the Access table. When an SNMP request comes to the system, the system first determines which group the user sending the request belongs to. This is done by looking up the username and the Security model used (V1, V2 or USM) in the SNMP request. Once the group is determined the system looks up the Access table entries and decides:

- whether the authentication and encryption is required for the SNMP query. This is determined by looking up the “Security Level” parameter in the Access table. Authentication is verified based on the information in the User table parameters “Auth Key” or “Auth Password”. In case encryption is required, the key defined by User table parameters “Priv Key” or “Priv Password” is used.

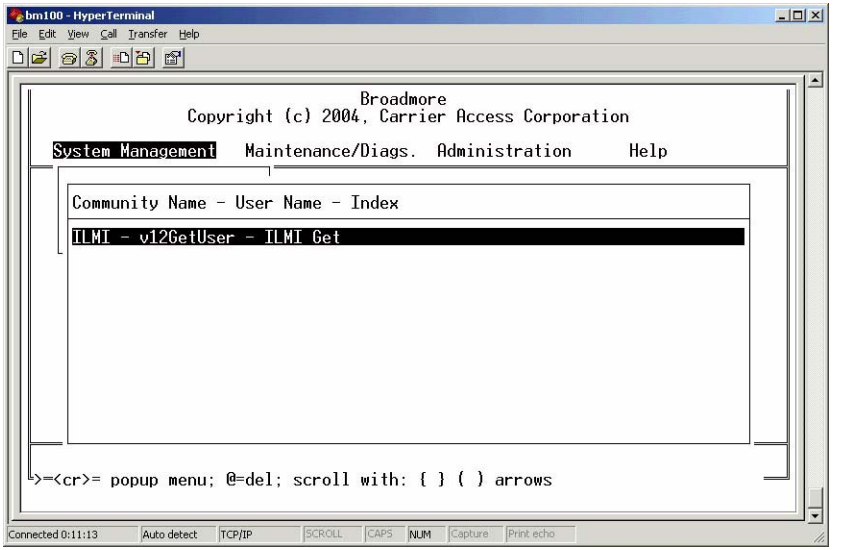
- whether the user sending the SNMP request is eligible to get or set the MIB variable. This is determined by looking up the “Read” and “Write” parameters of the Access table
- the exact set of MIB variables (managed objects) that will be visible to the user. This is determined by the Views table entries.
- access based on the security model set in Groups and Access table.

NOTE: In addition to the standard MIBs, the Broadmore includes enterprise MIBs that are specific to its operation.

Communities

Broadmore supports SNMPv1 and SNMPv2 through the SNMPv3 co-existence model. SNMPv1 and SNMPv2 community strings can be defined using the following menu option. Broadmore permits up to 20 entries in this table.

These settings can only be accessed and changed by a SuperUser.


bm100 - HyperTerminal
File Edit View Call Transfer Help
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help
Community Name - User Name - Index
ILMI - v12GetUser - ILMI Get
>=<cr>= popup menu; @=del; scroll with: { } ( ) arrows
Connected 0:11:13 Auto detect TCP/IP SCROLL CAPS NUM Capture Print echo

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Communities** ↵
Select one of the following:

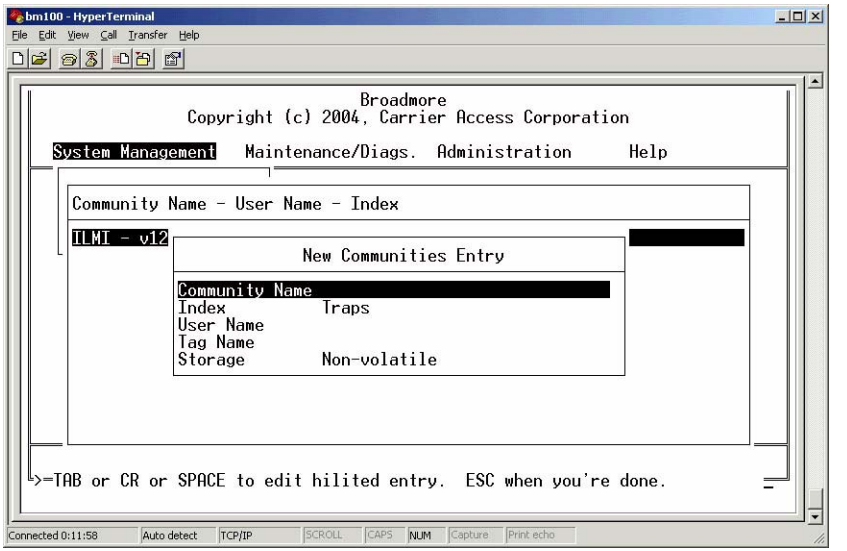
* **Edit** ↵, edit the Community information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing Community
* **New** ↵, enter a new Community
* **Validate Table** ↵, check table entries for consistency

With each selection, confirm your changes and press *Esc* to exit.

NOTE: Selecting the Validate command on this table will check each "User Name" value for existence in the "usmUserTable" and check each Tag Name for existence in the "snmpNotifyTable".

NOTE: So that the Broadmore properly registers with the ATM Switch, the Broadmore automatically creates an "ILMI" community with predefined Views, Users, Groups, and Access. These properties are read-only.

ILMI (Interim Local Management Interface) is an independent industry standard used for configuration of ATM interfaces. Although it is based on SNMP, ILMI communication actually occurs using a transport other than IP that traverses only the physical ATM link. ILMI is essential to functions such as ATM auto-discovery and LANE (LAN Emulation).



Use the Esc key to exit this menu. If you have made any changes to the Community information the system will ask you for confirmation on whether you want to accept the changes or not.

The following table describes the selections.

Item	Options	Comments
Community Name	string	SNMPv1 and SNMPv2 Community Name, 1 to 30 characters. The Community Name is used with the User Name and Tag Name to determine get, set, and trap access.
Index	string	The Index permits the table to specify the same actual string multiple times. A particular Index must be unique.
User Name	string	Value for User Name, 1 to 30 characters.
Tag Name	string	Value for Tag Name, 1 to 30 characters. Must be same as in Notify table if this community will be used in notifications. Leave empty if used only for get and set access.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings can be changed. Settings can not be deleted. Settings can not be changed.

There are several ways to set up communities, so automatically setting all of them up will unnecessarily limit the customer's flexibility.

Example 1: Use the same "public" string for every get/set/trap

- Create a user "v1v2GetSetUser" with all the get/set privilege you desire using the appropriate view, user, group, and access table entries. In this case, the "access" record would have both the read and write views filled in.
- Create a community name "public" with index "GetSet" and an empty "Tag", and specify the user "v1v2GetSetUser"
- Create a community name "public" with index "Trap" and tag "Trap". Specify the predefined user "v12TrapsUser".

Example 2: Use a different string for “set”

- Create a user “v1v2SetUser” with all the set privilege you desire using the view, user, group, and access tables
- Create a community name “private” with index “Set” and no tag. Specify the user “v1v2SetUser”.
- Create a community name “public” with index “Trap” and tag “Trap”. Specify the predefined user “v12TrapsUser”.
- Create a community name “public” with index “Get” and no tag. Specify the predefined user “v12Getuser”.

In fact, you can create as many get and/or set community strings as you desire, provided that they map back to users with the privileges you desire and they all have arbitrarily unique index fields. For example, if you wished to have three different “get” community strings, you might use the index values “Get1”, “Get2”, “Get3”.

Trap Configuration

- Trap Detection Overview ... *12-29*
- Trap Management Overview ... *12-30*
- Table Usage ... *12-33*
- Targets ... *12-34*
- Target Parameters ... *12-36*
- Notifications ... *12-38*
- Notify Filters ... *12-41*
- Notify Profiles ... *12-43*

Trap Detection Overview

The Broadmore supports trap-directed notifications. This means that the Broadmore can automatically send a notification message to a network manager when a certain trap event occurs. This is much more efficient than having to continually poll each device on a network to check if it is working properly.

The Broadmore can send the following kinds of trap notifications:

- Major/Minor Alarms for each module and port – each “set” alarm is matched by a “clear”, indicating the states of the fault LEDs on the chassis or module
- Module inserted or removed from the chassis
- Slot failure
- UNI up/down
- Redundancy switchover
- File restores by the user – a user audit event
- Reboots by the user – a user audit event
- Reboots from system failure
- Cold start – a generic trap

Traps are a valuable network management tool for monitoring system status. However, to realize their full value, the system should post only those conditions requiring action by maintenance personnel. Otherwise, a fundamental system problem might create an avalanche of related traps, resulting in further degradation of the network.

Network operations organizations also have varying policies on what conditions to monitor and what alarm severities to assign to each condition.

The Broadmore gives selective control over traps to mitigate these issues.

- Squelching traps following a serious outstanding trap, to focus attention only on that “first fault.”
- Enabling or disabling individual traps, to match local monitoringpolicies.
- Adjusting individual traps severities works in concert with the “squelch” setting to control traps volume. This severity setting is completely independent from the major/minor indication in some of the traps names.

Trap Management Overview

SNMP traps are managed by a “Notification Originator” – a software application that makes decisions based on events and the contents of various SNMP tables. If the decision is to send a notification message, the Notification Originator assembles the relevant trap information into a Protocol Data Unit (PDU) and sends the PDU to a network manager at a target address.

SNMPv3 uses SNMPv2 PDUs and also adds target address tables that tell the Notification Originator which targets should be sent notifications with given Object IDs (OIDs) in them. For example, a particular event may cause a *linkDown* or a *warmStart* notification. You can specify that the *linkDown* message be sent to a specific entity or a group of entities in the target address table.

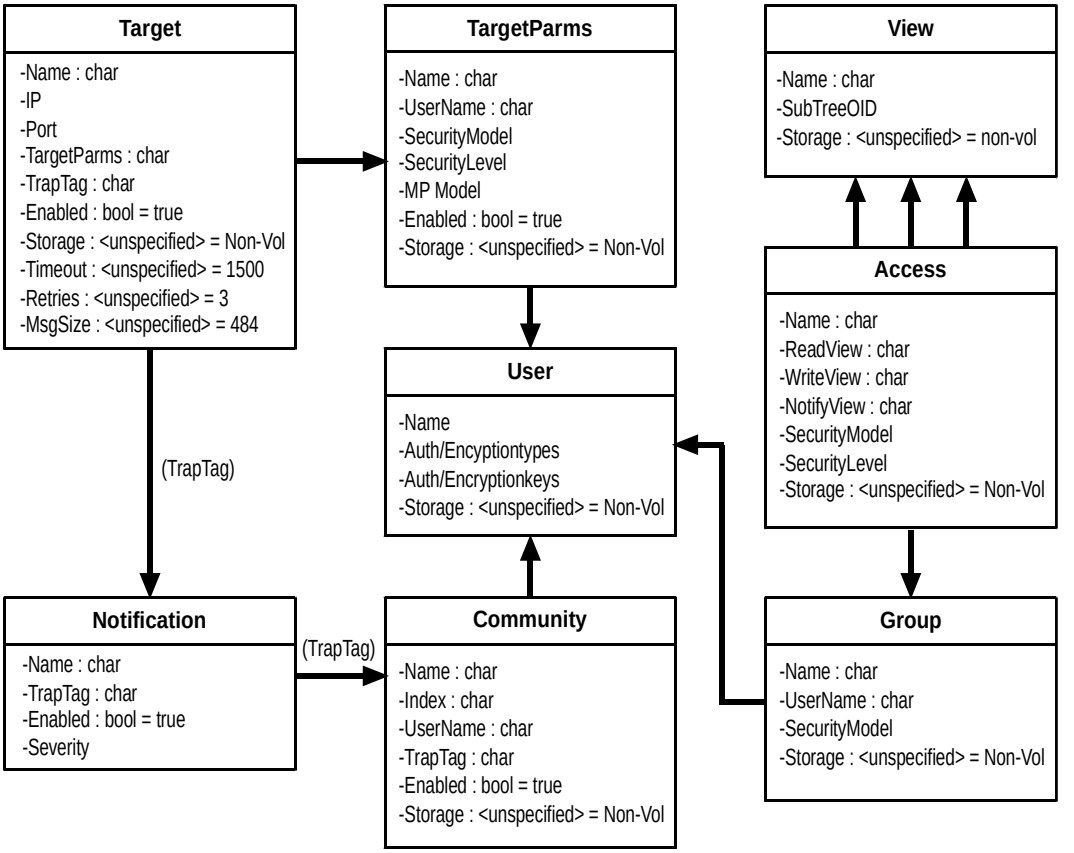
To define targets, the Notification Originator application uses the SNMP Notify and target tables. The target tables include filter and profile tables that are used to determine if specific notifications should be sent to entities in the target address table. Other tables are used for defining parameters that are needed in SNMPv3 PDUs, such as the Security Model, the Security Level, and the Security Name.

The Notification Originator uses the various tables in two ways:

- to identify the targets to send information to, including the priority of one type or event over another
- to create the PDU that will be sent.

The idea is to organize the information into specific tables that can be tied to other tables, as shown in the figure below. All the target addresses used by notifications and proxies are put into one table. Another table is used to identify which elements belong to the notifications. The target parameter table contains the information for creating SNMPv3 PDUs. Other tables are used to identify which notifications should go to which targets. When the Notification Originator creates an INFORM or v2TRAP PDU for SNMPv3, as opposed to locating proper targets, it takes information from the target address table and the target params table.

Broadmore Implementation of SNMP tables in RFCs 2273 and 2573.



The sequence of events in using these tables is as follows:

1. An event occurs and the Notification Originator goes to work.
2. The Notification Originator uses the notify table to identify possible targets to which to send a message. These are only possible targets because there may be notification filters setup to identify a subset of these possible targets that will be sent the message.
3. If no filters are set up (that is, no entry is in the snmpNotifyFilterProfileTable corresponding to this target), the Notification Originator can create and send the PDU(s). The process is then done.
4. If filters are on but the Notification Originator cannot find an entry for any of the specific targets, no PDUs can be sent. The process is then done.
5. If filters on and we have a filter entry, the Notification Originator checks the filter to see if it is set to include or exclude this target. If the filter is set to exclude this target, then the message need not be sent to this target.
6. If filters are on and the filter associated with the target provides a mask, the mask is used to see if this trap event can be sent to this target. The mask allows the Notification Originator to check if the OID of the trap and snmpTrapOID.0 matches the subtree that is in the notify filter table. That way, it can check for a certain event to send to a target, such as a warmStart message only.
7. Finally, using information from the target params table that is accessed from the target address table, the Notification Originator checks the target address (user information) to see if the entity has view privileges for the object. If the view is okay, the PDU(s) are sent. Either way, the process is completed. Views are checked whether or not filters exist.

Table Usage

The following summarizes the way that the SNMP tables are used.

User Management:

- User, Community, View, Access, and Group Tables (from VACM)

Format for the PDU to send (also used for Proxies):

- Target Address Table – contains domain and addressing information, timeout and retry information, and a tag list (snmpTargetAddrTagList) to define where to send notifications (and to forward proxied messages). There is also a link into the Target Params table.
- Target Params Table – contains the definition of parameters such as the Message Processing Model, the Security Model, the Security Level, and the Security Name to build and SNMPv3 PDU.

Tables to identify targets and provide finer selection of events to send:

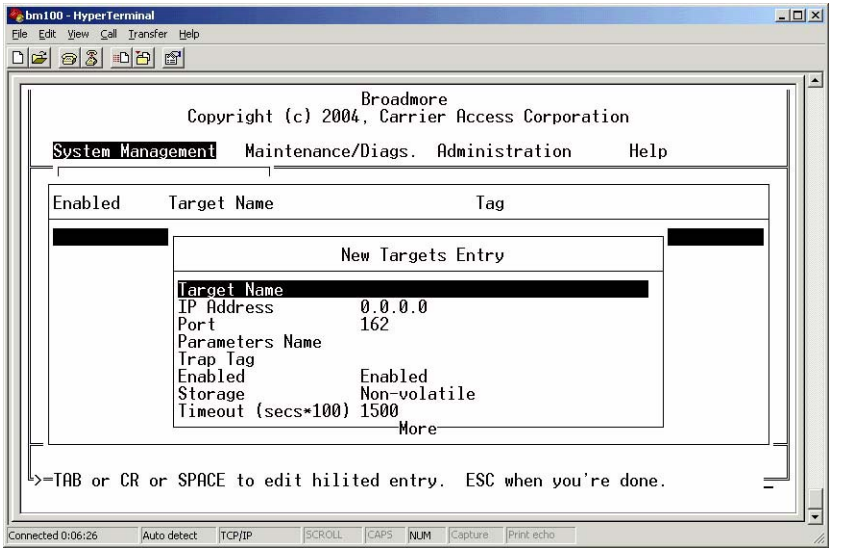
- Notifications Table – how to group targets to send notifications and what type of notification to send
- Notify Filter Profile Table – a list of filters used by a notification for finer grained control over trap destinations
- Notify Filter Table – filters to limit the number of notifications generated for a particular target

NOTE: The Broadmore predefines some SNMP v1/v2 trap parameters to ensure proper operation with ATM switches. For example, there is a predefined v1/v2 "get" user that can be added to a community (such as "public") in the communities table.

NOTE: When configuring traps, please note the consequences of selecting certain "Storage Type" parameters in the tables. "Permanent" entries cannot be deleted except by deleting the entire SNMP configuration and rebooting. "Read Only" entries can only be edited or removed by deleting the entire SNMP configuration and rebooting.

Targets

This selection allows you to enter up to 10 target IP addresses to receive trap notifications. The screen below shows the New Targets Entry.



Follow the sequence below to modify the list.

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Targets** ↵
Select one of the following:

* **Edit** ↵, edit the Target information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing Target
* **New** ↵, enter a new Target
* **Validate Table** ↵, check table entries for consistency

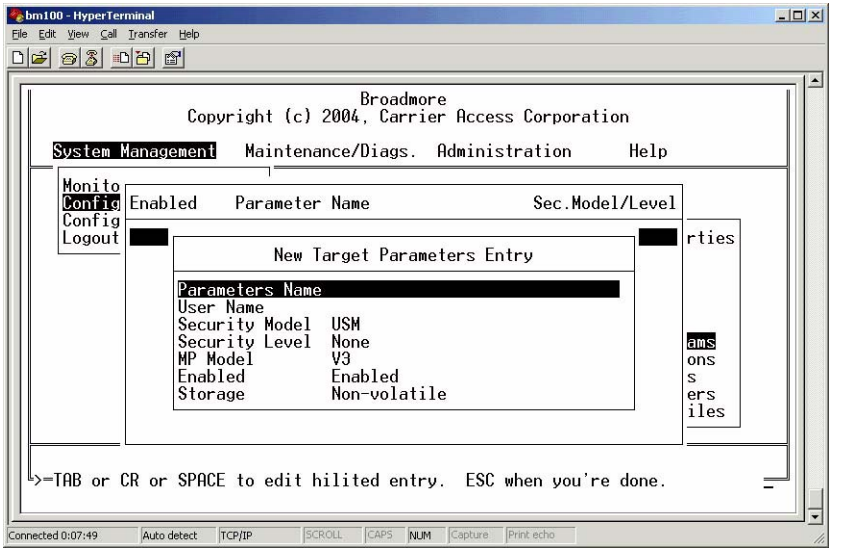
With each selection, confirm your changes and press *Esc* to exit.

The following table describes the selections.

Item	Options	Comments
Target Name		A unique value for Target Name, 1 to 30 characters.
IP Address		Format xxx.xxx.xxx.xxx where xxx is a decimal number, 0 to 255
Port		A number between 1024 and 65535.
Parameter Name		A unique value for Parameter Name, 1 to 30 characters.
Trap Tag		A unique value for Trap Tag, 1 to 30 characters.
Enabled	Enabled Disabled	Enables/disables this target.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings can be changed. Settings can not be deleted. Settings can not be changed.
Timeout (secs*100)		Enter Timeout as a value in hundredths of a second. For example, 1500 represents 15 seconds. This value indicates the expected maximum round trip time for communicating with the IP address defined by this target. When a message is sent to this address, and a response (if one is expected) is not received within this time period, it may assumed that the response will not be delivered.
Retries		The number of times to attempt sending the notification.
Max Message Size		TBD

Target Parameters

This selection allows you to enter the kind of protocol and security to be used for the target destinations. The screen below shows the New Target Parameters Entry.



Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Target Params** ↵
Select one of the following:

* **Edit** ↵, edit the Target Parameter information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing Target Parameter
* **New** ↵, enter a new Target Parameter
* **Validate Table** ↵, check table entries for consistency

With each selection, confirm your changes and press *Esc* to exit.

NOTE: Selecting **Validate Table** will check that each Parameter Name in this table exists in the Notify Profiles table.

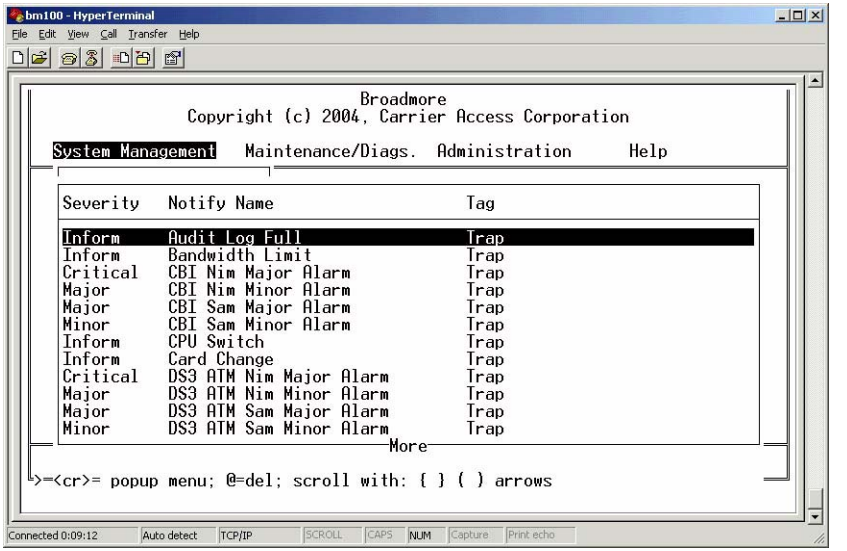
The following table describes the selections.

Item	Options	Comments
Parameter Name	string	Value for Parameter Name, 1 to 30 characters. **Note:** This entry must agree with the Parameters Name in the Notify Profiles table (see *"Notify Profiles" on page 12-43*).
User Name	string	Value for User Name, 1 to 30 characters.
Security Model	V1 V2 USM	The Security Model used in processing an SNMP query from a client. This parameter can be used to restrict access to the managed objects based on the security model set for a group.
Security Level	None AuthnoPriv AuthPriv	None – the incoming request requires no authentication or encryption. AuthnoPriv – authentication is required but SNMP messages will not be encrypted. AuthPriv – authentication is required and SNMP messages are encrypted.
MP Model	V1 V2 V3	Message Processing model
Enabled	Enabled Disabled	Enables/disables this target parameter.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings can be changed. Settings can not be deleted. Settings can not be changed.

Notifications

This selection displays a list of all available trap events that can be used for notifications. These entries automatically appear in the MIB "snmpNotifyTable" accessible by a remote manager. The notification names cannot be changed but individual traps can be enabled and assigned a tag name and a severity level. The severity is reported as a variable with the trap, and also works in concert with the squelch level (in SNMP Properties) to keep traps appropriately throttled.

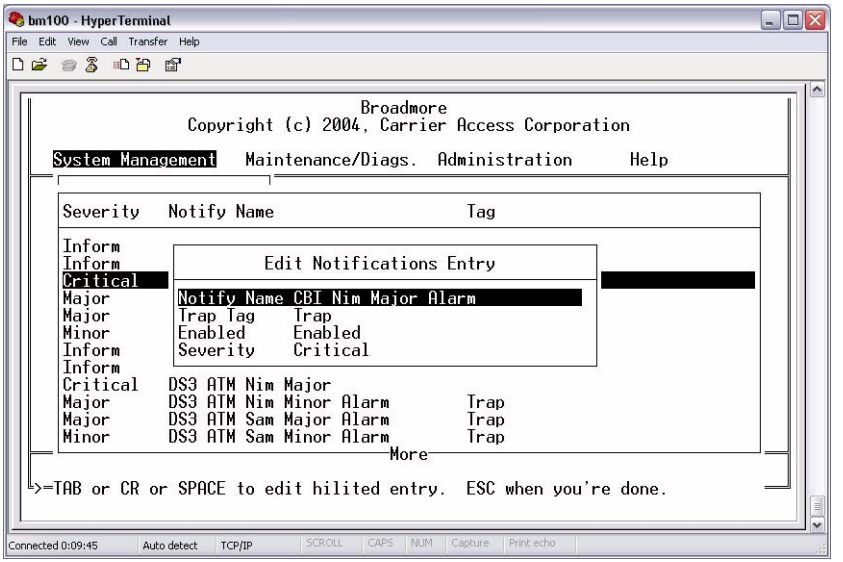
Follow the sequence below to enable or disable each trap type in the list. Save the configuration when asked to activate the SNMP trap reporting.


bm100 - HyperTerminal
File Edit View Call Transfer Help
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help
Severity Notify Name Tag
Inform Audit Log Full Trap
Inform Bandwidth Limit Trap
Critical CBI Nim Major Alarm Trap
Major CBI Nim Minor Alarm Trap
Major CBI Sam Major Alarm Trap
Minor CBI Sam Minor Alarm Trap
Inform CPU Switch Trap
Inform Card Change Trap
Critical DS3 ATM Nim Major Alarm Trap
Major DS3 ATM Nim Minor Alarm Trap
Major DS3 ATM Sam Major Alarm Trap
Minor DS3 ATM Sam Minor Alarm Trap
More
>=<cr>= popup menu; @=del; scroll with: { } ( ) arrows
Connected 0:09:12 Auto detect TCP/IP SCROLL CAPS NUM Capture Print echo

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Properties** ↵
Select **Notifications** ↵

NOTE: Entries in this table cannot be added or deleted, only edited. The storage type for these entries is automatically configured to “permanent”.

The screen below shows the Edit Notifications Entry.



The following table describes the selections

Item	Options	Comments
Notify Name		This parameter cannot be changed.
Trap Tag	string	The default name is “Trap”. If remote managers are to receive all traps, there is no need to modify the Trap Tag. Different tag names would be useful if responsibility for trap management was subdivided by trap types such that a specific destination was only responsible for a partial set of traps.
Enabled	Enabled Disabled	Enables/disables this notification.

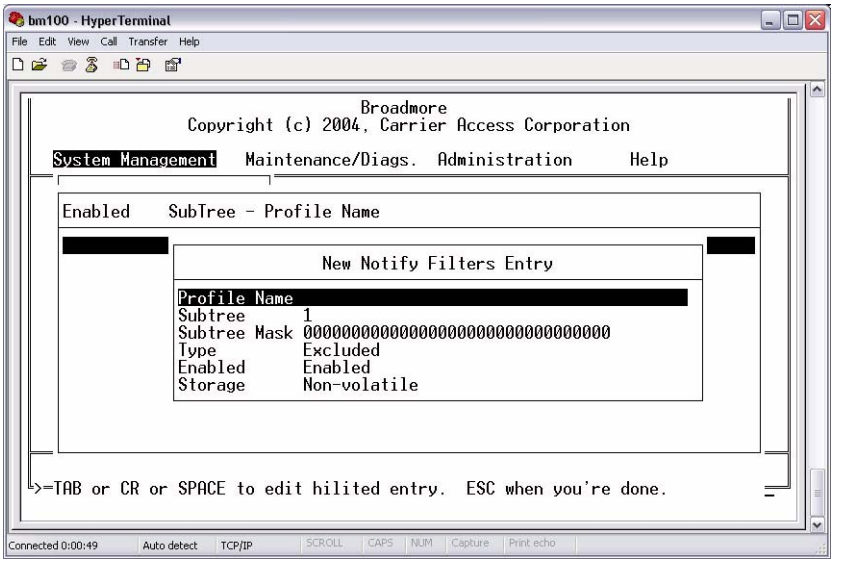
Item	Options	Comments
Severity	Critical Major Minor Inform	Reboot failure, NIM major alarm. SAM major alarm, slot failure, Uni up/down. SAM minor alarm, NIM minor alarm. Card insert/removed, NIM switchover, CPU switchover, restore, user reboot.

NOTE: "Severity" is an attribute only available for Broadmore enterprise traps; it is not described in the RFCs. Severity is reported as a variable with the trap. To configure trap severity from a remote SNMP manager, use the Broadmore enterprise MIB table "snmpTrapTypeTable".

Notify Filters

Entries may be completely maintained using the cammi Notify Filters table (which is a direct representation of "snmpNotifyFilterTable") or via a remote SNMP manager. This table allows finer grained control over trap reports. Broadmore permits up to 20 entries in this table. An empty table is acceptable.

These settings can only be accessed and changed by a SuperUser.



Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Notify Filters** ↵
Select one of the following:

* **Edit** ↵, edit the Filter information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing Filter
* **New** ↵, enter a new Filter
* **Validate Table** ↵, check table entries for consistency

With each selection, confirm your changes and press *Esc* to exit.

NOTE: Selecting **Validate Table** will check that each Profile Name in this table exists in the Notify Profiles table.

The Notify Filters table is used to avoid sending traps for specific mib variables. Each variable in the VarBindList is checked against the Subtree. If a match occurs, the trap is not sent.

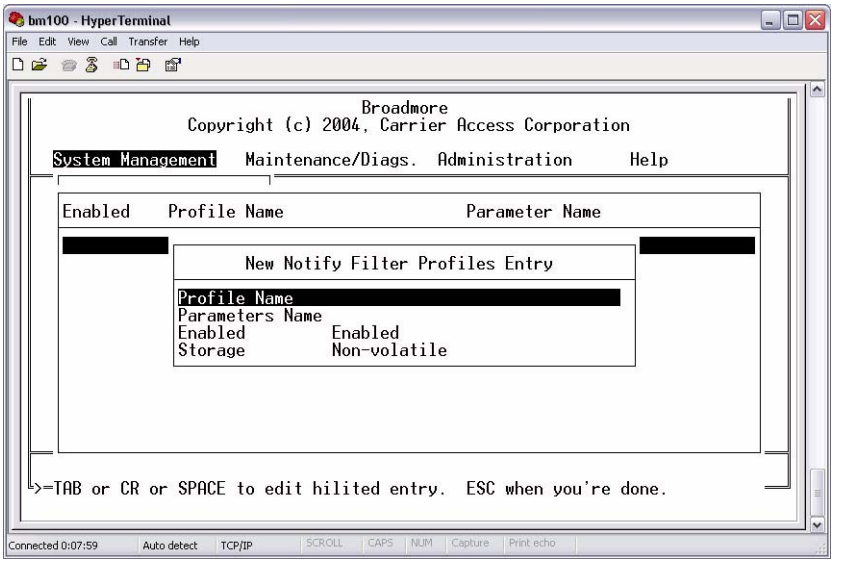
The following table describes the selections.

Item	Options	Comments
Profile Name	string	Value for Profile Name, 1 to 30 characters. **Note:** This entry must agree with the Profle Name in the Notify Profiles table (see *"Notify Profiles" on page 12-43*).
Subtree	bit string	The MIB tree address indicating the branch nodes to be filtered.
Subtree Mask	bit string	The "mask" is a bit string where a "1" ignores the corresponding OID bit and a "0" indicates a wild card match for the corresponding OID bit. A string of all "1" bits will accept any OID.
Type	Included Excluded	Must be same as in Notify table.
Enabled	Enabled Disabled	Enables/disables this filter.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings can be changed. Settings can not be deleted. Settings can not be changed.

Notify Profiles

Entries may be completely maintained using the cammi Notification Profiles table (which is a direct representation of “snmpNotifyFilterProfileTable”) or via a remote SNMP manager. This table allows finer grained control over trap reports. Broadmore permits up to 20 entries in this table. An empty table is acceptable.

These settings can only be accessed and changed by a SuperUser.


bm100 - HyperTerminal
File Edit View Call Transfer Help
Broadmore
Copyright (c) 2004, Carrier Access Corporation
System Management Maintenance/Diags. Administration Help
Enabled Profile Name Parameter Name
New Notify Filter Profiles Entry
Profile Name
Parameters Name
Enabled Enabled
Storage Non-volatile
>=TAB or CR or SPACE to edit hilited entry. ESC when you're done.
Connected 0:07:59 Auto detect TCP/IP SCROLL CAPS NUM Capture Print echo

Select **System Management** ↵
Select **Configuration** ↵
Select **System Services** ↵
Select **SNMP Configure** ↵
Select **Notify Profiles** ↵
Select one of the following:

* **Edit** ↵, edit the Profile information
* **Copy** ↵, (to put a copy of the information into the list)
* **Delete** ↵, delete an existing Profile
* **New** ↵, enter a new Profile
* **Validate Table** ↵, check table entries for consistency

With each selection, confirm your changes and press *Esc* to exit.

NOTE: Selecting **Validate Table** will check that each Parameter Name in this table exists in the Target Parameters table.

The only reason for this table is to allow more than one notify filter with the same Profile Name and different subtree, so that "profile_name" + "subtree" is the key into the Notify Filter table. When a "profile_name" is found in the Notify Profiles table, the Notify Filter table is searched for all entries having the same "profile_name".

The following table describes the selections.

Item	Options	Comments
Profile Name	string	Value for Profile Name, 1 to 30 characters.
Parameter Name	string	Value for Parameter Name, 1 to 30 characters. **Note:** This entry must agree with the Parameters Name in the Target Parameters table (see *"Target Parameters" on page 12-36*).
Enabled	Enabled Disabled	Enables/disables this profile.
Storage	Volatile Non-volatile Permanent Readonly Other	Settings lost without power. Settings remembered after reboot. Settings can not be deleted. Settings can not be changed.

APPENDIX A

Technical Specifications

In this Appendix:

- Broadmore 1700 Platform ... *A-2*
- Broadmore Modules ... *A-6*

Broadmore 1700 Platform

System Architecture

- Mid-plane architecture
- Internal Stratum 3E clock with dual BITS clock inputs
- Redundant CPUs, NIMs, backplane, power supplies
- DS3, OC-12c/STM-4c or OC-3c/STM-1c ATM network interfaces
- Up to 80 T1s, 60 E1s, 11 DS3s, or 12 E3s per chassis

Management

- RS-232/V.24 async craft port
- SNMP v1, v2 & v3 (RFC 2574, 2575, RFC 1213.MIB, RFC 2571-76.MIB)
- IPv4 and IPv6 ready Controller
- In-band management:

  LAN Emulation Client

  CLIP (RFC-1577)
- Out-of-band management:

  10Base-T Ethernet port

  RS-232/V.24 async craft port
- Management interface:

  Command Line Interface (CLI)

  Text-based menu-driven
- Optional security features:

  FIPS 140-2 approved Secure Shell (SSH) v2.0

  Common Criteria validated CCEVS-VR-06-0032

  RSA SecurID® User Client v5.0.2

Network Standards

- ATM Forum compliant AAL1 and AAL5 QoS
- ATM Forum compliant SVCs and PVCs
- ATM Forum Circuit Emulation Service v2.0 (CES)
- ITU-T and ANSI compliant UNI 3.0, 3.1, and 4.0 Signaling
- ATM Forum compliant ILMI 4.0
- Network Timing Protocol Client per RFC 1305

Redundancy

- System Level:

  Backplane: redundant segment protection

  Dual Power -48 VDC power rails to each card

- Interface Level:

  CPUs: 1:1

  NIMs: 1+1 SONET APS per Telcordia™ GR-253-CORE with Digital Protection Switching

  Dual BITS clock inputs with internal Stratum 3E holdover clock

Alarms

- Dry contacts for major and minor alarms
- LEDs indicating major and minor alarms
- User-defined alarm configuration
- SNMP trap generation for user-defined alarms

Testing & Diagnostics

- Network loop-backs (structured DS3: DS3 port; unstructured DS3, E3 port; structured DS1, E1)
- Service loop-backs (structured DS3: DS3 port, tributary and DS0; unstructured DS3, E3: port; structured DS1)
- Internal BERT generation and monitoring (structured DS3, unstructured DS3, E3)
- FEAC loop-back generation and detection

Power

- –48 VDC dual inputs, labeled A and B
- 240 W maximum for fully populated system

  10 watts per NIM

  8 watts per SAM

  20 watts per CPU
- Fused at 7.5 A, each input
- Alarm power module, 1 slot

  Over-voltage threshold: 58 ± 1.5 VDC
  Under-voltage threshold: 38 ± 0.72 VDC
- Optional Dual Redundant AC Power Supply, external

Regulatory Approvals

- FCC Part 15, Class A radiated emissions
- ANSI/UL 60950, CSA-C22.2 NO. 60950-00
- FIPS-140-2 Validated
- Joint Interoperability Test Command (JITC) Certified
- DISA Information Assurance (IA) Tested
- FIPS 140-2, Level 1 validated, Certificate 478

Physical

- 17-slot chassis
- Card slots: 1 to 12 SAMs, 1 or 2 NIMs, 1 or 2 CPUs, 1 alarm power module
- Rack mountable in 19 in (48.26 cm) or 23 in (58.42 cm) racks
- Dimensions:

  17.5 in (H) x 17.25 in (W) x 15.3 in (D)

  44.45 cm (H) x 43.82 cm (W) x 38.86 cm (D)
- Weight: 31 lb. (14.1 kg) empty, 48 lbs (21.8 kg) fully loaded

Environment

- Operating temperature range: 50 °F to 122 °F (10 °C to 50 °C)
- Storage temperature range: –4 °F to 158 °F (–20 °C to 70 °C)
- Relative humidity (non-condensing) range: 5% to 80%

Broadmore Modules

OC-12c and OC-3c Network Interface Modules (NIMs)

- SONET/ SDH OC-12c/ STM-4c 622.08 Mbps: network synchronization
- SONET/ SDH OC-3c/ STM-1c, 155.52 Mbps: network synchronization
- Single mode and multi-mode options
- Optical connectors type: SC
- Premise Reach:

  Type - multi-mode

  Wavelength - 1300 nm

  Tx Output power - greater than or equal to –18.0 dBm

  Rx sensitivity - less than or equal to –28.0 dBm
- Intermediate Reach:

  Type - single-mode

  Wavelength - 1300nm

  Tx Output power - greater than or equal to –11.0 dBm

  Rx sensitivity- less than or equal to –28.0 dBm

ATM DS3 NIM/SAM

- Supports both SVCs and PVCs
- 44.736 Mbps, B3ZS code
- Pulse Shape per IAW ANSI T1.404-1994
- Loop or Internal timing selectable with external BITS option (NIM only)
- ATM Direct Cell Mapping to DS3 per ITU-T Recommendation G.804
- DS3 PLCP per ANSI TA-TSY-000772 and TA-TSY-000773
- BNC connector access on rear panel IOM

DS1 (T1) or E1 Circuit Emulation Service Access Module (SAM)

- 8 ports per card
- RJ-48 connectors on rear panel IOM
- CES Version 2 (AAL1) and ITU-T recommendation I.363:

  Structured (Nx64): T1 (1 to 24 ATM PVCs or SVCs per port),
  E1 (1 to 31 ATM PVCs or SVCs per port)

  Un-structured: T1 or E1
- Clocking: Network, BITS, Service port, Adaptive, SRTS (unstructured), Loop

NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.

DS3 (T3) Structured Circuit Emulation SAM

- 1 port per card
- BNC connector access on rear panel IOM
- CES Version 2 (AAL1) and ITU-T recommendation I.363:

  Structured (N x 64) DS3: (1 to 672 ATM PVCs or SVCs per port)
- DS3 options: C Bit parity, M13
- Clocking: Network, BITS, Adaptive, SRTS, Loop

NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.

DS3 Unstructured Circuit Emulation SAM

- 3 ports per card
- BNC connector access on rear panel IOM
- Unstructured CES Version 2 (AAL1) and ITU-T recommendation I.363
- DS3 options: C Bit parity, clear channel
- Clocking: Network, BITS, Adaptive, SRTS, Loop

NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.

E3 Unstructured Circuit Emulation SAM

- 3 ports per card
- BNC connector access on rear panel IOM
- Unstructured CES Version 2 (AAL1) and ITU-T recommendation I.363
- E3 options: clear channel
- Clocking: Network, BITS, Adaptive, SRTS, Loop

NOTE: SRTS is a proprietary timing algorithm and may ONLY be used with specific written prior permission from Carrier Access Corporation. Additional license fees may apply.

High Bit Rate Serial SAM

- 4 full-duplex, programmable serial ports per card
- RJ-48 connectors on rear panel IOM
- Provisionable from 75 bps to 24 Mbps synchronous in 1 Bit increments
- Serial electrical interface protocols: RS-422
- Unstructured AAL1 circuit emulation per ATM forum low speed
- CES specification

Multiple Bit Rate Serial SAM

- 4 full-duplex, user programmable serial ports per card
- HDB-26 Connectors on rear panel IOM
- Provisionable from 75 bps to 10 Mbps synchronous in 1 Bit increments
- Serial electrical interface protocols: RS-422, RS-232, RS-530, RS-449
- Unstructured AAL1 circuit emulation per ATM forum low speed
- CES specification

High Speed SAM

- Programmable input and output serial ports per card
- Combo-D Connectors on rear panel IOM
- Provisionable 137 Mbps, 47 Mbps, 10 Mbps, 200 kbps rates
- Composite 247 Mbps with 137 Mbps I and Q signal rates
- Serial electrical interface protocols: LVDS

RS-530 Cell Bearing Interface NIM/SAM

- Provides ATM cell admission & aggregation over an RS-530 interface
- Connectors on rear panel IOM
- Operates as either a Network Interface Module (NIM) or a Service Access Module (SAM)
- Supports Crypto Re-Sync
- Provisionable from 56 kbps to 16.384 Mbps in 1 kbps increments

HSSI Cell Bearing Interface NIM/SAM

- Provides ATM cell admission & aggregation over a HSSI interface
- Connectors on rear panel IOM
- Operates as either a Network Interface Module (NIM) or a Service Access Module (SAM)
- Supports Crypto Re-Sync
- Provisionable from 56 kbps to 52 Mbps in 1 kbps increments

Serial Mux SAM

- Provides Clock & Data multiplexing of up to 8 serial links over ATM networks
- 8 programmable serial input ports per card
- 4 DB26 connectors on rear panel IOM
- Universal RS-232, RS-422, RS-449, RS-530, or V.35 interfaces
- 500 kbps to 3 Mbps synchronous data rates
- Unstructured AAL1 circuit emulation per ATM forum low-speed CES specification

Serial De-Mux SAM

- Provides Clock & Data de-multiplexing of up to 8 serial links over ATM networks
- 8 programmable serial output ports per card
- 4 DB26 connectors on rear panel IOM
- Universal RS-232, RS-422, RS-449, RS-530, or V.35 interfaces
- 500 kbps to 3 Mbps synchronous data rates
- Unstructured AAL1 circuit emulation per ATM forum low-speed CES specification

APPENDIX B

Spare Parts List

The most common spare parts are listed below. The fan filters and fuse/fuse cover assemblies may be ordered from Carrier Access Corporation or directly from the manufacturer. The manufacturer's name and part numbers are provided for these items.

Contact your local Sales Account Manager for the latest availability and pricing information. Please have your system model and serial number available when calling to facilitate service. In the unlikely event that a part not listed above is required, the Customer Support Center will provide detailed information on replacing the component.

P/N	Description	Page
7660-004	Module, DS1 SAM, CE, N×64, 8 Port	
7660-005	Module, E1 SAM, CE, N×64, 8 Port	
7660-008	Module, Structured MBR SAM	
7660-009	Module, Structured HBR SAM	
7660-017	Module, Synchronous CBI NIM/SAM	
7660-018	Module, ATM DS3 NIM/SAM, 1 Port	
7660-019	Module, ATM DS3 IOM	
7660-020	Module, Synchronous HSSI-CBI NIM/SAM	
7660-022	Fan Tray Assembly	
7660-023	Module, Alarm/Power (APM)	
7660-034	Module, Unstructured DS3 SAM, CE, 3-Port	
7660-040	Module, Serial Multiplexer SAM	
7660-041	Module, Serial Demultiplexer SAM	
7660-042	Module, High Speed SAM	
7660-045	Module, Unstructured E3 SAM, 3 Port	
7660-110	Module, OC-12/STM-4, NWK INTFC, IR, FC	
7660-114	Module, OC-12/STM-4, IR, SC	
7660-116	Module, OC-3/STM-1c, SM, IR, SC	
7660-117	Module, OC-3/STM-1c, MM, IR, SC	
7660-206	Module, CPU with FIPS, Ethernet and SAR	
7660-403	Module, DS3 SAM IOM, 3-Port	
7660-404	Module, DS1 SAM IOM, 8 RJ48 Connectors	
7660-406	Module, NIM IOM	
7660-408	Module, MBR SAM IOM	
7660-411	Module, CPU IOM	
7660-416	Module, Structured DS3 SAM IOM, 1 BNC Port	

7660-417	Module, Synchronous CBI NIM/SAM IOM	
7660-418	Module, HBR SAM IOM	
7660-420	Module, Synchronous HSSI-CBI NIM/SAM IOM	
7660-442	Module, High Speed SAM IOM	
7660-443	Card, HSAM Bridge	
7660-501	Cable Adapter, RS-449 DB37 DCE (5 ft.)	E 2
7660-502	Cable Adapter, EIA-530 DB25 DCE (5 ft.)	E 4
7660-503	Cable Adapter, V.35 (5 ft.)	E 6
7660-504	Cable Adapter, RS-232 DB25 DCE (5 ft.)	E 8
7660-511	Cable Adapter, RS-449 DB37 DTE (5 ft.)	E 10
7660-512	Cable Adapter, EIA-530 DB25 DTE (5 ft.)	E 12
7660-513	Cable Adapter, V.35/V.36 DTE (5 ft.)	E 14
7660-514	Cable Adapter, RS-232 DB25 DTE (5 ft.)	E 16
7660-520	CBI Cable Adapter, RS-530 DB25 (5 ft.) M-M	E 18
7660-521	CBI Cable Adapter, RS-530 DB25 (5 ft.) M-F	E 18
7660-522	CBI Cable Adapter, RS-530 DB25 (5 ft.) F-F	E 18
7660-523	Cable, HSSI-CBI HDB50 (10 ft.) M-M	E 20
7660-524	Cable Adapter, HBR, RJ45 to DB25 DCE	E 21
7660-672	Module, Structured DS3 SAM	
51670066-01	LapLink Cable, PC to Broadmore 1700 serial port cable with DB9-F and DB25-F connectors on both ends	
034-0016	CPU-2 Replacement Battery, Panasonic VL1220-1HF or equivalent	
Bussman #GMT7.5	Bussman 7.5 Amp Fuse	
Bussman #GMT-X	Bussman Fuse Cover	

Globe Motors #FFM745	Globe Motors Fan Filter

NOTE: Cables and adapters used for connection to MBR, HBR, CBI, and HSSI-CBI SAM IOM cards must be built to the cable specifications in Appendix E.

APPENDIX C

Software Error Messages

In this Appendix:

- Overview
- System Errors
- Setup Errors

Overview

Error messages are displayed for a number of reasons. In many cases an error message is the result of normal operation and no operator action is required. The messages shown below are divided into two groups: SYSTEM ERRORS and SETUP ERRORS. Typically, the SETUP ERRORS are configuration problems which the user can correct through normal operations as noted in chapters three and four. SYSTEM ERRORS provide clues about system operation which are meant primarily for Carrier Access Customer Support analysis.

NOTE: System errors may be observed during normal operation. These errors may be an indication of events which are not necessarily a problem. Evaluate the Broadmore 1700 operation according to *"Maintenance and Troubleshooting" on page 8-1*, when in doubt.

```
/*
* Error codes used primarily by the Configuration Manger.
*
* These codes are in a range not used by pSOS. For codes not in
this
* list, see the pSOS manual.
*/
```

System Errors

The user cannot address these errors. Contact Carrier Access Customer Support.

```
MALLOC_FAILED              = 0x1000
   NULL_POINTER             = 0x1001
   NOT_A_NIM                = 0x1002
   NOT_A_SAM                = 0x1003
   BAD_ATMIFNUM             = 0x1004
      /* Illegal value for atmIfNum */
ENTRY_ZERO_NOT_RESERVED = 0x1005
      /* Connection table entry 0 must be
                          * reserved for use by error handling
                          * code. NO_ATM_IF_INDEX must = 0!
                         */
   LIKELY_MEMORY_LEAK       = 0x1006
   NONSENSICAL_STATE        = 0x1007
      /* "Impossible" state of affairs found */
   UNREACHABLE_CODE         = 0x1008
      /* Unreachable code reached! */
   OUTBOUND_MSG_TOO_LONG    = 0x1009
     /* Msg to a DSP is too long   */
   INBOUND_MSG_TOO_LONG     = 0x1010
     /* Msg from a DSP is too long */
   TRANSMIT_ERROR           = 0x1011
     /* Error transmitting data to a card */
   INVALID_QUEUE_NUMBER     = 0x1012
    /* Invalid message queue number */
```

Setup Errors

These errors can usually be corrected by the user.

```
BAD_OC3_INPUT_PARAMETER    = 0x1101
BAD_OC3_FRAME_TYPE         = 0x1102
BAD_OC3_CLOCK_MODE         = 0x1103
BAD_OC3_LASER_STATE        = 0x1104
BAD_OC3_SCAMBLE_CONTROL    = 0x1105
BAD_OC3_BIPFEBE_OPTION     = 0x1106
BAD_OC3_PLSCRAMBLE_OPTION = 0x1107
BAD_OC3_XTABLE_FORMAT      = 0x1108
BAD_OC3_RESET_OPTION       = 0x1109
BAD_OC3_LOOP_MODE          = 0x110a
BAD_OC3_ACTION_ID          = 0x110b

BAD_NX64_ACTION_ID         = 0x110c
BAD_NX64_INPUT_PARAMETER   = 0x110d
BAD_NX64_LINE_CODE_FORMAT = 0x110e
BAD_NX64_TIMING_SOURCE     = 0x110f
BAD_NX64_LOOPMODE          = 0x1110
BAD_NX64_LINE_LENGTH       = 0x1111
BAD_NX64_FRAME_TYPE        = 0x1112
BAD_NX64_TRANSMIT_ALARM    = 0x1113
BAD_NX64_DIAG_CONTROL      = 0x1114
BAD_NX64_SERVICE_TYPE      = 0x1115

INVALID_SLOT_NUMBER        = 0x1116
NULL_SDU_POINTER           = 0x1117
CONFIGURE_ITEM_QSEND_ERR   = 0x1118
CONFIGURE_ITEM_ERROR       = 0x1119
READ_NX64SAMFILE_ERROR     = 0x111a
READ_OC3NIMFILE_ERROR      = 0x111b
WRITE_NX64SAMFILE_ERROR    = 0x111c
```

```
WRITE_OC3NIMFILE_ERROR    = 0x111d
DATABASE_CREATION_ERROR   = 0x111e
WRITE_7665INIFILE_ERROR   = 0x111f
CONMAN_RETRIES_EXCEEDED   = 0x1120

SVCS_CARDTYPE_MISMATCH    = 0x1121

WRONG_RX_TX_DATALEN       = 0x1122
WRONG_LOAD_XTABLE_DATALEN = 0x1123
VPVC_NOT_TRANSLATABLE     = 0x1124
   /* VP/VC incompatible with the
          * current OC3 Translation Table
          * Address Format.
          */
NIM_NOT_FOUND             = 0x1125
CONNECTION_LIMIT_REACHED  = 0x1126
   /* Connection table is full */
CHANNELS_ALREADY_IN_USE   = 0x1127
       /*VC related errors */
SIG_LINK_NOT_READY        = 0x1128
INVALID_PORT_NUMBER       = 0x1129
INVALID_CHANNEL_NUMBER    = 0x112a
INVALID_CHANNEL_MAP       = 0x112b
INVALID_ATM_IFINDEX       = 0x112c
   /* Connection not found
        * or ifIndex was not as expected. */
CONNECTION_IN_USE         = 0x112d
    /* Connection in use; request not
          * allowed at this time.*/
INVALID_DSS                = 0x112e
INVALID_SDU_DATAWORD_LEN   = 0x112f
PVCS_CARDTYPE_MISMATCH     = 0x1130
    /* PVCs not supported for card type */
```

```
INVALID_INTERNAL_VCI      = 0x1131
INVALID_CALL_IDENT        = 0x1132
INVALID_CARD_TYPE         = 0x1133
CONFIG_DEFAULTS_USED      = 0x1134
INVALID_MESSAGE_TYPE      = 0x1135
UNKNOWN_ACTION_ID         = 0x1136
```

APPENDIX D

Sample Network with RFC 1577 Configuration

This Appendix provides a sample network configuration to explain how the Classic IP (CIP) over ATM functions. CIP provides the path for control of remote Broadmore 1700s from a master control station over the ATM network.The three possible configurations are shown in the figure on the next page. These are:

1. The master control station has Ethernet access to the Broadmore 1700 (Broadmore 1700 #1).
2. The master control station does not have Ethernet access to the Broadmore 1700 and the Broadmore 1700 does not have an Ethernet local control station (Broadmore 1700 # 2).
3. The master control station does not have Ethernet access to the Broadmore 1700 and the Broadmore 1700 has an Ethernet local control station (Broadmore 1700 #3).

The objective is for the master control station to be able to communicate with all three Broadmore 1700s. This is done by creating a subnet over the ATM. This subnet consists of the three Broadmore 1700s, each having a unique CIP Ethernet address as shown. Broadmore 1700 #1 has Ethernet connectivity with the master control station. The ARP server, which is the ATM switch or a suitable device on the ATM network, maintains the Logical IP Subnetwork (LIS) as explained in the CIP over ATM section of Chapter 7.

Ethernet traffic for Broadmore 1700 #1 goes through the gateway directly to Broadmore 1700 #1. Ethernet traffic for Broadmore 1700 #2 and Broadmore 1700 #3 is routed by the gateway to Broadmore 1700 #1. The server uses LIS data to convert the IP address to an ATM address; the traffic is then sent over the ATM to the destination Broadmore 1700 where it is interpreted and acted upon as required.

Broadmore 1700 #2 has no Ethernet connection and its gateway is set to null. This unit sends all Ethernet traffic over the ATM (to Broadmore 1700 #1) by default. Broadmore 1700 #1 then forwards the traffic to the gateway and ultimately to the appropriate Ethernet address.

Broadmore 1700 #3 has an Ethernet gateway (local control station in example diagram on next page). However, traffic to the master control station will not be delivered since the local control station has no Ethernet route to the master control station. A static route is added to Broadmore 1700 #3. This static route sends the response to all traffic from the master control station back to it via a static route using LIS conversion.

This use of CIP over ATM provides inband control of remote Broadmore 1700s. After initial setup, this configuration is essentially transparent to the master control station.

The master control station addresses:

Broadmore 1700 #1 by IP address,
Broadmore 1700 #2 by CIP address,
Broadmore 1700 #3 by CIP address.

Broadmore 1700 #3 addresses its local control station by IP address and the master control station via static route and Broadmore 1700 #1.

The master control station addresses
Broadmore 1700 #1 as IP 192.40.100.90,
Broadmore 1700 #2 as CIP 10.10.10.174, and
Broadmore 1700 #3 as CIP 10.10.10.63.

The local control station addresses Broadmore 1700 #3 as IP 192.50.200.63.

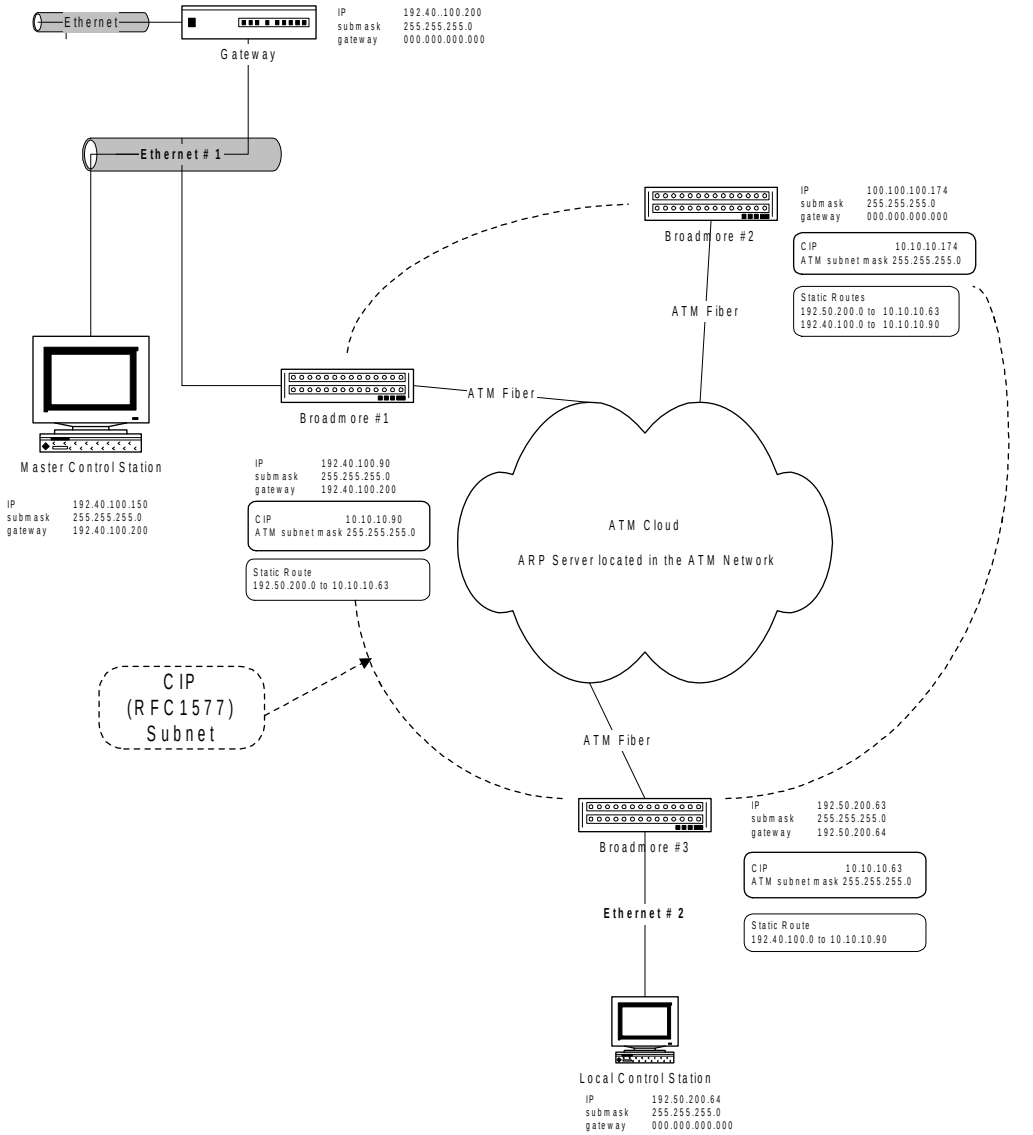
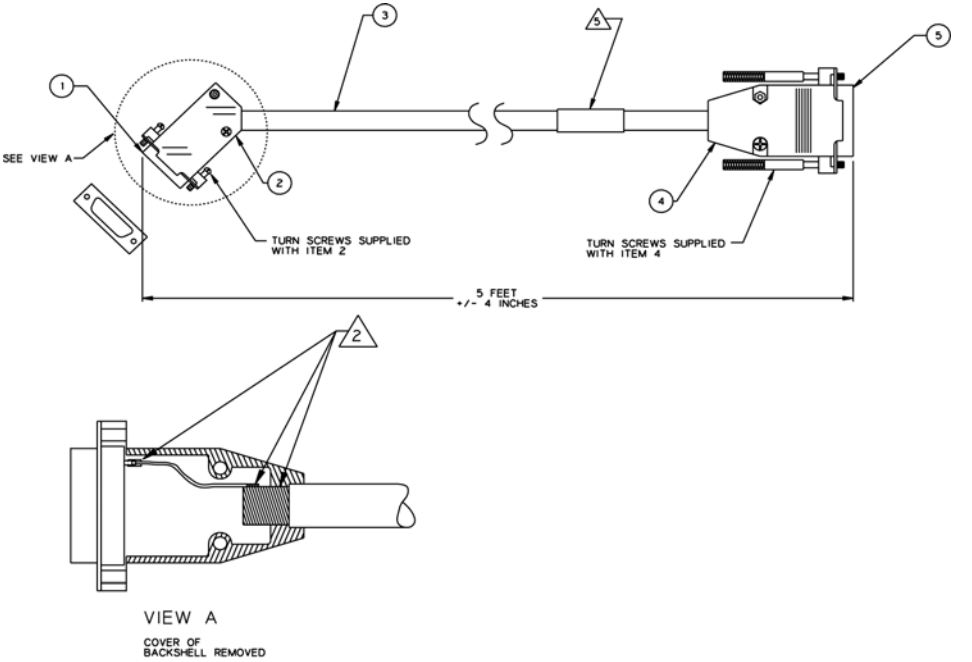


Figure D-1: Sample Network with RFC 1577 Classic IP (CIP) Over ATM

APPENDIX E

Cable Specifications

P/N	Description	Page
7660-501	Cable Adapter, RS-449 DB37 DCE (5 ft.)	E 2
7660-502	Cable Adapter, EIA-530 DB25 DCE (5 ft.)	E 4
7660-503	Cable Adapter, V.35 (5 ft.)	E 6
7660-504	Cable Adapter, RS-232 DB25 DCE (5 ft.)	E 8
7660-511	Cable Adapter, RS-449 DB37 DTE (5 ft.)	E 10
7660-512	Cable Adapter, EIA-530 DB25 DTE (5 ft.)	E 12
7660-513	Cable Adapter, V.35/V.36 DTE (5 ft.)	E 14
7660-514	Cable Adapter, RS-232 DB25 DTE (5 ft.)	E 16
7660-520	Cable Adapter, CBI, RS-530 DB25 (5 ft.) M-M	E 18
7660-521	Cable Adapter, CBI, RS-530 DB25 (5 ft.) M-F	E 18
7660-522	Cable Adapter, CBI, RS-530 DB25 (5 ft.) F-F	E 18
7660-523	Cable, HSSI-CBI, HDB50 (10 ft.) M-M	E 20
7660-524	Cable Adapter, HBR, RJ45 to DB25 DCE	E 21
7660-525	Cable Adapter, Mux, 4xHDB26 to DB37 DTE (6 ft.)	E 23
7660-526	Cable Adapter, Demux, 4xHDB26 to DB37 DCE (6 ft.)	E 25
	Connector Pinout, High Speed SAM (HSAM)	E 27



VIEW A

COVER OF BACKSHELL REMOVED

NOTES:

1. ITEMS 1 AND 5 MANUFACTURED BY: KYCON INC.
   1810 LITTLE ORCHARD ST.
   SAN JOSE, CA. 75125

   ITEM 4 MANUFACTURED BY: ADAM TECH
   909 RAHWAY AVE.
   UNION, NJ 07083

   ITEM 3 MANUFACTURED BY: BELDEN
   PO BOX 1980
   RICHMOND, IN 47375

   ITEM 2 MANUFACTURED BY: 3M ELECTRONIC PRODUCTS DIVISION
   6801 RIVER PLACE BLVD.
   AUSTIN, TX 78726

2. CABLE SHIELD BRAID TO BE CONNECTED TO BACKSHELL OF P1 AND P2 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 AND P2 CONNECTORS USING 24 AWG DRAIN WIRE.

3. ALL SOLDER JOINTS TO BE COVERED BY HEAT SHRINK.

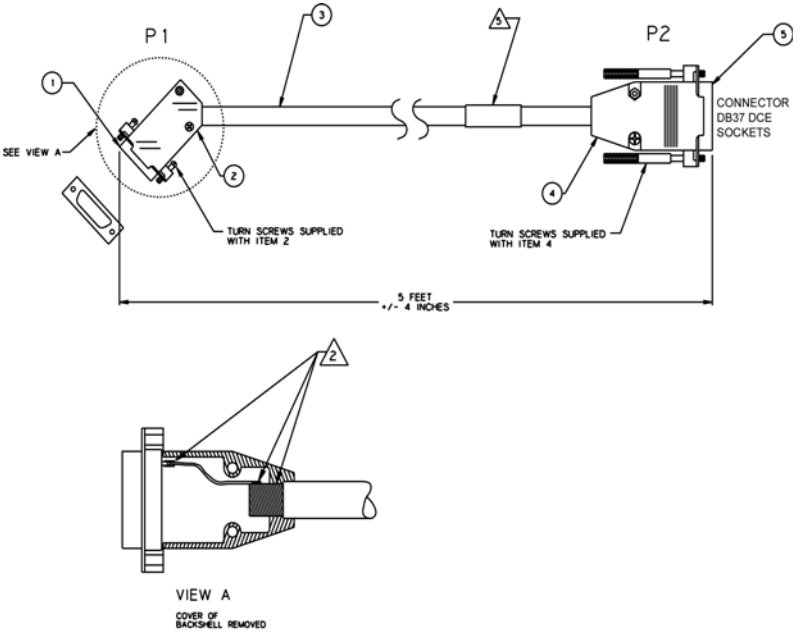
4. LOCKING COMPOUND TO BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.

5. MARK WITH "7660-501"
   "005-0080 REV *"
   * = CURRENT REVISION OF THIS DRAWING.
   WRAP AROUND STYLE LABEL ACCEPTABLE.

Figure E-2 Cable Adapter RS-449 DB37 DCE (P/N 7660-501)

Table E-1 Wire List for Cable Adapter RS-449 DB37 DCE (P/N 7660-501)

WIRE LIST			
FROM	TO	SIGNAL	COLOR
P1-1	P2-1	CABLE SHLD BRAID	DRAIN WIRE
P1-2	P2-6	RXD(A)	WHITE/ BLUE STRIPE
P1-3	P2-24	RXD(B)	BLUE/ WHITE STRIPE
P1-4	P2-17	SCTE(A)	WHITE/ ORN STRIPE
P1-5	P2-35	SCTE(B)	ORN/ WHITE STRIPE
P1-6	P2-11	DSR(A)	WHITE/ GRN STRIPE
P1-7	P2-29	DSR(B)	GRN/ WHITE STRIPE
P1-8	P2-7	RTS(A)	WHITE/ BRN STRIPE
P1-9	P2-25	RTS(B)	BRN/ WHITE STRIPE
P1-10	P2-8	RXC(A)	WHITE/ GREY STRIPE
P1-11	P2-26	RXC(B)	GREY/ WHITE STRIPE
P1-12	P2-4	TXD(A)	RED/ BLUE STRIPE
P1-13	P2-22	TXD(B)	BLUE/ RED STRIPE
P1-14	P2-13	DCD(A)	RED/ ORN STRIPE
P1-15	P2-31	DCD(B)	ORN/ RED STRIPE
P1-16	P2-10	LL	RED/ GRN STRIPE
P1-NC	P2- -	NC	GRN/ RED STRIPE
P1-18	P2-19	SIGNAL GND	RED/ BRN STRIPE
P1-NC	P2- -	NC	BRN/ RED STRIPE
P1-20	P2-5	TXC(A)	RED/ GREY STRIPE
P1-21	P2-23	TXC(B)	GREY/ RED STRIPE
P1-22	P2-9	CTS(A)	BLACK/ BLUE STRIPE
P1-23	P2-27	CTS(B)	BLUE BLACK STRIPE
P1-24	P2-12	DTR(A)	BLACK/ ORN STRIPE
P1-25	P2-30	DTR(B)	ORN/ BLACK STRIPE
P1-26	P2-15	RI	BLACK/ GRN STRIPE



NOTES:

1. ITEMS 1 AND 5 MANUFACTURED BY: KYCON INC.
   1810 LITTLE ORCHARD ST.
   SAN JOSE, CA. 75125
   ITEM 4 MANUFACTURED BY: ADAM TECH
   909 RAHWAY AVE.
   UNION, NJ 07083
   ITEM 3 MANUFACTURED BY: BELDEN
   PO BOX 1980
   RICHMOND, IN 47375
   ITEM 2 MANUFACTURED BY: 3M ELECTRONIC PRODUCTS DIVISION
   6801 RIVER PLACE BLVD.
   AUSTIN, TX 78726

2. CABLE SHIELD BRAID TO BE CONNECTED TO BACKSHELL OF P1 AND P2 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 AND P2 CONNECTORS USING 24 AWG DRAIN WIRE.

3. ALL SOLDER JOINTS TO BE COVERED BY HEAT SHRINK.

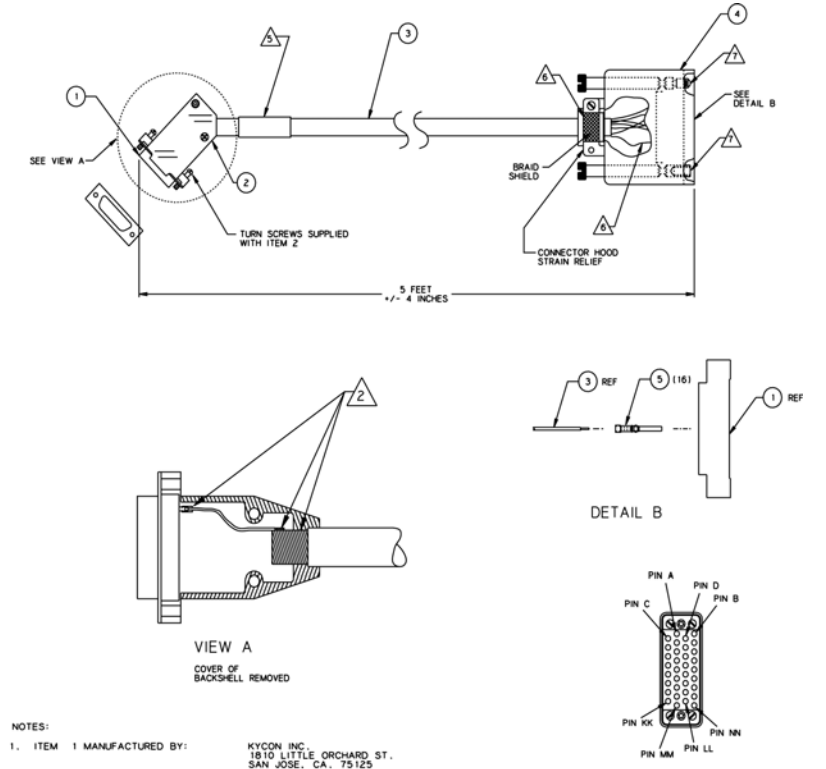
4. LOCKING COMPOUND TO BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.

5. MARK WITH "7660-502"
   "AND CABLE ASSEMBLY PART NUMBER"
   WRAP AROUND STYLE LABEL ACCEPTABLE.

Figure E-3 Cable Adapter EIA-530 Cable DB25 DCE (P/N 7660-502)

Table E-2 Wire List Cable Adapter EIA-530 Cable DB25 DCE (P/N 7660-502)

WIRE LIST			
FROM	TO	SIGNAL	COLOR
P1-1	P2-1	CABLE SHLD BRAID	DRAIN WIRE
P1-2	P2-3	RXD(A)	WHITE/ BLUE STRIPE
P1-3	P2-16	RXD(B)	BLUE/ WHITE STRIPE
P1-4	P2-24	SCTE(A)	WHITE/ ORN STRIPE
P1-5	P2-11	SCTE(B)	ORN/ WHITE STRIPE
P1-6	P2-6	DSR(A)	WHITE/ GRN STRIPE
P1-7	P2-22	DSR(B)	GRN/ WHITE STRIPE
P1-8	P2-4	RTS(A)	WHITE/ BRN STRIPE
P1-9	P2-19	RTS(B)	BRN/ WHITE STRIPE
P1-10	P2-17	RXC(A)	WHITE/ GREY STRIPE
P1-11	P2-9	RXC(B)	GREY/ WHITE STRIPE
P1-12	P2-2	TXD(A)	RED/ BLUE STRIPE
P1-13	P2-14	TXD(B)	BLUE/ RED STRIPE
P1-14	P2-8	DCD(A)	RED/ ORN STRIPE
P1-15	P2-10	DCD(B)	ORN/ RED STRIPE
P1-16	P2-18	LL	RED/ GRN STRIPE
P1-NC	P2- -	NC	GRN/ RED STRIPE
P1-18	P2-7	SIGNAL GND	RED/ BRN STRIPE
P1-NC	P2- -	NC	BRN/ RED STRIPE
P1-20	P2-15	TXC(A)	RED/ GREY STRIPE
P1-21	P2-12	TXC(B)	GREY/ RED STRIPE
P1-22	P2-5	CTS(A)	BLACK/ BLUE STRIPE
P1-23	P2-13	CTS(B)	BLUE BLACK STRIPE
P1-24	P2-20	DTR(A)	BLACK/ ORN STRIPE
P1-25	P2-23	DTR(B)	ORN/ BLACK STRIPE
P1-26	P2- -	RI	BLACK/ GRN STRIPE



NOTES:

1. ITEM 1 MANUFACTURED BY: KYCON INC. 1810 LITTLE ORCHARD ST. SAN JOSE, CA 75125
   ITEM 2 MANUFACTURED BY: 3M ELECTRONIC PRODUCTS DIVISION 6801 RIVER PLACE BLVD. AUSTIN, TX 78726
   ITEM 3 MANUFACTURED BY: BELDEN PO BOX 1980 RICHMOND, IN 47375
   ITEMS 4 AND 5 MANUFACTURED BY: POSITRONIC IND. INC. 423 NO. CAMPBELL ST. SPRINGFIELD, MO.

2. CABLE SHIELD BRAID TO BE CONNECTED TO BACKSHELL OF P1 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 CONNECTOR USING 24 AW DRAIN WIRE.

3. ALL SOLDER JOINTS TO BE COVERED BY HEAT SHRINK.

4. LOCKING COMPOUND TO BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.

5. MARK WITH "7660-503" "005-0071 REV *" * = CURRENT REVISION OF THIS DRAWING. WRAP AROUND STYLE LABEL ACCEPTABLE.

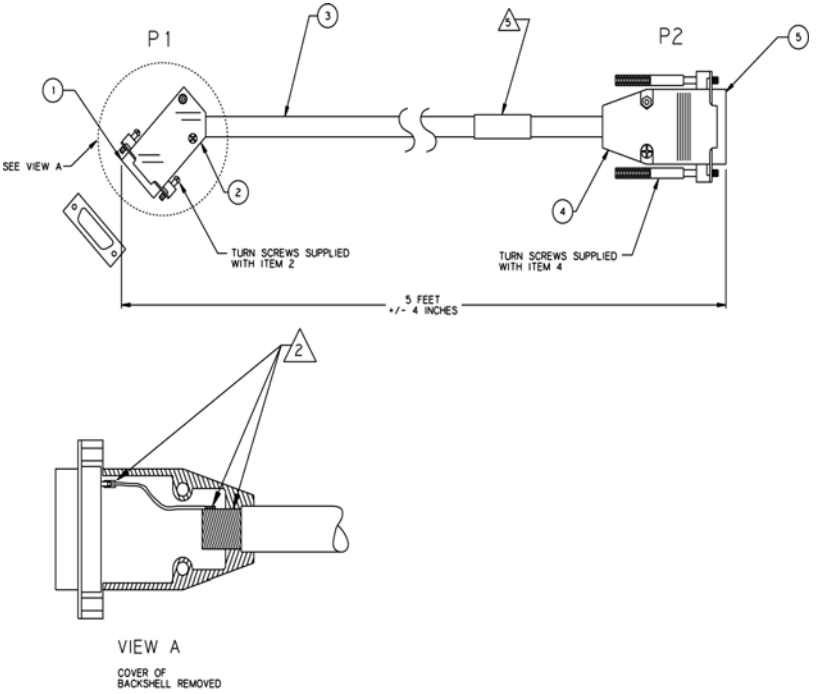
6. BRAID SHIELD TO BE FOLDED BACK APPROX 1/2 INCH OVER CABLE JACKET AND CLAMPED TO JACKET BY OUTER STRAIN RELIEF. SHIELD BRAID TO BE WIRED TO PIN A OF P2 CONNECTOR USING 24 AWG DRAIN WIRE.

7. MOUNT JACK SOCKET BELOW PIN "MM" AND JACKSCREW ABOVE PIN "A".

Figure E-4 Cable Adapter V.35 (P/N 7660-503)

Table E-3 Wire List for Cable Adapter V.35 (P/N 7660-503)

WIRE LIST			
FROM	TO	SIGNAL	COLOR
P1-1	P2-A	CABLE SHLD BRAID	DRAIN WIRE
P1-2	P2-R	RXD(A)	WHITE/ BLUE STRIPE
P1-3	P2-T	RXD(B)	BLUE/ WHITE STRIPE
P1-4	P2-U	SCTE(A)	WHITE/ ORN STRIPE
P1-5	P2-W	SCTE(B)	ORN/ WHITE STRIPE
P1-6	P2-E	DSR(A)	WHITE/ GRN STRIPE
P1-7	P2- -	NC	GRN/ WHITE STRIPE
P1-8	P2-C	RTS(A)	WHITE/ BRN STRIPE
P1-9	P2- -	NC	BRN/ WHITE STRIPE
P1-10	P2-V	RXC(A)	WHITE/ GREY STRIPE
P1-11	P2-X	RXC(B)	GREY/ WHITE STRIPE
P1-12	P2-P	TXD(A)	RED/ BLUE STRIPE
P1-13	P2-S	TXD(B)	BLUE/ RED STRIPE
P1-14	P2-F	DCD	RED/ ORN STRIPE
P1-15	P2- -	NC	ORN/ RED STRIPE
P1-16	P2-J	LL	RED/ GRN STRIPE
P1-NC	P2- -	NC	GRN/ RED STRIPE
P1-18	P2-B	SIGNAL GND	RED/ BRN STRIPE
P1-NC	P2- -	NC	BRN/ RED STRIPE
P1-20	P2-Y	TXC(A)	RED/ GREY STRIPE
P1-21	P2-AA	TXC(B)	GREY/ RED STRIPE
P1-22	P2-D	CTS(A)	BLACK/ BLUE STRIPE
P1-23	P2- -	NC	BLUE BLACK STRIPE
P1-24	P2-H	DTR(A)	BLACK/ ORN STRIPE
P1-25	P2- -	NC	ORN/ BLACK STRIPE
P1-26	P2- -	NC	BLACK/ GRN STRIPE



NOTES:

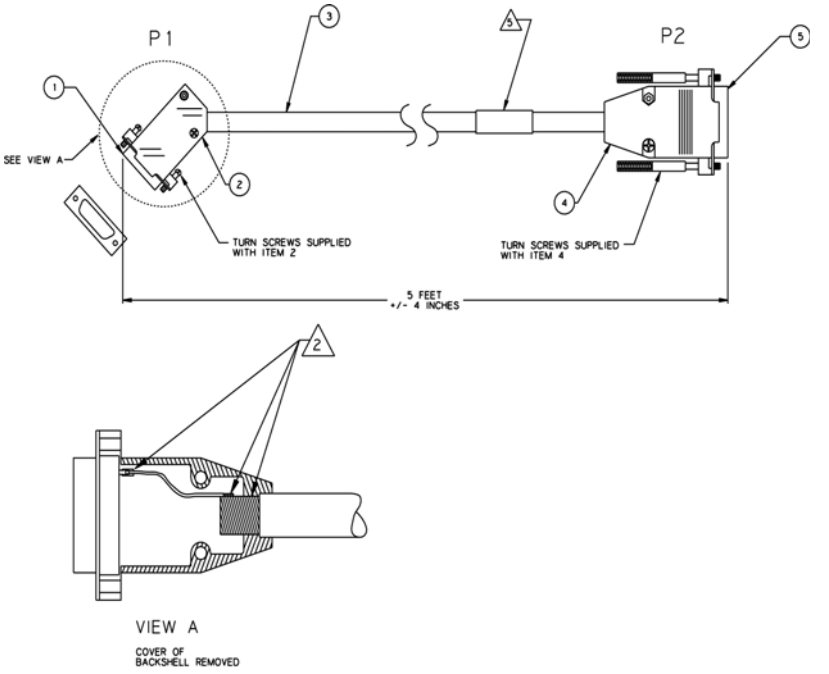
1. ITEMS 1 AND 5 MANUFACTURED BY: KYCON INC.
   1810 LITTLE ORCHARD ST.
   SAN JOSE, CA. 75125
   ITEM 4 MANUFACTURED BY: ADAM TECH
   909 RAHWAY AVE.
   UNION, NJ 07083
   ITEM 3 MANUFACTURED BY: BELDEN
   PO BOX 1980
   RICHMOND, IN 47375
   ITEM 2 MANUFACTURED BY: 3M ELECTRONIC PRODUCTS DIVISION
   6801 RIVER PLACE BLVD.
   AUSTIN, TX 78726

2. CABLE SHIELD BRAID TO BE CONNECTED TO BACKSHELL OF P1 AND P2 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 AND P2 CONNECTORS USING 24 AWG DRAIN WIRE.
3. ALL SOLDER JOINTS TO BE COVERED BY HEAT SHRINK.
4. LOCKING COMPOUND TO BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.
5. MARK WITH "7660-504" "005-0082 REV *"
   * = CURRENT REVISION OF THIS DRAWING.
   WRAP AROUND STYLE LABEL ACCEPTABLE.

Figure E-5 Cable Adapter RS-232 DB25 DCE (P/N 7660-504)

Table E-4 Wire List for Cable Adapter RS-232 DB25 DCE (P/N 7660-504)

WIRE LIST			
FROM	TO	SIGNAL	COLOR
P1-1	P2-1	CABLE SHLD BRAID	DRAIN WIRE
P1-2	P2-3	RXD	WHITE/ BLUE STRIPE
P1-3	P2- -	NC	BLUE/ WHITE STRIPE
P1-4	P2-24	SCTE	WHITE/ ORN STRIPE
P1-5	P2- -	NC	ORN/ WHITE STRIPE
P1-6	P2-6	DSR	WHITE/ GRN STRIPE
P1-7	P2- -	NC	GRN/ WHITE STRIPE
P1-8	P2-4	RTS	WHITE/ BRN STRIPE
P1-9	P2- -	NC	BRN/ WHITE STRIPE
P1-10	P2-17	RXC	WHITE/ GREY STRIPE
P1-11	P2- -	NC	GREY/ WHITE STRIPE
P1-12	P2-2	TXD	RED/ BLUE STRIPE
P1-13	P2- -	NC	BLUE/ RED STRIPE
P1-14	P2-8	DCD	RED/ ORN STRIPE
P1-15	P2- -	NC	ORN/ RED STRIPE
P1-16	P2-18	LL	RED/ GRN STRIPE
P1-NC	P2- -	NC	GRN/ RED STRIPE
P1-18	P2-7	SIGNAL GND	RED/ BRN STRIPE
P1-NC	P2- -	NC	BRN/ RED STRIPE
P1-20	P2-15	TXC	RED/ GREY STRIPE
P1-21	P2- -	NC	GREY/ RED STRIPE
P1-22	P2-5	CTS	BLACK/ BLUE STRIPE
P1-23	P2- -	NC	BLUE BLACK STRIPE
P1-24	P2-20	DTR	BLACK/ ORN STRIPE
P1-25	P2- -	NC	ORN/ BLACK STRIPE
P1-26	P2-22	RI	BLACK/ GRN STRIPE



NOTES:

1. ITEMS 1 AND 5 MANUFACTURED BY: KYCON INC.
   1810 LITTLE ORCHARD ST.
   SAN JOSE, CA. 75125
   ITEM 4 MANUFACTURED BY: ADAM TECH
   909 RAHWAY AVE.
   UNION, NJ 07083
   ITEM 3 MANUFACTURED BY: BELDEN
   PO BOX 1980
   RICHMOND, IN 47375
   ITEM 2 MANUFACTURED BY: 3M ELECTRONIC PRODUCTS DIVISION
   6801 RIVER PLACE BLVD.
   AUSTIN, TX 78726

2. CABLE SHIELD BRAID TO BE CONNECTED TO BACKSHELL OF P1 AND P2 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 AND P2 CONNECTORS USING 24 AWG DRAIN WIRE.

3. ALL SOLDER JOINTS TO BE COVERED BY HEAT SHRINK.

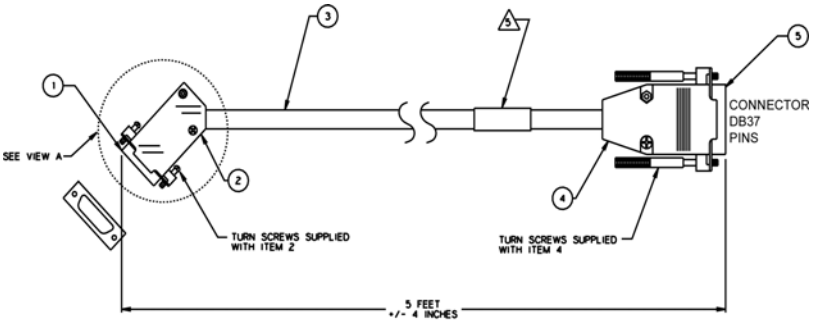
4. LOCKING COMPOUND TO BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.

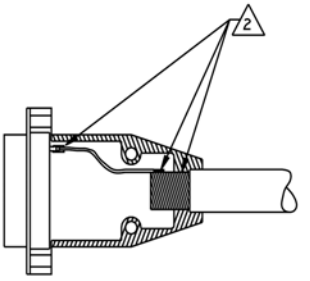
5. MARK WITH "7660-511" "005-0072 REV *"
   * = CURRENT REVISION OF THIS DRAWING.
   WRAP AROUND STYLE LABEL ACCEPTABLE.

Figure E-6 Cable Acapter RS-449 DB37 DTE (P/N 7660-511)

Table E-5 Wire List for Cable Adapter RS-449 DB37 DTE (P/N 7660-511)

WIRE LIST			
FROM	TO	SIGNAL	COLOR
P1-1	P2-1	CABLE SHLD BRAID	DRAIN WIRE
P1-2	P2-4	TXD(A)	WHITE/ BLUE STRIPE
P1-3	P2-22	TXD(B)	BLUE/ WHITE STRIPE
P1-4	P2-8	RXC(A)	WHITE/ ORN STRIPE
P1-5	P2-26	RXC(B)	ORN/ WHITE STRIPE
P1-6	P2-12	DTR(A)	WHITE/ GRN STRIPE
P1-7	P2-30	DTR(B)	GRN/ WHITE STRIPE
P1-8	P2-9	CTS(A)	WHITE/ BRN STRIPE
P1-9	P2-27	CTS(B)	BRN/ WHITE STRIPE
P1-10	P2-17	SCTE(A)	WHITE/ GREY STRIPE
P1-11	P2-35	SCTE(B)	GREY/ WHITE STRIPE
P1-12	P2-6	RXD(A)	RED/ BLUE STRIPE
P1-13	P2-24	RXD(B)	BLUE/ RED STRIPE
P1-14	P2-13	DCD(A)	RED/ ORN STRIPE
P1-15	P2-31	DCD(B)	ORN/ RED STRIPE
P1-16	P2-10	LL	RED/ GRN STRIPE
P1-17	P2-15	RI	GRN/ RED STRIPE
P1-18	P2-19	SIGNAL GND	RED/ BRN STRIPE
P1-19	P2-	NC	BRN/ RED STRIPE
P1-20	P2-5	TXC(A)	RED/ GREY STRIPE
P1-21	P2-23	TXC(B)	GREY/ RED STRIPE
P1-22	P2-7	RTS(A)	BLACK/ BLUE STRIPE
P1-23	P2-25	RTS(B)	BLUE BLACK STRIPE
P1-24	P2-11	DSR(A)	BLACK/ ORN STRIPE
P1-25	P2-29	DSR(B)	ORN/ BLACK STRIPE
P1-26	P2-	NC	BLACK/ GRN STRIPE





VIEW A

COVER OF BACKSHELL REMOVED

NOTES:

1. ITEMS 1 AND 5 MANUFACTURED BY: KYCON INC.
   1810 LITTLE ORCHARD ST.
   SAN JOSE, CA. 75125
   ITEM 4 MANUFACTURED BY: ADAM TECH
   909 RAHWAY AVE.
   UNION, NJ 07083
   ITEM 3 MANUFACTURED BY: BELDEN
   PO BOX 1980
   RICHMOND, IN 47375
   ITEM 2 MANUFACTURED BY: 3M ELECTRONIC PRODUCTS DIVISION
   6801 RIVER PLACE BLVD.
   AUSTIN, TX 78726

2. CABLE SHIELD BRAID TO BE CONNECTED TO BACKSHELL OF P1 AND P2 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 AND P2 CONNECTORS USING 24 AWG DRAIN WIRE.

3. ALL SOLDER JOINTS TO BE COVERED BY HEAT SHRINK.

4. LOCKING COMPOUND TO BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.

5. MARK WITH "7660-512" "005-0083 REV *"
   * = CURRENT REVISION OF THIS DRAWING.
   WRAP AROUND STYLE LABEL ACCEPTABLE.

Figure E-7 Cable Adapter EIA-530 DB25 DTE (P/N 7660-512)

Table E-6 Wire List for Cable Adapter EIA-530 DB25 DTE (P/N 7660-512)

WIRE LIST			
FROM	TO	SIGNAL	COLOR
P1-1	P2-1	CABLE SHLD BRAID	DRAIN WIRE
P1-2 P1-3	P2-2 P2-14	TXD(A) TXD(B)	WHITE/ BLUE STRIPE BLUE/ WHITE STRIPE
P1-4 P1-5	P2-17 P2-9	RXC(A) RXC(B)	WHITE/ ORN STRIPE ORN/ WHITE STRIPE
P1-6 P1-7	P2-20 P2-23	DTR(A) DTR(B)	WHITE/ GRN STRIPE GRN/ WHITE STRIPE
P1-8 P1-9	P2-5 P2-13	CTS(A) CTS(B)	WHITE/ BRN STRIPE BRN/ WHITE STRIPE
P1-10 P1-11	P2-24 P2-11	SCTE(A) SCTE(B)	WHITE/ GREY STRIPE GREY/ WHITE STRIPE
P1-12 P1-13	P2-3 P2-16	RXD(A) RXD(B)	RED/ BLUE STRIPE BLUE/ RED STRIPE
P1-14 P1-15	P2-8 P2-10	DCD(A) DCD(B)	RED/ ORN STRIPE ORN/ RED STRIPE
P1-16 P1-17	P2-18 P2-	LL NC	RED/ GRN STRIPE GRN/ RED STRIPE
P1-18 P1-19	P2-7 P2-	SIGNAL GND NC	RED/ BRN STRIPE BRN/ RED STRIPE
P1-20 P1-21	P2-15 P2-12	TXC(A) TXC(B)	RED/ GREY STRIPE GREY/ RED STRIPE
P1-22 P1-23	P2-4 P2-19	RTS(A) RTS(B)	BLACK/ BLUE STRIPE BLUE BLACK STRIPE
P1-24 P1-25	P2-6 P2-22	DSR(A) DSR(B)	BLACK/ ORN STRIPE ORN/ BLACK STRIPE
P1-26	P2-	NC	BLACK/ GRN STRIPE

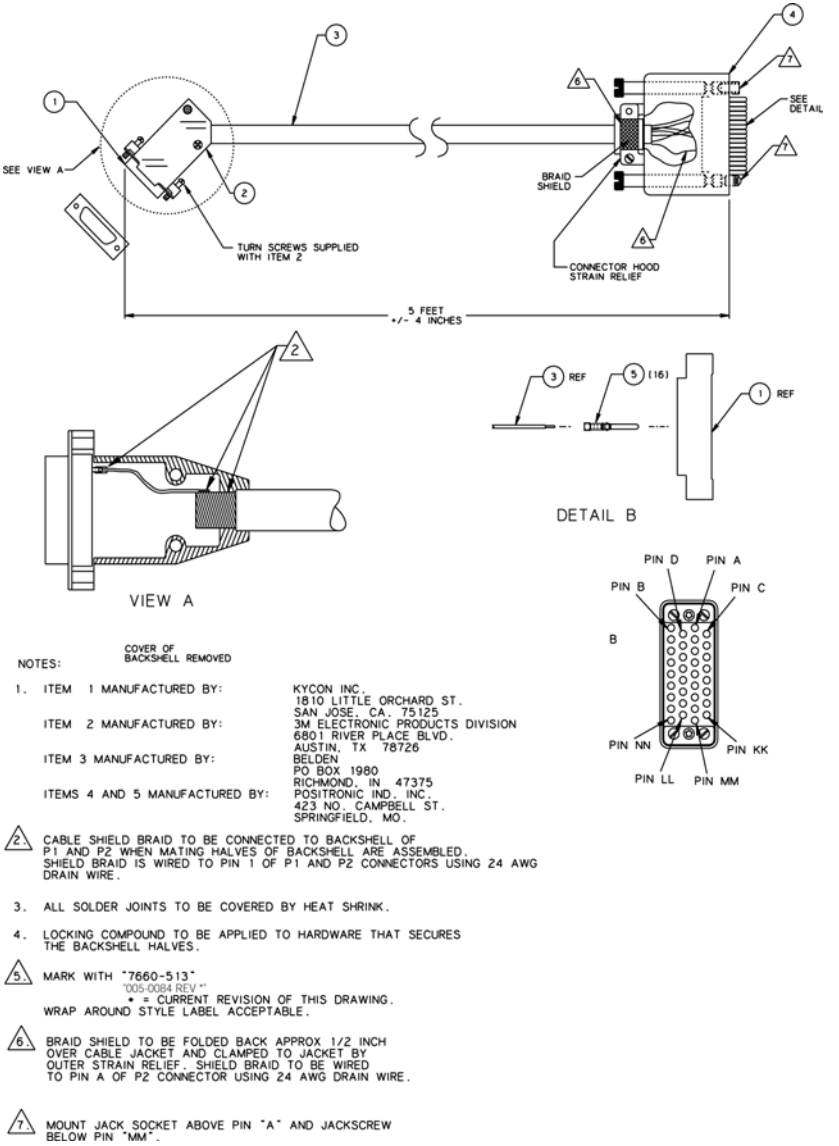
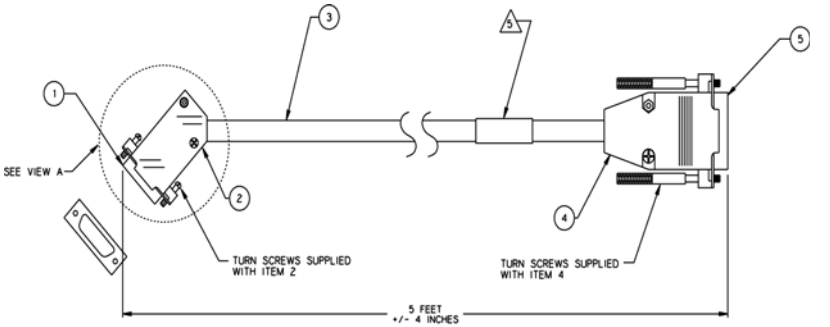
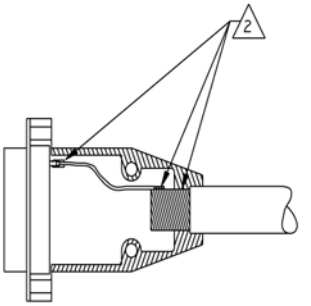

SEE VIEW A
TURN SCREWS SUPPLIED WITH ITEM 2
BRAID SHIELD
CONNECTOR HOOD STRAIN RELIEF
SEE DETAIL
5 FEET +/- 4 INCHES
VIEW A
DETAIL B
3 REF
5 (16)
1 REF
PIN D
PIN A
PIN B
PIN C
B
PIN NN
PIN KK
PIN LL
PIN MM
COVER OF BACKSHELL REMOVED
NOTES:
1. ITEM 1 MANUFACTURED BY: KYCON INC. 1810 LITTLE ORCHARD ST. SAN JOSE, CA. 75125
ITEM 2 MANUFACTURED BY: 3M ELECTRONIC PRODUCTS DIVISION 6801 RIVER PLACE BLVD. AUSTIN, TX 78726
ITEM 3 MANUFACTURED BY: BELDEN PO BOX 1980 RICHMOND, IN 47375
ITEMS 4 AND 5 MANUFACTURED BY: POSITRONIC IND. INC. 423 NO. CAMPBELL ST. SPRINGFIELD, MO.
2. CABLE SHIELD BRAID TO BE CONNECTED TO BACKSHELL OF P1 AND P2 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 AND P2 CONNECTORS USING 24 AWG DRAIN WIRE.
3. ALL SOLDER JOINTS TO BE COVERED BY HEAT SHRINK.
4. LOCKING COMPOUND TO BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.
5. MARK WITH "7660-513" "005-0084 REV *" * = CURRENT REVISION OF THIS DRAWING. WRAP AROUND STYLE LABEL ACCEPTABLE.
6. BRAID SHIELD TO BE FOLDED BACK APPROX 1/2 INCH OVER CABLE JACKET AND CLAMPED TO JACKET BY OUTER STRAIN RELIEF. SHIELD BRAID TO BE WIRED TO PIN A OF P2 CONNECTOR USING 24 AWG DRAIN WIRE.
7. MOUNT JACK SOCKET ABOVE PIN "A" AND JACKSCREW BELOW PIN "MM".

Figure E-8 Cable Adapter V.35/V.36 DTE (P/N 7660-513)

Table E-7 Wire List for Cable Adapter V.35/V.36 DTE (P/N 7660-513)

WIRE LIST			
FROM	TO	SIGNAL	COLOR
P1-1	P2-A	CABLE SHLD BRAID	DRAIN WIRE
P1-2	P2-P	TXD(A)	WHITE/ BLUE STRIPE
P1-3	P2-S	TXD(B)	BLUE/ WHITE STRIPE
P1-4	P2-V	RXC(A)	WHITE/ ORN STRIPE
P1-5	P2-X	RXC(B)	ORN/ WHITE STRIPE
P1-6	P2-H	DTR	WHITE/ GRN STRIPE
P1-7	P2-	NC	GRN/ WHITE STRIPE
P1-8	P2-D	CTS	WHITE/ BRN STRIPE
P1-9	P2-	NC	BRN/ WHITE STRIPE
P1-10	P2-U	SCTE(A)	WHITE/ GREY STRIPE
P1-11	P2-W	SCTE(B)	GREY/ WHITE STRIPE
P1-12	P2-R	RXD(A)	RED/ BLUE STRIPE
P1-13	P2-T	RXD(B)	BLUE/ RED STRIPE
P1-14	P2-F	DCD	RED/ ORN STRIPE
P1-15	P2-	NC	ORN/ RED STRIPE
P1-16	P2-J	LL	RED/ GRN STRIPE
P1-17	P2-	NC	GRN/ RED STRIPE
P1-18	P2-B	SIGNAL GND	RED/ BRN STRIPE
P1-19	P2-	NC	BRN/ RED STRIPE
P1-20	P2-Y	TXC(A)	RED/ GREY STRIPE
P1-21	P2-AA	TXC(B)	GREY/ RED STRIPE
P1-22	P2-C	RTS	BLACK/ BLUE STRIPE
P1-23	P2-	NC	BLUE BLACK STRIPE
P1-24	P2-E	DSR	BLACK/ ORN STRIPE
P1-25	P2-	NC	ORN/ BLACK STRIPE
P1-26	P2-	NC	BLACK/ GRN STRIPE





VIEW A

COVER OF BACKSHELL REMOVED

NOTES:

1. ITEMS 1 AND 5 MANUFACTURED BY: KYCON INC.
   1810 LITTLE ORCHARD ST.
   SAN JOSE, CA. 75125
   ITEM 4 MANUFACTURED BY: ADAM TECH
   909 RAHWAY AVE.
   UNION, NJ 07083
   ITEM 3 MANUFACTURED BY: BELDEN
   PO BOX 1980
   RICHMOND, IN 47375
   ITEM 2 MANUFACTURED BY: 3M ELECTRONIC PRODUCTS DIVISION
   6801 RIVER PLACE BLVD.
   AUSTIN, TX 78726

2. CABLE SHIELD BRAID TO BE CONNECTED TO BACKSHELL OF P1 AND P2 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 AND P2 CONNECTORS USING 24 AWG DRAIN WIRE.

3. ALL SOLDER JOINTS TO BE COVERED BY HEAT SHRINK.

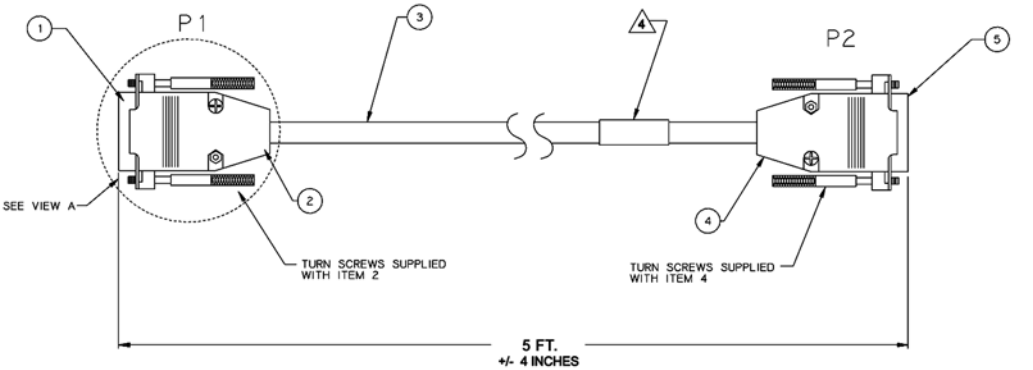
4. LOCKING COMPOUND TO BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.

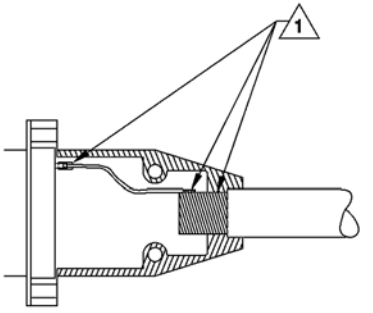
5. MARK WITH "7660-514"
   "005-0073 REV *"
   * = CURRENT REVISION OF THIS DRAWING.
   WRAP AROUND STYLE LABEL ACCEPTABLE.

Figure E-9 Cable Adapter RS-232 DB25 DTE (P/N 7660-514)

Table E-8 Wire List for Cable Adapter RS-232 DB25 DTE (P/N 7660-514)

WIRE LIST			
FROM	TO	SIGNAL	COLOR
P1-1	P2-1	CABLE SHLD BRAID	DRAIN WIRE
P1-2	P2-2	TXD	WHITE/ BLUE STRIPE
P1-3	P2-	NC	BLUE/ WHITE STRIPE
P1-4	P2-17	RXC	WHITE/ ORN STRIPE
P1-5	P2-	NC	ORN/ WHITE STRIPE
P1-6	P2-20	DTR	WHITE/ GRN STRIPE
P1-7	P2-	NC	GRN/ WHITE STRIPE
P1-8	P2-5	CTS	WHITE/ BRN STRIPE
P1-9	P2-	NC	BRN/ WHITE STRIPE
P1-10	P2-24	SCTE	WHITE/ GREY STRIPE
P1-11	P2-	NC	GREY/ WHITE STRIPE
P1-12	P2-3	RXD	RED/ BLUE STRIPE
P1-13	P2-	NC	BLUE/ RED STRIPE
P1-14	P2-8	DCD	RED/ ORN STRIPE
P1-15	P2-	NC	ORN/ RED STRIPE
P1-16	P2-18	LL	RED/ GRN STRIPE
P1-17	P2-22	RI	GRN/ RED STRIPE
P1-18	P2-7	SIGNAL GND	RED/ BRN STRIPE
P1-19	P2-	NC	BRN/ RED STRIPE
P1-20	P2-15	TXC	RED/ GREY STRIPE
P1-21	P2-	NC	GREY/ RED STRIPE
P1-22	P2-4	RTS	BLACK/ BLUE STRIPE
P1-23	P2-	NC	BLUE BLACK STRIPE
P1-24	P2-6	DSR	BLACK/ ORN STRIPE
P1-25	P2-	NC	ORN/ BLACK STRIPE
P1-26	P2-	NC	BLACK/ GRN STRIPE





NOTES:

1. CABLE SHIELD BRAID SHALL BE CONNECTED TO BACKSHELL OF P1 AND P2 WHEN MATING HALVES OF BACKSHELL ARE ASSEMBLED. SHIELD BRAID IS WIRED TO PIN 1 OF P1 AND P2 CONNECTORS USING 24 AWG DRAIN WIRE.
2. ALL SOLDER JOINTS SHALL BE COVERED BY HEAT SHRINK.
3. LOCKING COMPOIND SHALL BE APPLIED TO HARDWARE THAT SECURES THE BACKSHELL HALVES.
4. MARK WITH:
   PART NUMBER: 005-XXXX-XXXX
   DATE CODE: WW/YY (WW = WEEK, YY = YEAR)
   WRAP AROUND STYLE LABEL ACCEPTABLE.
5. CABLE LENGTHS MAY IMPACT BANDWIDTH. DO NOT EXCEED 10 FEET FOR UP TO 16 Mbps OR 25 FEET FOR UP TO 8Mbps.

PART NUMBER	CONNECTOR
7660-520	MALE-MALE
7660-521	MALE-FEMALE
7660-522	FEMALE-FEMALE

Figure E-10 Cable Adapter, CBI, RS-530 DB25 (P/N 7660-520 to 522)

Table E-9 Wire List for Cable Adapter, CBI, RS-530 DB25 (P/N 7660-520 to 522)

WIRE LIST			
FROM (P1)	TO (P2)	SIGNAL	COLOR
1	1	SHIELD	BLACK & DRAIN
18	18	LOCAL LOOPBACK (RESYNC+)	WHITE
2	2	TXD A	RED
14	14	TXD B	GREEN
3	3	RXD	BROWN
16	16	RXD	BLUE
4	4	RTS	ORANGE
19	19	RTS	YELLOW
5	5	CTS	VIOLET
13	13	SECONDARY CTS	GRAY
6	6	DSR	TAN
22	22	DSR	PINK
7	7	SIGNAL GROUND	WHITE/BLUE
21	21	REMOTE LOOPBACK (RESYNC-)	BLUE/WHITE
8	8	RSLD	WHITE/BROWN
10	10	RSLD	BROWN/WHITE
9	9	SERIAL CLOCK RX	WHITE/ORANGE
17	17	SERIAL CLOCK RX	ORANGE/WHITE
11	11	SERIAL CLOCK TX E	WHITE/GREEN
24	24	SERIAL CLOCK TX E	GREEN/WHITE
12	12	SERIAL CLOCK TX	WHITE/RED
15	15	SERIAL CLOCK TX	RED/WHITE
20	20	DTR	WHITE/BLACK
23	23	DTR	BLACK/WHITE
25	25	TEST MODE	GREEN/YELLOW

This cable may be used with either the DCE or DTE connectors on the CBI I/O module.

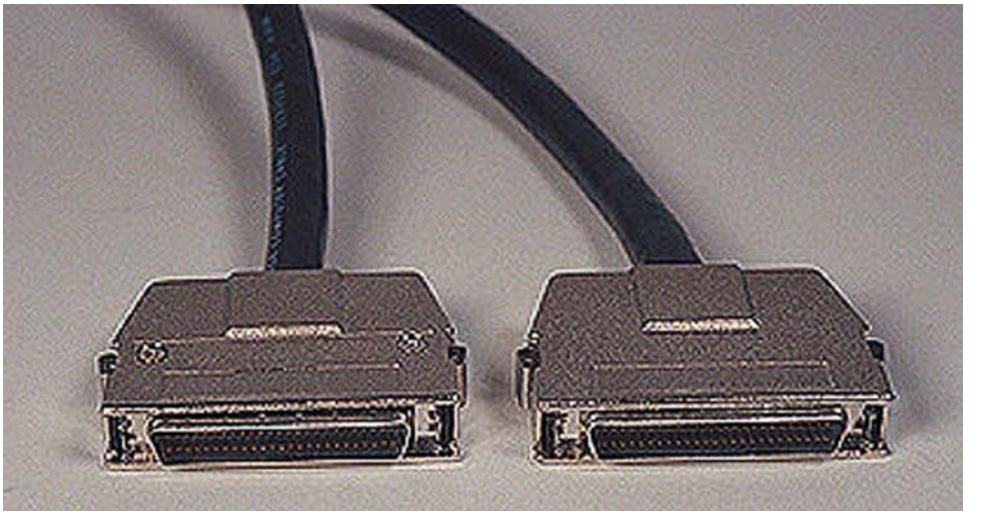


Figure E-11 Cable Adapter, HSSI-CBI (P/N 7660-523)

Description: Standard SCSI-2 shielded cable with straight-through wires and HDB50 plug connectors. HSSI-CBI cables mate with the socket HDB50 DTE or DCE connector on the IOM.

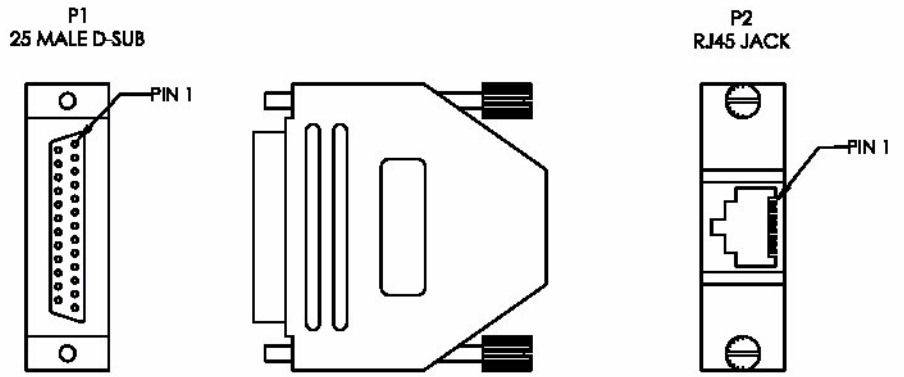


Figure E-12 Cable Adapter, HBR (P/N 7660-524)

DB25 RS-530 to RJ45 RS-422 Converter

Table E-10 Wire List for Cable Adapter RJ45 to DB25 DCE (P/N 7660-524)

P1	SIGNAL NAME	P2
1	SHIELD	NOT USED
2	Tx DATA A	6
3	RECEIVED DATA A	2
4	REQUEST TO SEND	NOT USED
5	CLEAR TO SEND	NOT USED
6	DCE READY	NOT USED
7	SIGNAL GROUND	NOT USED
8	RECEIVED LINE SIGNAL DETECTOR A	NOT USED
9	RECEIVER SIGNAL ELEMENT TIMING B	3
10	RECEIVED LINE SIGNAL DETECTOR B	NOT USED
11	TRANSMITTER SIGNAL SIGNAL ELEMENT TIMING DTE B	7
12	TRANSMITTER SIGNAL SIGNAL ELEMENT TIMING DCE B	3
13	CLEAR TO SEND	NOT USED
14	TRANSMITTTED DATA B	5
15	TRANSMITTER SIGNAL ELEMENT TIMING DCE A	4
16	RECEIVED DATA B	1
17	RECEIVER SIGNAL ELEMENT TIMING DCE A	4
18	LOCAL LOOPBACK	NOT USED
19	REQUEST TO SEND TO B	NOT USED
20	DTE READY A	NOT USED
21	REMOTE LOPBACK	NOT USED
22	DCE READY B	NOT USED
23	DTE READY B	NOT USED
24	TRASMITTER SIGNAL ELEMENT TIMING DTE A	8
25	TEST MODE	NOT USED

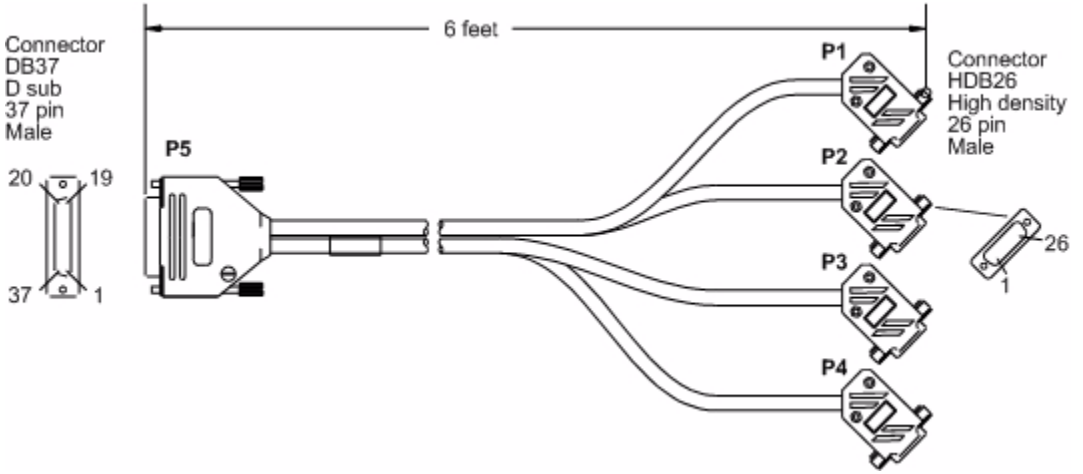


Figure E-13 Cable Adapter, Mux, 4xDB26 to DB37 DTE (P/N 7660-525)

Table E-11 Wire List for Cable Adapter, Mux, 4xDB26 to DB37 DTE (P/N 7660-525)

From	Signal	To	Signal
P1-1	Cable Shield	P5-19/37	Drain Wire
P1-2	TXD (A)	P5-6	Clock 2 out (A)
P1-3	TXD (B)	P5-24	Clock 2 out (B)
P1-4	RXC (A)	P5-2	Clock IN (A)
P1-5	RXC (B)	P5-20	Clock IN (B)
P1-10	SCTE (A)	P5-4	Clock 1 out (A)
P1-11	SCTE (B)	P5-22	Clock 1 out (B)
P1-12	RXD (A)	P5-3	Data 1 in (A)
P1-13	RXD (B)	P5-21	Data 1 in (B)
P1-20	TXC (A)	P5-5	Data 2 in (A)
P1-21	TXC (B)	P5-23	Data 2 in (B)
P2-1	Cable Shield	P5-19/37	Drain Wire

From	Signal	To	Signal
P2-2	TXD (A)	P5-10	Clock 4 out (A)
P2-3	TXD (B)	P5-28	Clock 4 out (B)
P2-10	SCTE (A)	P5-8	Clock 3 out (A)
P2-11	SCTE (B)	P5-26	Clock 3 out (B)
P2-12	RXD (A)	P5-7	Data 3 in (A)
P2-13	RXD (B)	P5-25	Data 3 in (B)
P2-20	TXC (A)	P5-9	Data 4 in (A)
P2-21	TXC (B)	P5-27	Data 4 in (B)
P3-1	Cable Shield	P5-19/37	Drain Wire
P3-2	TXD (A)	P5-14	Clock 6 out (A)
P3-3	TXD (B)	P5-32	Clock 6 out (B)
P3-10	SCTE (A)	P5-12	Clock 5 out (A)
P3-11	SCTE (B)	P5-30	Clock 5 out (B)
P3-12	RXD (A)	P5-11	Data 5 in (A)
P3-13	RXD (B)	P5-29	Data 5 in (B)
P3-20	TXC (A)	P5-13	Data 6 in (A)
P3-21	TXC (B)	P5-31	Data 6 in (B)
P4-1	Cable Shield	P5-19/37	Drain Wire
P4-2	TXD (A)	P5-18	Clock 8 out (A)
P4-3	TXD (B)	P5-36	Clock 8 out (B)
P4-10	SCTE (A)	P5-16	Clock 7 out (A)
P4-11	SCTE (B)	P5-34	Clock 7 out (B)
P4-12	RXD (A)	P5-15	Data 7 in (A)
P4-13	RXD (B)	P5-33	Data 7 in (B)
P4-20	TXC (A)	P5-17	Data 8 in (A)
P4-21	TXC (B)	P5-35	Data 8 in (B)

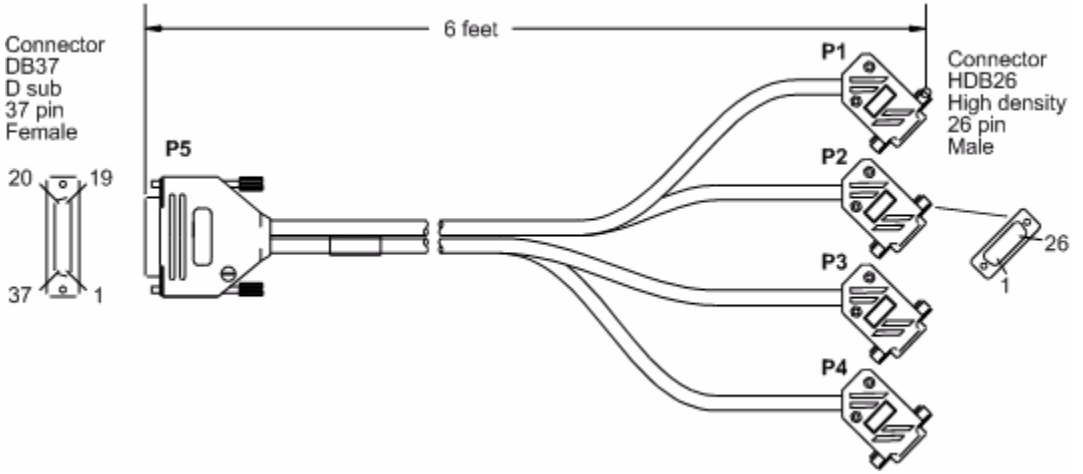


Figure E-14 Cable Adapter, Demux, 4xDB26 to DB37 DCE (P/N 7660-526)

Table E-12. Wire List for Cable Adapter, Demux, 4xDB26 to DB37 DCE (P/N 7660-526)

From	Signal	To	Signal
P1-1	Cable Shield	P5-19/37	Cable Shield
P1-22	CTS (A)	P5-6	Clock 2 out (A)
P1-23	CTS (B)	P5-24	Clock 2 out (B)
P1-4	SCTE (A)	P5-2	REF Clock IN (A)
P1-5	SCTE (B)	P5-20	REF Clock IN (B)
P1-20	TXC (A)	P5-4	Clock 1 out (A)
P1-21	TXC (B)	P5-22	Clock 1 out (B)
P1-10	RXC (A)	P5-3	Data 1 out (A)
P1-11	RXC (B)	P5-21	Data 1 out (B)
P1-2	RXD (A)	P5-5	Data 2 out (A)
P1-3	RXD (B)	P5-23	Data 2 out (B)
P2-1	Cable Shield	P5-19/37	Cable Shield

From	Signal	To	Signal
P2-22	CTS (A)	P5-10	Clock 4 out (A)
P2-23	CTS (B)	P5-28	Clock 4 out (B)
P2-20	TXC (A)	P5-8	Clock 3 out (A)
P2-21	TXC (B)	P5-26	Clock 3 out (B)
P2-10	RXC (A)	P5-7	Data 3 out (A)
P2-11	RXC (B)	P5-25	Data 3 out B)
P2-2	RXD (A)	P5-9	Data 4 out (A)
P2-3	RXD (B)	P5-27	Data 4 out (B)
P3-1	Cable Shield	P5-19/37	Cable Shield
P3-22	CTS (A)	P5-14	Clock 6 out (A)
P3-23	CTS (B)	P5-32	Clock 6 out (B)
P3-20	TXC (A)	P5-12	Clock 5 out (A)
P3-21	TXC (B)	P5-30	Clock 5 out (B)
P3-10	RXC (A)	P5-11	Data 5 out (A)
P3-11	RXC (B)	P5-29	Data 5 out (B)
P3-2	RXD (A)	P5-13	Data 6 out (A)
P3-3	RXD (B)	P5-31	Data 6 out (B)
P4-1	Cable Shield	P5-19/37	Cable Shield
P4-22	CTS (A)	P5-18	Clock 8 out (A)
P4-23	CTS (B)	P5-36	Clock 8 out (B)
P4-20	TXC (A)	P5-16	Clock 7 out (A)
P4-21	TXC (B)	P5-34	Clock 7 out (B)
P4-10	RXC (A)	P5-15	Data 7 out (A)
P4-11	RXC (B)	P5-33	Data 7 out (B)
P4-2	RXD (A)	P5-17	Data 8 out (A)
P4-3	RXD (B)	P5-35	Data 8 out (B)

Table E-13. Wire List for High Speed SAM (HSAM) Connectors (Combo-D female)

Signal Name	Signal Type	Data Rates (bps)	Connector	Input/ Output
In I Data	LVDS	200K,137M,47M,10M	J1-A5	Input
In I DataNot	LVDS	200K,137M,47M,10M	J1-A6	Input
In Q Data	LVDS	137M	J1-A3	Input
In Q DataNot	LVDS	137M	J1-A4	Input
In I/Q Clock	LVDS	200K,137M,47M,10M	J1-A1	Input
In I/Q ClockNot	LVDS	200K,137M,47M,10M	J1-A2	Input
Out I Data	LVDS	200K,137M,47M,10M	J2-A5	Output
Out I DataNot	LVDS	200K,137M,47M,10M	J2-A6	Output
Out Q Data	LVDS	137M	J2-A3	Output
Out Q DataNot	LVDS	137M	J2-A4	Output
Out I/Q Clock	LVDS	200K,137M,47M,10M	J2-A1	Output
Out I/Q ClockNot	LVDS	200K,137M,47M,10M	J2-A2	Output

APPENDIX F

Chassis Differences

Broadmore Chassis Differences

This user manual covers the 7665-17B and 7665C chassis. The 7665C chassis is the newest chassis in the Broadmore series and has the most functionality. There is also a 17A chassis.

Hardware Differences

Major differences between chassis include mid-plane wiring, alarm/power modules, and fan trays as shown in the following table.

Chassis	Major Assemblies	Part Nos.	Comments
7665-17A	Alarm & Power Module	7660-021	No I/O module
	Fan Tray	7660-022	2-wire connection
7665-17B	Alarm & Power Module	7660-023	Requires I/O module
	Alarm & Power Module I/O	7660-025	
	Fan Tray	7660-024	4-wire connection
7665-17C	Alarm & Power Module	7660-023	Requires I/O module
	Alarm & Power Module I/O	7660-025	
	Fan Tray	7660-024	4-wire connection

The 17A and 17C chassis used in the Broadmore 1700 provide the same functionality and support the same NIM, SAM, and CPU modules. The 17C chassis also uses the improved Alarm & Power Module and Fan Tray assemblies as the 17B chassis.

The 17B chassis used in the Broadmore 1750 provides 1:4 SAM redundancy and has been NEBS tested with the OC-12 NIM (7660-114 or 7660-113) and the Un-Structured DS3 SAMs (7660-034 or 7660-672).

Software Differences

Software release v3.4.1 was introduced to support the 7665-17C chassis used in the Broadmore 1700. However, software release 3.4.1 and higher can be used on all Broadmore chassis.

The CPU module can be moved between different chassis but it must be configured to recognize the chassis in which it is installed. The chassis version is specified by the presence of a file pointer in the \CAM directory. For example, a 7665-17C chassis is identified by the presence of a file named CAM7665.17C in the \CAM directory. Similarly, the 17A and 17B chassis are identified by the files CAM7665.17A and CAM7665.17B.

To change the file pointer in the CPU, perform the following steps:

1. Log in as a Superuser. The default login is: ***SYSADMIN*** and password: ***INITIAL***
2. From the operating system prompt, enter ***cd cam***
3. Type ***dir*** and look for one of the following files: ***CAM7665.17A***, ***CAM7665.17B***, or ***CAM7665.17C***
4. If the file name does not correspond to the chassis in which the CPU is installed, the file must be renamed. For example, to change the chassis version from 17A to 17C, enter the following command: ***mv CAM7665.17A CAM7665.17C***
5. If no file can be found, it can be created using the touch command. For example, to create a file for the 17C chassis, enter the command: ***touch CAM7665.17C***

A CPU shipped with a Broadmore 500 will have the file CAM7665.5, which tells the CPU to recongnize only the first 5 module slots. This file can also be changed as described above if the CPU is installed in a different chassis.

APPENDIX G

IPv6 Support

In this Appendix:

- Overview ... *G-2*
- Configuring IPv6 Addresses for Network Interfaces ... *G-2*
- Pinging over IPv6 ... *G-4*
- Testing route6 Application ... *G-5*

Overview

This Appendix provides methods for demonstrating IPv6 functionality on Broadmore.

You can find more test methods in the Product Application Notice: Broadmore IPv6 (PAN-07-0001), which is available on the Customer Support website.

Configuring IPv6 Addresses for Network Interfaces

When the system boots up, an IPv6 address of link-local scope is configured on all the network interfaces. This section provides steps to configure a network interface.

Adding an IPv6 Address

To set the IPv6 address on the Ethernet interface, from the Broadmore shell issue the command **`ifconf6`** with the argument **`-a`**. For example:

```
Broadmore-> ifconf6 1 -a 3ffe:0:0:13::5
```

The above command sets the IPv6 address **`3ffe:0:0:13::5`** on the Ethernet interface **`1`**.

Displaying an Address

Check the interface configuration.

```
Broadmore->ifconf6 1

     Interface Name            : 1
     Index number              : 2
     Type                      : ETHERNET
     Inet6 Address             : fe80::2e0:97ff:fe6b:7ffe
     Prefix Mask               : ffff:ffff:ffff:ffff::
     Flags                     : 0 <>
     Inet6 Address             : 3ffe:0:0:13::5
     Prefix Mask               : ffff:ffff:ffff:ffff::
     Flags                     : 0 <>
```

```
Physical Address          : 00:e0:97:6b:7f:fe
MTU  Size                 : 1500
Packets received          : 23
Packets sent              : 2
Mcast Packets received    : 0
Mcast Packets sent        : 0
Total Bytes received      : 1480
Total Bytes sent          : 64
Input errors              : 0
Output errors             : 0
Packets dropped on input  : 0
Flags  : 0xa8e1 <BROADCAST,RUNNING,UP,MULTICAST>
```

Deleting an IPv6 Address

To delete the IPv6 address from an Ethernet interface, from the Broadmore shell issue the command **`ifconf6`** with the argument **`-d`**. For example:

```
Broadmore->ifconf6 1 -d 3ffe:0:0:13::5
```

Pinging over IPv6

To ping a remote host over IPv6, issue the command **ping6**. The remote host must also be configured with an IPv6 address of global scope and must be in the same subnet as the Broadmore.

Pinging an IPv6 Host

The following example shows a **ping6** operation to a host with an IPv6 address **3ffe:0:0:13::4**. The subnet in this case is 3ffe:0:0:13::x.

```
Broadmore->ping6 -c 1 3ffe:0:0:13::4
PING6(56=40+8+8 bytes) 3ffe:0:0:13::4
16 bytes from 3ffe:0:0:13::4, icmp_seq=0 hlim=64
--- ping6 statistics ---
1 packets transmitted, 1 packets received, 0% packet loss
```

Ping the Loopback Interface Address

```
Broadmore->ping6 -c 10 -s 1000 ::1

PING6(1048=40+8+1000 bytes) ::1
1008 bytes from ::1, icmp_seq=0 hlim=64
1008 bytes from ::1, icmp_seq=1 hlim=64
1008 bytes from ::1, icmp_seq=2 hlim=64
1008 bytes from ::1, icmp_seq=3 hlim=64
1008 bytes from ::1, icmp_seq=4 hlim=64
1008 bytes from ::1, icmp_seq=5 hlim=64
1008 bytes from ::1, icmp_seq=6 hlim=64
1008 bytes from ::1, icmp_seq=7 hlim=64
1008 bytes from ::1, icmp_seq=8 hlim=64
1008 bytes from ::1, icmp_seq=9 hlim=64

--- ping6 statistics ---
10 packets transmitted, 10 packets received, 0% packet loss
```

Testing route6 Application

This section provides examples for adding, deleting and displaying routes.

Adding an IPv6 Route

To add an IPv6 route, issue the Broadmore command **`route6.`** The following example adds a default route on the Broadmore:

```
Broadmore->route6 add default gateway 3ffe:0:0:13::4
```

Adding a Host Route

The following example adds a host route on the Broadmore:

```
Broadmore->route6 add host dst 3ffe:0:0:14::41 gateway
3ffe:0:0:13::4
```

Adding a Network Route

The following example adds a network route on the Broadmore:

```
Broadmore->route6 add net dst 3ffe:0:0:14:: gateway
3ffe:0:0:13::4prefixlen 64
```

Showing all IPv6 routes configured in the Broadmore

To display all the IPv6 routes configured in the system, issue the command **route6** with option **-a**. For example:

```
Broadmore->route6 -a

Destination        Gateway            Flags    Refcnt   Use      Interface

-------------------------------------------------------------------------------
-------------------------------------
::
        3ffe:0:0:13::4
                                     10803    0        0        1
-------------------------------------
::1
        ::1
                                     200005   0        0        6
-------------------------------------
3ffe:0:0:13::
        3ffe:0:0:13::5
                                     101      1        0        1
-------------------------------------
fe80:1::
        fe80:1::1
                                     10001    0        0        6
-------------------------------------
fe80:2::
        fe80:2::2e0:97ff:fe6b:7ffe
                                     101      0        0        1
-------------------------------------
ff01::
        ::1
                                     800001   0        0        6
-------------------------------------
ff02:1::
        ::1
                                     800101   0        0        6
-------------------------------------
ff02:2::
        fe80:2::2e0:97ff:fe6b:7ffe
                                     800101   0        0        1
-------------------------------------
-------------------------------------------------------------------------------
```

Deleting the Default Route

The following example deletes the default route on the Broadmore:

```
Broadmore->route6 delete default gateway 3ffe:0:0:13::4
```

Deleting a Host Route

The following example deletes a host route on the Broadmore:

```
Broadmore->route6 delete host dst 3ffe:0:0:14::41 gateway
3ffe:0:0:13::4
```

Deleting a Network Route

The following example deletes a network route on the Broadmore:

```
Broadmore->route6 delete net dst 3ffe:0:0:14:: gateway
3ffe:0:0:13::4 prefixlen 64
```

APPENDIX H

Broadmore Command List

In this Appendix:

- Commands Available at the Command Prompt
- Commands Available at the CLI Prompt

Commands Available at the Command Prompt

The commands listed below are available immediately after you log into the Broadmore.

Type ‘help’ at the command prompt to view the list of commands. You can also type ‘help *command*’ to obtain help on any of the listed commands.

arp
cammi
cat
cd
chargen6tcp
chargen6udp
cli
cmp
comp
copy
cp
daytime6tcp
daytime6udp
del
dir
du
echo
fipsmode
head
help
icmp6StatsShow
ifconf
ifconf6
in6AddrShow
ip6StatsShow
ls
mbStatsShow
md
netStackUdpStatsShow
netstat
ping
ping6
prefixListShow
pwd
rd
resetSecurID
resetSecurIDIp
rm
rmdir
route
route6
savert
scp
selftest
setbaud
setenv
settimeout
setwrite
showconfig
sigmem
snmpinit
sntpGet
sntpShow
sshdSessionShow
sshdShow
tail

mem	tc6
mkdir	timeoutStatsShow
move	touch
mv	ts6
nd6Cache	uc6
netStackTcpPcbShow	us6
netStackUdpPcbShow	zeroize

See *"Shell Commands (Non-FIPS Mode)" on page 10-19* and *"Shell Commands (FIPS Mode)" on page 11-34* for information about the availability of commands based on user access level.

Commands Available at the CLI Prompt

The commands listed below are available at the cli> prompt.

Type ? at the command prompt to view the list of commands.

General Commands

up cls quit ? (help)

Level Commands

sys maintain about

GLOSSARY

Acronyms and Abbreviations

AAL	ATM Adaptation Layer
AIS	Alarm Indication Signal
ANSI	American National Standards Institute
APS	Automatic Protection Switching
ARP	Address Resolution Protocol
AS	Available Seconds
ATM	Asynchronous Transfer Mode
AWG	American Wire Gage
BITS	Building Integrated Timing System
bps	Bits Per Second

C	Celsius
CAMMI	CAM Management Interface
CAS	Channel Associated Signaling
CBES	C-Bit Errored Seconds (network)
CBI	Cell Bearing Interface
CBR	Constant Bit Rate
CCV	C-Bit Coding Violation
CDVT	Cell Delay Variation Tolerance
CE	Circuit Emulation
CES	C-Bit Errored Seconds
CES	Circuit Emulation SAM
CIP	Classic IP
CLI	Command Line Interface
CPU	Central Processor Unit
CSES	C-Bit Severely Errored Seconds
DSP	Digital Signal Processor
DS-n	Digital Signal level *n*
EIA	Electronic Industries Alliance

ELAN	Emulated Local Area Network
ESD	Electrostatic Discharge
FCC	Federal Communications Commission
FEAC	Far End Alarm Control
FIPS	Federal Information Processing Standard
FTP	File Transfer Protocol
GUI	Graphical User Interface
HBR	High Bit Rate
HEC	Header Error Control
IOM	Input Output Module
IP	Internet Protocol
IR	Intermediate Reach
kbps	Kilobits Per Second
LAN	Local Area Network
LANE	LAN Emulation
LCV	Line Coding Violation
LEC	LAN Emulation Client
LECS	LANE Emulation Configuration Server

LED	Light-Emitting Diode
LES	Line Errored Seconds
LES	LANE Emulation Server
LIS	Logical IP Subnetwork
LIU	Line Interface Unit
LOCD	Loss of ATM Cell Delineation
LOF	Loss of Frame
LOP	Loss of Pointer
LOS	Loss of Signal
MAC	Media Access Control
MBR	Multi-bit-rate
Mbps	Megabits per second
MIB	Management Information Base
NEBS	Network Equipment Building Standards
NIM	Network Interface Module
OC-n	Optical Carrier level *n*
OSC	Optical Service Conversion platform
PC	Personal Computer

PCB	Printed Circuit Board
PCM	Pulse Coded Modulation
PCV	P-Bit Coding Violation
PES	P-Bit Errored Second
PLCP	Physical Layer Convergence Protocol
PLOA	Protocol Layer Over ATM
POST	Power On Self Test
PSES	P-Bit Severely Errored Seconds
PWR	Power
PVC	Permanent Virtual Circuit
RIP	Routing Indicator Protocol
RDI	Remote Defect Indication
RFC	Request For Comment (the term used to track numbered proposals for ATM use)
RMA	Return Material Authorization
RX	Receive
SAM	Service Access Module
SAR	Segmentation and Reassembly
SEFS	Severely Errored Framing Seconds

SEQ	Sequence Errors
SG	Signal Ground
SNMP	Simple Network Management Protocol
SONET	Synchronous Optical Network
SVC	Switched Virtual Circuit
TBD	To Be Determined
TR	Transmitter
TX	Transmit
UAS	Unavailable Seconds
UNI	User Network Interface
VC	Virtual Channel
VCI	Virtual Channel Identifier
VGA	Video Graphics Adapter
VOM	Volt Ohm Meter
VP	Virtual Path
VPI	Virtual Path Identifier

Definitions

ATM Adaptation Layer (AAL)

Type 1 – AAL functions in support of constant bit rate, time-dependent traffic such as voice or video (default in CES-CBR configuration).

Type 2 – AAL undefined by International Standards bodies. Its anticipated use is for variable bit rate video transmission.

Type 3/4 – AAL functions in support of variable bit rate, delay-tolerant data traffic requiring some sequencing and/or error detection support. This was originally two AAL types, i.e., connection-oriented data traffic requiring minimal sequencing or error detection support.

Type 5 – AAL functions in support of variable bit rate, delay-tolerant connection-oriented data traffic requiring minimal sequencing or error detection support.

Broadband Bearer Capability

A bearer class field that is part of the initial address message.

BCOB

Broadband Connection Oriented Bearer – information in the SETUP message that indicates the type of service requested by the calling user.

BCOB-A

Bearer Class A – indicated by ATM end user in SETUP message for connection-oriented, constant bit rate service. The network may perform internetworking based on AAL information element (default in CES-CBR configuration).

BCOB-C

Bearer Class C – indicated by ATM end user in SETUP message for connection-oriented, variable bit rate service. The network may perform internetworking based on AAL information element.

BCOB-X

Bearer Class X – indicated by ATM end user in SETUP message for ATM transport service where AAL, traffic type, and timing requirements are transparent to the network.

Circuit Emulation

A connection over a virtual channel-based network that provides service to the end user that is indistinguishable from a real fixed-bandwidth circuit.

Constant Bit Rate

An ATM service category that supports a constant or guaranteed rate to transport services such as video or voice as well as circuit emulation requiring rigorous timing control and performance parameters.

Circuit Emulation Service (CES)

The ATM Forum circuit emulation service interoperability specification provides interoperability agreements for supporting constant bit rate (CBR) traffic over ATM networks that comply with the other ATM Forum interoperability agreements. Specifically, this specification supports emulation of existing TDM circuits over ATM networks.

Federal Information Processing Standard

Federal Information Processing Standard PUB 140-2 sets security standards for hardware and software in cryptographic equipment.

End-to-End Timing Requirement

Timing requirements that address the restrictions on the amount of time a cell can take in getting from source to destination.

Point-to-Point Connection

A connection with two endpoints (default in CES-CBR configuration).

Point-to-Multipoint Connection

A collection of associated ATM VC or VP links, with associated endpoint nodes, with the following properties:

Link 1 – One ATM link, called the Root Link, serves as the root in a simple tree topology. When the Root Node sends information, all the remaining nodes on the connection, called the Leaf Nodes, receive copies of the information.

Link 2 – Each of the Leaf Nodes on the connection can send information directly to the Root Node. The Root Node cannot distinguish which Leaf is sending information without additional (higher layer) information.

Link 3 – The Leaf Nodes cannot communicate directly to each other with this connection type.

Quality of Service (QoS)

A QoS class can have specified performance parameters (specified QoS class) or no specified performance parameters (unspecified QoS class). QoS classes are inherently associated with a connection. A specified QoS class specifies a set of performance parameters and the objective values for each performance parameter identified. Examples of performance parameters that could be in a QoS class are:

- Cell Loss Ratio
- Cell Transfer Delay
- Cell Delay Variation

A specified QoS class provides a quality of service to an ATM virtual connection (VCC or VPC) in terms of a subset of ATM performance parameters defined in Section 3 of Appendix A of the ATM Forum UNI v3.1 specifications. Initially, each service provider should define objective values for a subset of the ATM performance parameters for at least one of the Service Classes (Service Class A, B, C, or D) from ITU-T recommendation I.362 in a reference configuration that may depend on mileage and other factors.

QoS Classes are currently defined as:

Class 1 – Supports a QoS that will meet Service Class A performance requirements (circuit emulation, constant bit rate video). Should yield performance comparable to current digital private line performance.

Class 2 – Supports a QoS that will meet Service Class B performance requirements (variable bit rate audio and video). Intended for video and audio in teleconferencing and multi-media applications using packets.

Class 3– Supports a QoS that will meet Service Class C performance

	requirements (connection-oriented data transfer). Intended for interoperation of connection-oriented protocols, such as Frame Relay.
Header Error Control	A field (octet) in the ATM cell header used to detect multiple errors in the cell header or correct a single error in the cell header.
User Plane Connection	Signaling operates in the control plane (doing control-type functions such as setting up the connection). After signaling is finished, it drops out of the way and the established connection is used to move data. This moving of payload data is done in the user plane. Control and user planes are parts of the conceptual model of ATM.
Variable Bit Rate	An ATM Forum defined service category supporting variable bit rate data traffic with average and peak traffic parameters.

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